DISPLAYTRONIC

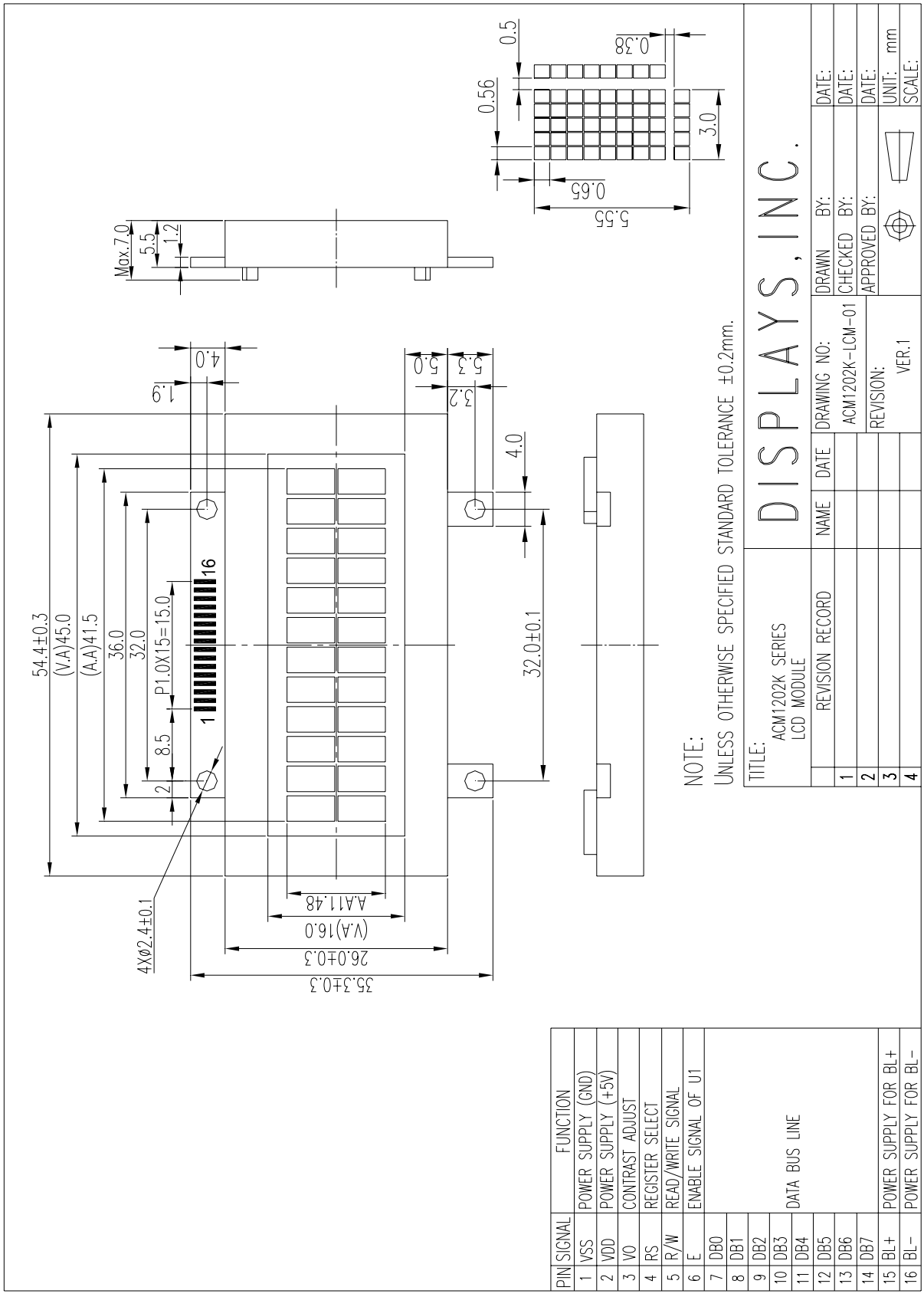
A DIVISION OF ZE XIAMEN CO., LTD.

SPECIFICATIONS FOR LIQUID CRYSTAL DISPLAY

PART NUMBER: **ACM1202K SERIES**

DATE: **MAR 16 2005**

1.0 MECHANICAL DIAGRAM



2.0 MECHANICAL SPECS

1. Overall Module Size	54.4mm(W) x 35.3mm(H) x max 7.0mm(D)
2. Dot Size	0.56X0.65 mm
3. Character Pitch	3.5mm
4. Duty	1/16
5. Controller IC	SPLC783A-01
6. LC Fluid Options	STN YELLOW-GREEN MODE
7. Polarizer Options	Transflective
8. Backlight Options	LED Yellow-Green
9. Temperature Range Options	Wide(-20ºC ~ 70ºC)

3.0 ABSOLUTE MAXIMUM RATINGS

Item	Symbol	Min	Typ	Max	Unit
Operating temperature (Wide temperature)	Top	-20	-	70	ºC
Storage temperature (Wide temperature)	Tst	-25	-	75	ºC
Input voltage	Vin	Vss		Vdd	V
Supply voltage for logic	Vdd- Vss	2.7	-	5.5	V
Supply voltage for LCD drive	Vdd- Vo	3.0		5.5	V

4.0 ELECTRICAL CHARACTERISTICS

Item	Symbol	Condition	Min	Typ	Max	Unit
Input voltage (high)	V_{ih}	H level	2.2	-	Vdd	V
Input voltage (low)	V_{il}	L level	0	-	0.6	V
Recommended LC Driving Voltage (Wide Temp)	V_{dd} -V_o	-20ºC	-	5.5		V
		0ºC	-	4.8	-	
		70ºC	3.7	4.1	-	
Power Supply Current	I_{dd}	Vdd=3.3V, fosc=270kHz	-		2.0	mA
Back Light	Vf	If=80mA		2.1	2.3	V

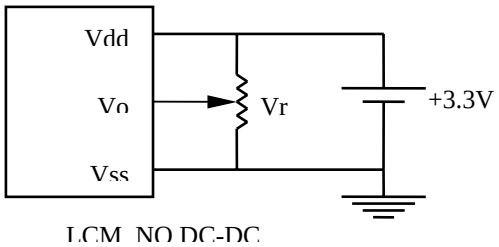
5.0 OPTICAL CHARACTERISTICS (Ta=25ºC, Vdd= 3.3V±0.25V, STN LC fluid)

Item	Symbol	Condition	Min	Typ	Max	Unit
Viewing angle (horizontal)	θ	Cr ≥ 2.0	-60	-	35	deg
Viewing angle (vertical)	ϕ	Cr ≥ 2.0	-40	-	40	deg
Contrast Ratio	Cr	ϕ=0°, θ=0°	-	6	-	
Response time (rise)	Tr	ϕ=0°, θ=0°	-	150	250	ms
Response time (fall)	Tf	ϕ=0°, θ=0°	-	150	250	ms

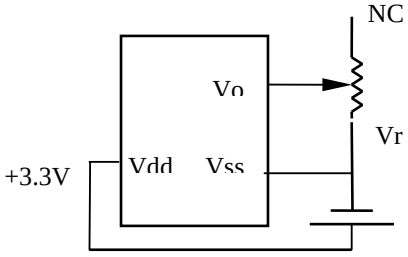
6.0 PIN ASSIGNMENT

Pin No.	Symbol	Function
1	Vss	Ground
2	Vdd	+3.3V
3	Vo	LCD contrast adjust
4	RS	Register select
5	R/W	Read / write
6	E	Enable
7	DB0	Data bit 0
8	DB1	Data bit 1
9	DB2	Data bit 2
10	DB3	Data bit 3
11	DB4	Data bit 4
12	DB5	Data bit 5
13	DB6	Data bit 6
14	DB7	Data bit 7
15	BL+	Power Supply for BL+
16	BL-	Power Supply for BL-

7.0 POWER SUPPLY



LCM NO DC-DC



LCM WITH DC-DC

Vr = 5KΩ ~ 10KΩ

8.0 TIMING CHARACTERISTICS

Item	Symbol	Test Condition	Min.	Typ.	Max.	Unit
Enable cycle time	t_C	Fig. a, Fig. b	500	-	-	ns
Enable pulse width	t_W	Fig. a, Fig. b	220	-	-	ns
Enable rise/fall time	t_R , t_F	Fig. a, Fig. b	-	-	25	ns
RS, R/W set up time	t_{SU}	Fig. a, Fig. b	40	-	-	ns
RS, R/W hold time	t_H	Fig. a, Fig. b	10	-	-	ns
Data delay time	t_D	Fig. b	-	-	120	ns
Data set up time	t_{DSU}	Fig. a	60	-	-	ns
Data hold time	t_{DH}	Fig. a, Fig. b	20	-	-	ns

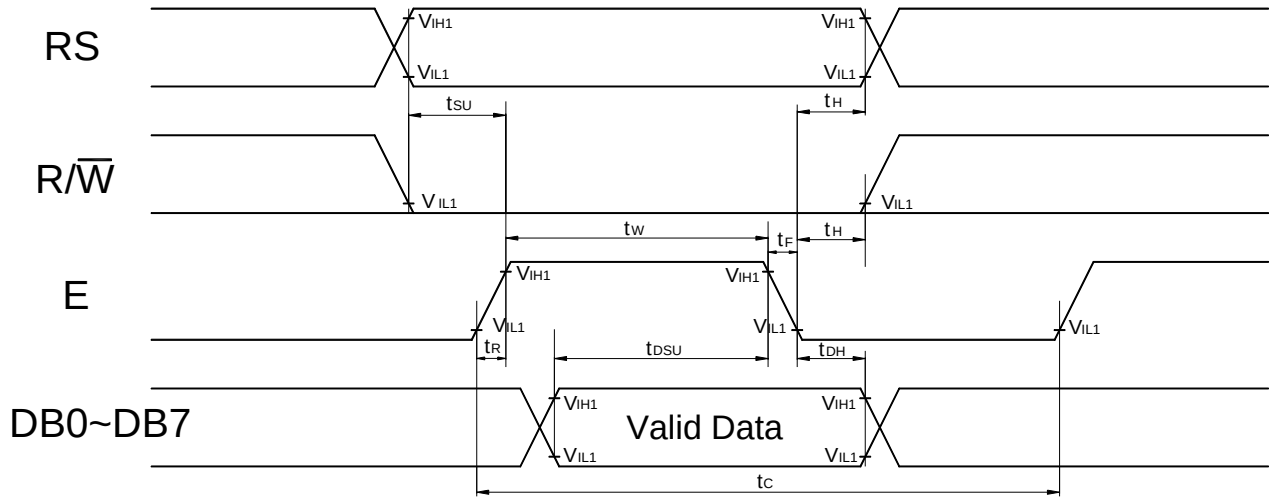


Fig. a Interface timing (data write)

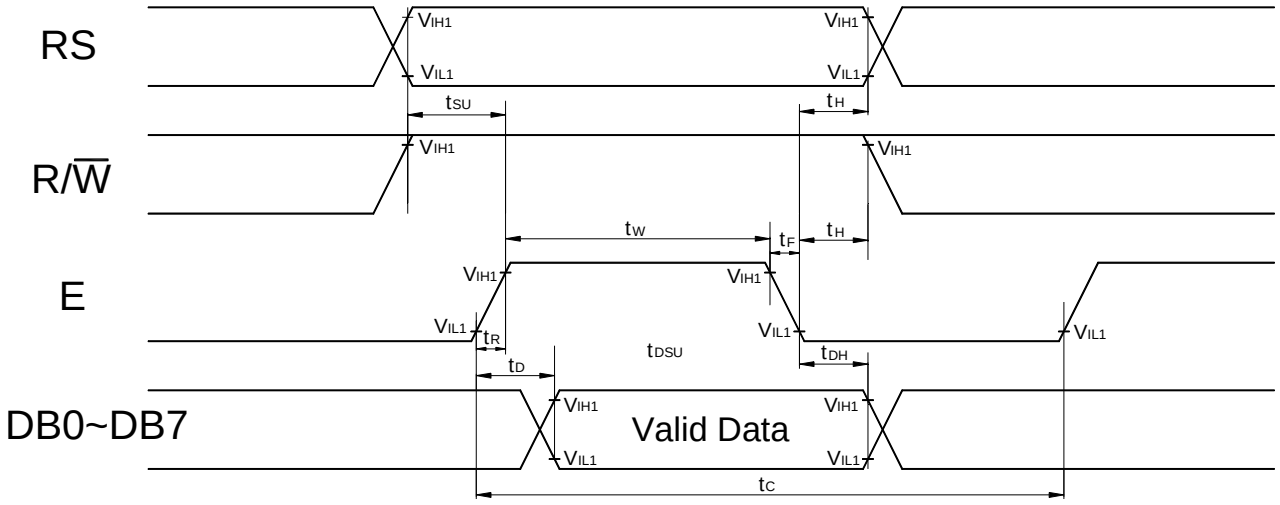


Fig. b Interface timing (data read)

10.0 THE RELATION OF POSITION AND DDRAM:

POSITION	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16
DDRAM (first)	00H	01H	02H	03H	04H	05H	06H	07H	08H	09H	0AH	0BH	0CH	0DH	0EH	0FH
DDRAM (second)	40H	41H	42H	43H	44H	45H	46H	47H	48H	49H	4AH	4BH	4CH	4DH	4EH	4FH

11.0 RELIABILITY TEST

Storage Condition	Content	Evaluations and Assessment*			
		Current Consumption	Oozing	Contrast	Other Appearances
Operation at high temperature and humidity	40ºC,90% RH,240hrs	Twice initial value or less	none	More than 80% of initial value	No abnormality
High temperature storage	60ºC, 240hrs	Twice initial value or less	none	More than 80% of initial value	No abnormality
Low temperature storage	-20ºC, 240hrs	Twice initial value or less		More than 80% of initial value	No abnormality

*Evaluations and assessment to be made two hours after returning to room temperature (25ºC±5ºC).
*The LCDs subjected to the test must not have dew condensation.

12.0 DISPLAY INSTRUCTION TABLE

COMMAND	RS	R/W	DB7	DB6	DB5	DB4	DB3	DB2	DB1	DB0	DESCRIPTION	Executing time fosc=250khz
Clear Display	0	0	0	0	0	0	0	0	0	1	Clears Display & Returns to Address 0.	1.64ms
Cursor at Home	0	0	0	0	0	0	0	0	1	x	Returns Cursor to Address 0. Also returns the display being shifted to the original position. DDRAM contents remain unchanged.	1.64ms
Entry Mode Set	0	0	0	0	0	0	0	1	I/D	S	I/D: Set Cursor Moving Direction I/D=1: Increment I/D=0: Decrement S: Specify Shift of Display S=1: The display is shifted S=0: The display is not shifted	40µs
Display ON/OFF Control	0	0	0	0	0	0	1	D	C	B	Display D=1: Display on D=0: Display off Cursor C=1: Cursor on C=0: Cursor off Brink B=1: Brink on B=0: Brink off	40µs
Cursor / Display Shift	0	0	0	0	0	1	S/C	R/L	x	x	Moves cursor or shifts the display w/o changing DD RAM contents S/C=0: Cursor Shift (RAM unchanged) S/C=1: Display Shift (RAM unchanged) R/L=1: Shift to the Right R/L=0: Shift to the Left	40µs
Function Set	0	0	0	0	1	DL	N	F	x	x	Sets data bus length (DL), # of display lines (N), and character fonts (F). DL=1: 8 bits F=0: 5x7 dots DL=0: 4 bits F=1: 5x10 dots N=0: 1 line display N=1: 2 lines display	40µs
Set CG RAM Address	0	0	0	1	Character Generator (CG) RAM Address						Sets CG RAM address. CG RAM data is sent and received after this instruction.	40µs
Set DD RAM Address	0	0	1	Display Data (DD) RAM Address / Cursor Address							Sets DD RAM address. DD Ram data is sent and received after this instruction.	40µs
Busy Flag / Address Read	0	1	BF	Address counter used for both DD & CG RAM address							Reads Busy Flag (BF) and address counter contents.	40µs
Write Data	1	0	Write Data								Writes data into DDRAM or CGRAM.	46µs
Read Data	1	1	Read Data								Reads data from DDRAM or CGRAM.	46µs

x: Don't Care

13.0 STANDARD CHARACTER PATTERNS

Note: The character generator RAM is the RAM with which the user can rewrite character patterns by program.