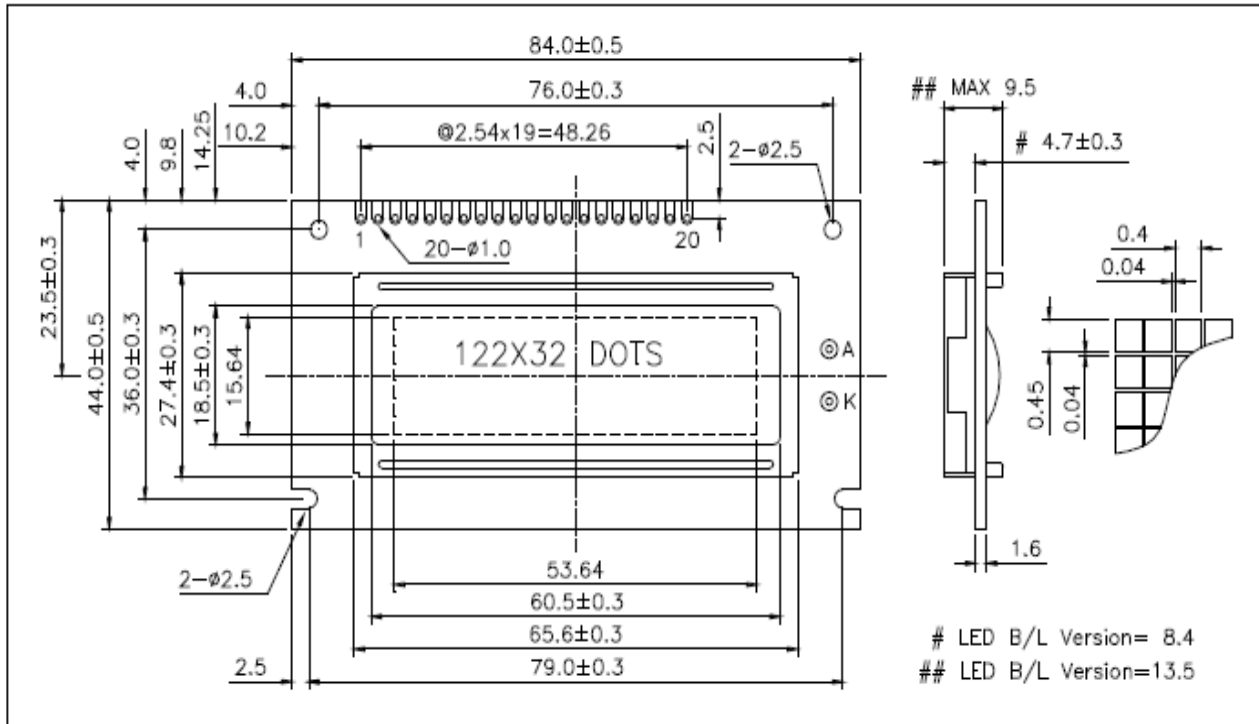
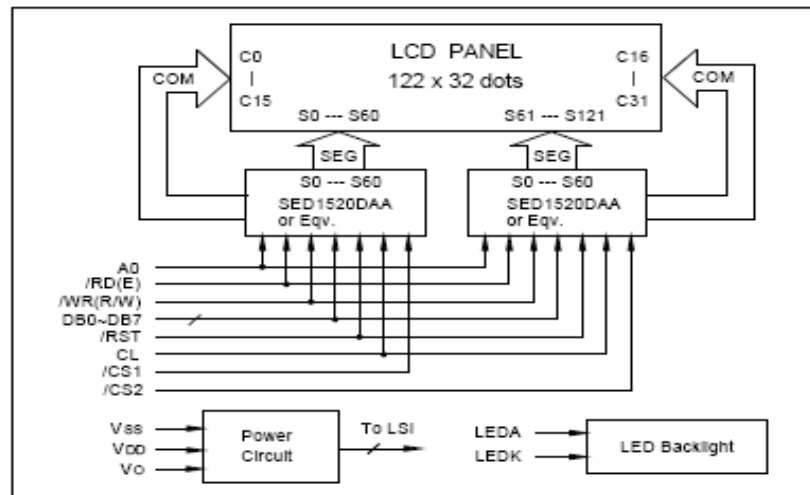


1.0 DIMENSIONAL DRAWING



2.0 BLOCK DIAGRAM



3.0 ELECTRICAL CHARACTERISTICS Ta=25 °C VDD=5.0V±0.25V

Item	Symbol	Test Condition	Standard Value			
			Min.	Typ.	Max.	Unit
Input High Voltage	V _{IH}	—	2.0	—	V _{DD}	V
Input Low Voltage	V _{IL}	—	- 0.3	—	0.8	V
Output High Voltage	V _{OH}	I _{OH} = - 3.0mA	2.4	—	V _{DD}	V
Output Low Voltage	V _{OL}	I _{OL} =3.0mA	0	—	0.4	V
Supply Current	I _{DD}	V _{DD} =5.0V	—	0.4	0.8	mA
LCD Driving Voltage	V _{DD} - V _O	Ta=25°C	—	6.5	—	V

4.0 ABSOLUTE MAXIMUM RATINGS

Item	Symbol	Standard Value		
		Min.	Max.	Unit
Supply Voltage (Logic)	VDD - Vss	-0.3	7.0	V
Supply Voltage (LCD)	VDD - Vo	-0.3	13.5	V
Input Voltage	Vi	-0.3	VDD+0.3	V
Operating Temp	Top.	-20	70	°C
Storage Temp	Tst.	-20	80	°C

5.0 PIN ASSIGNMENT

PIN NO.	NAME	RECOMMEND CONNECTION
1	Vss	GND
2	VDD	Power Supply for logic
3	Vo	Operating Supply for LCD
4	A0	H: Data ;L: Instruction code
5	/CS1	Chip selection for IC1, active "L"
6	/CS2	Chip selection for IC2, active "L"
7	CL	External clock input (2kHz)
8	/RD (E)	/RD for 80 MPU, E for 68 MPU
9	/WR(R/W)	/WR for 80 MPU, R/W for 68 MPU
10	DB0	Data bus line
11	DB1	
12	DB2	
13	DB3	
14	DB4	
15	DB5	
16	DB6	
17	DB7	
18	/RST	Reset, H→L: 80 MPU, L→H:68 MPU
19	LEDA	Power supply for LED backlight
20	LEDK	

Remark

1. LCD option: STN, FSTN .
2. Customized module.
3. Backlight option: EL backlight feature, other specs not available on catalog is under request.