HITACHI

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FOR MESSRS.	DATE	Mar.06,2009
	DATE.	<u>wai.00,2009</u>

CUSTOMER'S ACCEPTANCE SPECIFICATIONS

SP14Q002-C1 CONTENTS

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* When products will be discontinued, customers will be informed by HITACHI with twelve months prior announcement.

ACCEPTED BY;		PROPOSED BY;	Dan Chom	9
				1

KAOHSIUNG HITACHI	Sh.	7B64PS 2701- SP14Q002-C1-9	PAGE	1_1/1
ELECTRONICS CO.,LTD.	No.	7B04F3 2701- 3F14Q002-C1-9	FAGL	1-1/1

DATE	SHEET No.	SUMMARY										
Mar.29.02'	7B64PS-2704- SP14Q002-C1-2		ENVIRON	MEN ⁻	TAL AB	SOLUT	E M	AXIMUM	RATING	S		
	PAGE 4-1/1			ITEM		0	OPERATING			STORAGE		
							MIN. MAX.			MAX.		
			Ambient Te	empera	ature	0°	C	50°C Note 5	-20 °C	60 °C		
						 ↓ Re	evise	L d				
				ITEM	<u> </u>	C	PERA	ATING	STORAGE			
						М	IIN	MAX	MIN	MAX		
			Ambient Temperature				O°C	70°C Note 5	-30 ℃	80 °C		
Apl.19.02'	7B64PS-2704- SP14Q002-C1-3	Note 4.2 Adde		↓R 30℃ - ИЕNТ	evised < 48h, a AL ABS	at 80°C ∈ SOLUTI	< 168 E M <i>P</i>	sh. AXIMUM	RATING	S		
	PAGE 4-1/1	Note	e 6 Operation	on ten	np not ir	nclude (CFL la	amp.				
	7B64PS-2704-	5.1 ELECTRICAL CHARACTERISTICS										
	SP14Q002-C1-3		ITEM		S	YMBOL	CC	NDITION	TYP.	UNIT		
	PAGE 5-1/1		Recommer	Recommended LC VDD-V0 $Ta=0^{\circ}C$, $\phi=0^{\circ}$		22.0	V					
			Driving Vol	tage				25°C , <i>φ</i> =0°	21.0	V		
			Note 3				la=t evise	50°C, <i>∲</i> =0°	20.0	V		
			ITENA			,			TVD	LINIT		
			ITEM Recomme	ndod I		YMBOL /DD-V0		ONDITION 0° C, ϕ = 0°	(25.0)	UNIT		
			Driving Vo					5°C, <i>φ</i> =0°	(24.0)	V		
			Note 3	ilago				0°C , <i>φ</i> =0°	(23.0)	V		
		5.2 ELECTRICAL CHARACTERISTICS OF BACKLIGHT Added: Note: The half operating life time of backlight. CFL:50,000h(Average)										
	7B64PS 2706- SP14Q002-C1-3 PAGE 6-1/2	Res	OPTICAL C ponse(rise) ponse(fall)	tr:12	$0 \rightarrow (3$	36)						
	G HITACHI			Sh.								

DATE Mar.06,'09

ELECTRONICS CO.,LTD.

DATE	SHEET No.		SUMMARY						
Jul.11,'02	7B64PS-2703-	(1	0) BACK LIGHT						
	SP14Q002-C1-4		Added :						
	PAGE 3-1/1		The half brightnes		of backlight				
		1	CFL: 50,000h(av		ITE NANZINAI	UNA DAT	11.100		
	7B64PS-2704- SP14Q002-C1-4	4.	2 ENVIRONMENTA	L ABSOLU	JIE MAXIMI	JM RAT	INGS		
	PAGE 4-1/1		ITEM	OP	ERATING	ST	ORAGE		
				MIN.	MAX.	MIN.	MA	X.	
			Ambient Temperature	-20 ℃	70 °C Note 5	-30 ℃	80°	С	
					↓ Revised				
			ITEM	OP	ERATING	ST	ORAGE		
				MIN.	MAX.	MIN.	MA	Χ.	
			Ambient Temperature	0℃	50 °C Note 5	-20℃	60°	С	
	7B64PS 2705-		Note 2 Ta at 0°C < 48h, AT 60°C < 168h 5.1 ELECTRICAL CHARACTERISTICS						
	SP14Q002-C1-4		ITEM	SYMBOL	CONDIT	ION	TYP.	UNIT	
	PAGE 5-1/1		Recommended LC	OTNIBOL	Ta=0°C <i>φ</i> =		(25.0)	V	
			Driving Voltage	VDD-V0	Ta=25°C	<i>∮</i> =0 °	(24.0)	V	
			Note 3		Ta=50°C	<i>∮</i> =0 °	(23.0)	V	
			↓ Revised						
			ITEM	SYMBOL	CONDITI	ON	TYP.	UNIT	
			Recommended LC		Ta=0°C <i>φ</i>	=0°	22.0	V	
			Driving Voltage	VDD-V0	Ta=25°C <i>q</i>	<i>⁄</i> =0°	21.0	V	
			Note 3		Ta=50°C <i>q</i>	Ta=50°C <i>∲</i> =0°		V	
		5.: St De	ote 4 VDD-V0=(24.0 2 ELECTRICAL CHA arting discharge volta eleted : ote The half brightne FL : 50,000h(average	RACTER age min.	STICS OF E (1000) → 10	000	SHT		

KAOHSIUNG HITACHI	DATE	Mar.06,'09	Sh.	7B64PS 2702-SP14Q002-C1-9	DAGE	2 2/4
ELECTRONICS CO.,LTD.	DATE	101a1.00, 09	No.	7B04F3 2702-3F14Q002-C1-9	PAGE	2-2/4

DATE	SHEET No.		SUMM	ARY					
Jul.11,'02	7B64PS-2706-	6.1 OPTICAL CHARACT							
	SP14Q002-C1-4	Revised							
	PAGE 6-1/2	Response(rise) tr: (336) → 120							
			tesponse(fall) tf: (148) → 150						
Jul.16,'02	7B64PS-2703-	(10).Back Light Type							
	SP14Q002-C1-5	Cold cathode fluores		•					
	PAGE 3-1/1	The half brightness lif CFL: 50,000h(average		backlight					
		VPL : 50,000n(averaç	je)						
		Cold cathode fluores	scent lam	n					
		CFL life time : 50,000		-					
		Note : CFL life time =	, –	,	CFL brigh	ntness.			
	7B64PS-2709-	9.1 DIMENSIONS OUTL							
	SP14Q002-C1-5	Dimensions express re	evised						
	PAGE 9-1/2								
	7B64PS-2709-	9.3 INTERFACE PIN CO	NNECTIO	N					
	SP14Q002-C1-5	1.LCM I/F1 Revised –		CN1					
	PAGE 9-2/2	2.LCM CFL Revised =	Revisea → LCIVI CNZ						
	7B64PS-2710-	10.2 DEFINITION OF EA	CH ZONE						
	SP14Q002-C1-5	B zone : edge line of L	_						
	PAGE 10-1/3	↓ Revised							
1 1 40 100	_	B zone : Window of M							
Jul.18,'02	7B64PS-2704-	4.2 ENVIRONMENTAL	ABSOLUI	IE MAXIN	IUM RAI	INGS			
	SP14Q002-C1-6 PAGE 4-1/1	ITEM	OPER	ATING	STOR	RAGE			
	1 AGE 4-1/1		MIN.	MAX.	MIN.	MAX.			
		Ambient Temperature	0℃	50 ℃	-20 ℃	60℃			
			,	Revised					
		ITEM	OPER	ATING	STOF	RAGE			
		MIN. MAX. MIN. MAX.							
		Ambient Temperature -20°C 70°C -30°C 80°C							
		Note 2 Ta at -20°C < 48h ↓ Revis Note 2 Ta at -30°C < 48h	ed						

KAOHSIUNG HITACHI		Mar.06,'09	Sh.	7B64PS 2702-SP14Q002-C1-9	DAGE	2 2/4
ELECTRONICS CO.,LTD.	DATE	Mar.06, 09	No.	7B64P3 2702-SP14Q002-C1-9	PAGE	2-3/4

DATE	SHEET No.		SUMMARY								
Feb.25,'04	7B64PS-2706-	6	.1 OPTICAL (CHARACTER	RISTICS						
	SP14Q002-C1-7	R	Revised								
	PAGE 6-1/2	R	Response Tim	e (Rise) tr : 1	$20 \rightarrow 33$	36					
		R	Response Tim	e (Fall) tf:1	$50 \rightarrow 14$	18					
	7B64PS-2708-	8	.3 POWER	ON/OFF TIM	IING SE	QUENCE	=				
	SP14Q002-C1-7	R	Revised								
	PAGE 8-3/3		DLD : min. 20	$00 \rightarrow 50$							
	7132 0 0/0	t(tCH : max. 200 → 30								
May.14,'04	7B64PS2705	5	.1 ELECTRIC	CAL CHARA	CTERIS1	TICS					
-	SP14Q002-C1-8	A	dded								
	PAGE 5-1/2		ITEM		SYMBOL	MIN.	TYP.	MA	X		
			Power Supply	y Voltage Logic	VDD-VSS	3.2	3.3	3.4	1		
				,		21.0	22.0	23.	0		
			Recommend	LC Driving Voltage	VDD-VO	20.0	21.0				
						19.0	20.0				
	7DC4DC0705	5	.2 ELECTRIC	CAL CHARAC	TERIST						
	7B64PS2705		canceled		JILINIOI	100 01	D/ (OI	(LIOIII			
	SP14Q002-C1-8	Note 5:When ICFL is used over 5.5 mA ,it may cause uneven									
	PAGE 5-2/2		contrast near CFL location ,due to heart dispersion from CFL.								
	700400 0700		6.2 OPTICAL CHARACTERISTICS OF BACKLIGHT								
	7B64PS 2706-		Added								
	SP14Q002-C1-8										
	PAGE 6-2/2	The LCD driving voltage should be adjusted at the voltage where the peak contrast is obtained.									
	700400 0740		0.1 APPEAR				TION				
	7B64PS 2710-		Revised 45°-		LOTION	CONDI	IION				
	SP14Q002-C1-8	'	Vevised 43	723							
	PAGE 10-1/3										
Mar.06,'09	7B64PS2712	1	2. DESIGNA	TION OF LC	T MARK	(
	SP14Q002-C1-9	R	Revised rever	sion from R	EVto	REV.B					
	PAGE 12-1/1										
AOHSIUN	3 HITACHI		M 00 100	Sh.	2.0705.57	140000	04.5	D405	0.4		
. = 0 = 0 0 0	D	ATE	Mar.06,'09	, 7B64P3	S 2702-SF	'14Q002-	·C1-9	PAGE	2-4/		

ELECTRONICS CO.,LTD.

3. GENERAL SPECIFICATIONS

(1) Part Name SP14Q002-C1

(2) Module Size 167.0(W)mm×109.0(H)mm×10.0 (D)mm(max.)

(3) Effective Display Area 120(W)mm min. × 89(H)mm min.

(4) Dot Size 0.345(W)min. × 0.345(H)min.

(5) Dot Pitch 0.360(W)mm × 0.360(H)mm

(6) Dot Number (Resolution) 320 (W) x 240 (H) dots

(7) Duty Ratio 1/240

(8) LCD Type Transmissive type F-STN

With glare type upper polarizer

(9) Viewing Direction 6 O'clock

(10) Back Light Type Cold cathode fluorescent lamp.

CFL life time: 50,000h(average)

Note: CFL life time = life time for half of CFL

brightness.

4. ABSOLUTE MAXIMUM RATINGS

4.1 ELECTRICAL ABSOLUTE MAXIMUM RATINGS

ITEM	SYMBOL	MIN.	MAX.	UNIT	COMMENT
Power Supply for Logic	VDD-VSS	0	6.0	V	
Power Supply for LC Driving	VDD-VEE	0	27.5	V	
Input Signal Voltage	Vi	-0.3	VDD+0.3	V	Note 1
Input Signal Current	li	0	1	Α	
Static Electricity	VESD0	-	±100	V	Note 2,3,4
	VESD1	-	±10	kV	Note 2,3,5

VSS=0V: STANDARD

Note 1: DISP.OFF, FRAME, LOAD, CP, D0~D3.

Note 2: Make certain you are grounded when handling LCM.

Note 3 : Energy storage capacitance 200pF , discharge resistance 250 Ω Ta=25 $^{\circ}$ C , 60%RH.

Note 4: Contact discharge to I/F connector pins.

Note 5 : Contact discharge to front metal bezel.

4.2 ENVIRONMENTAL ABSOLUTE MAXIMUM RATINGS

1.2 ETVITOTUIETTILE PROCESTE WARRINGW TOTTINGS								
ITEM	OPER	ATING	STO	RAGE	OMMNT			
	MIN.	MAX.	MIN.	MAX.				
Ambient Temperature	-20 ℃	70 ℃	-30 ℃	80 °C	Note 2,3,7			
Humidity	Not	e 1	Note 1		Without Condensation			
		2.45m/s ²		11.76m/s ²				
Vibration	-	(0.25G)	-	(1.2G)	Note 4			
				Note 5	1h max.			
		29.4m/s ²		490.0m/s ²				
Shock	-	(3 G)	-	(50 G)	X、Y、Z Directions			
				Note 5				
Corrosive Gas	Not Accep	table	Not Accep	table				

Note 1 Ta \leq 40°C : 85%RH max.

Ta>40°C : Absolute humidity must be lower than the humidity of 85%RH at 40°C

Note 2 Ta at -30° C -----< 48h, at 80° C < 168h.

Note 3 Background color changes slightly depending on ambient temperature. This phenomenon is reversible.

Note 4 5Hz~100Hz (Except resonance frequency)

Note 5 This module should be operated normally after finish the test.

Note 6 When LCM will be operated at 0° C, the life time of CFL will be reduced. Please make sure that characteristics of the inverter meet the CFL specification.

Note 7 Operation temp not include CFL

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5. ELECTRICAL CHARACTERISTICS

5.1 ELECTRICAL CHARACTERISTICS

ITEM	SYMBOL	CONDITION	MIN.	TYP.	MAX.	UNIT	
Power Supply Voltage	VDD-VSS	_	4.75	5.0	5.25	V	
for Logic	VDD-V33	-	3.2	3.3	3.4	V	
Power Supply Voltage for LC Driving	VEE-VSS	-	-23.1	-22.0	-20.9	V	
Input Signal Voltage	Vi	H LEVEL	0.8VDD	-	VDD	V	
Note 1	VI	L LEVEL	0	-	0.2VDD	V	
Power Supply Current	IDD	VDD-VSS=5.0V	-	6.0	-	mA	
for Logic Note 1	טטו	VEE-VSS= -22.0V					
Power Supply Current	IEE	VDD-VSS=5.0V	-	5.0	-	mΑ	
for LC Driving Note 2	ICC	VEE-VSS= -22.0V					
Recommended LC		Ta= 0° C , ϕ = 0°	21.0	22.0	23.0	V	
Driving Voltage	VDD-V0	Ta=25 $^{\circ}$ C , ϕ = 0 $^{\circ}$	20.0	21.0	22.0	V	
Note 3		Ta=50°C , <i>φ</i> = 0°	19.0	20.0	21.0	V	
FRAME Frequency Note 4	fFRAME	-	70	75	80	Hz	

Note 1 DISP.OFF, FRAME, LOAD, CP, D0~D3.

Note 2 : FLM=75Hz , test pattern is all "Q". VDD-V0=21.0V , Ta=25 $^{\circ}$ C

Note 3 : Recommended LC driving voltage may fluctuate about $\pm 1.0 V$ by each module. Test pattern is all "Q"

Note 4: Please set the frame frequency so as to avoid flicker and rippling on the display.

5.2 ELECTRICAL CHARACTERISTICS OF BACKLIGHT

ITEM	SYMBOL	MIN.	TYP.	MAX.	UNIT	NOTE
Lamp Voltage	VL		(300)	1	Vrms	Ta=25°ℂ
Frequency	fL	-	70	85	kHz	Ta=25°ℂ
Lamp Current	IL	4	5	6	mArms	Ta=25°ℂ
Starting Discharge Voltage	VS	1000	-	-	Vrms	Ta=25°ℂ

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Note 1: Please make sure that your inverter is designed to meet the above specifications. Note 2: Starting discharge voltage is increased when LCM is operating at lower temperature, please check the characteristics of your inverter, so as to ensure discharge at low temperature. Note 3: Average life time of CFL will be decreased when LCM is operating at lower temperature. Note 4: Lower driving frequency of CFL inverter may cause mechanical noise of the backlight system. Before designing the inverter, please consider the driving frequency of noise.

KAOHSIUNG HITACHI		NA 00 100	Sh.	7D04D0 0705 0D440000 04 0	D 4 C F	F 0/0
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6. OPTICAL CHARACTERISTICS

6.1 OPTICAL CHARACTERISTICS OF LCD

ITEM

Viewing Area

Contrast Ratio

Response Time (Rise)

Response Time (Fall)

Note 1. Definition of θ and ϕ

Ta=25°ℂ (Backlight on)											
MIN.	TYP.	MAX.	UNIT	NOTE							
ı	40	-	deg	1,2							
ı	25	-	ı	3							
-	336	-	ms	4							

ms

4

(Measure condition by HITACHI)

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Note 3. Definition of contrast "K"

CONDITIONAL

K≥2.0

 $\theta = 0^{\circ}$

 $\theta = 0^{\circ}$

 $\phi = 0^{\circ}$, $\theta = 0^{\circ}$

 $\phi=0^{\circ}$

 $\phi=0^{\circ}$

SYMBOL

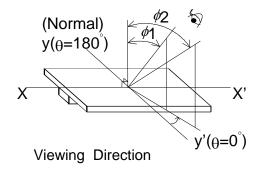
 $\phi 2 - \phi 1$

K

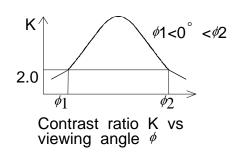
tr

tf

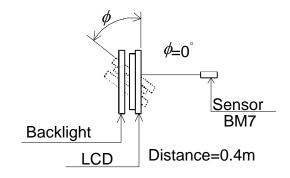
K= Brightness on selected dot (B1)
Brightness on non-selected dot (B2)



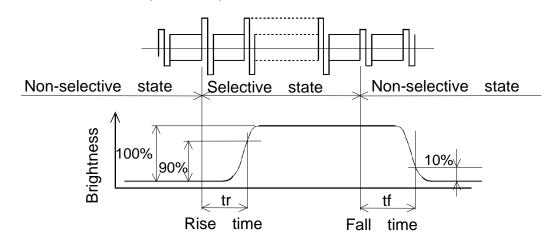
Note 2. Definition of viewing angle $\phi 1$ and $\phi 2$.



(B1) (Di="H") ϕ =10° (B2) (Di="L") Operating Voltage



Note 4. Definition of optical response



KAOHSIUNG HITACHI		Mar 06 '00	Sh.	7DC4DC 2700 CD440002 C4 0	DACE	6-1/2
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6.2 OPTICAL CHARACTERISTICS OF BACKLIGHT

ITEM	MIN.	TYP.	MAX.	UNIT	NOTE
Drightness		1.40		cd/m ²	IL=5mA
Brightness	-	140	-		Note 1,2
Diag Time		E		minuto	IL=5mA
Rise Time	-	5	-	minute	Brightness 80%
Brightness Uniformity	-	-	±30	%	Note 1,3

CFL: Initial, Ta=25°C

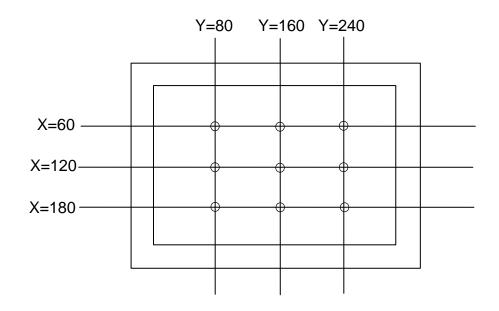
Display data should be all "ON".

The LCD driving voltage should be adjusted at the voltage where the peak contrast is obtained.

Note 1 Measurement after 10 minutes of CFL operating.

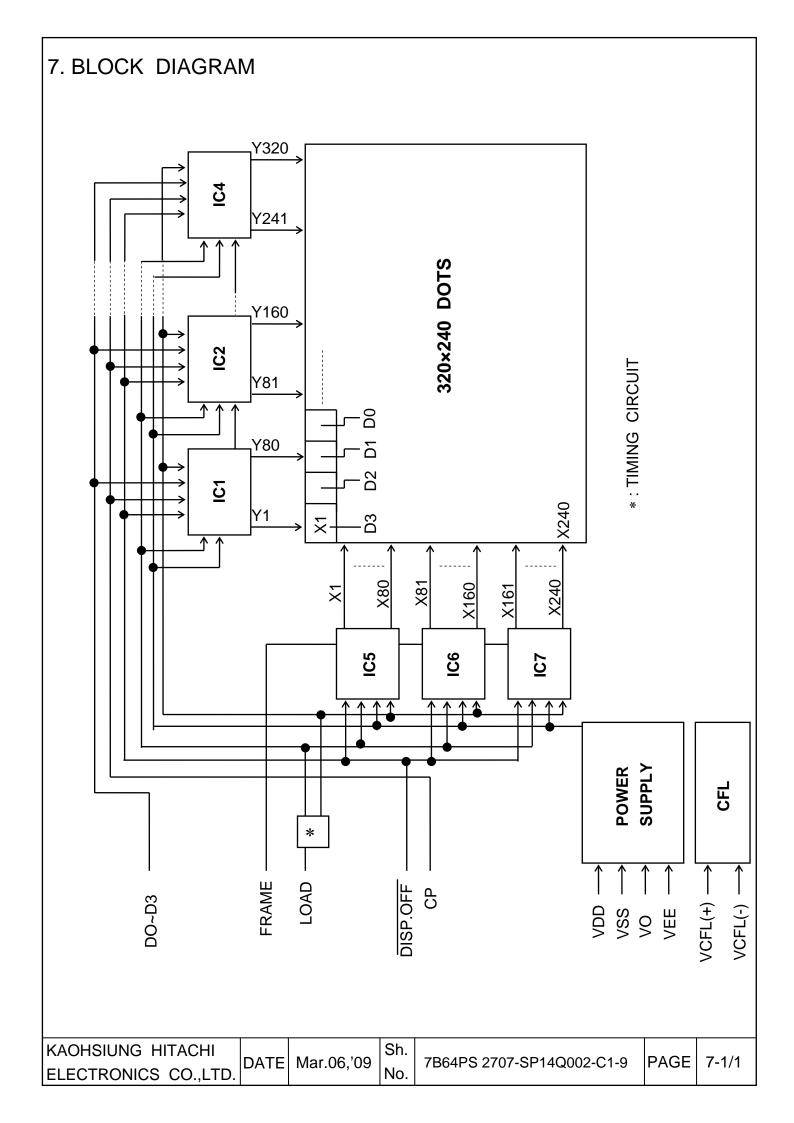
Note 2 Brightness control: 100%

Note 3 Measure of the following 9 places on the display.



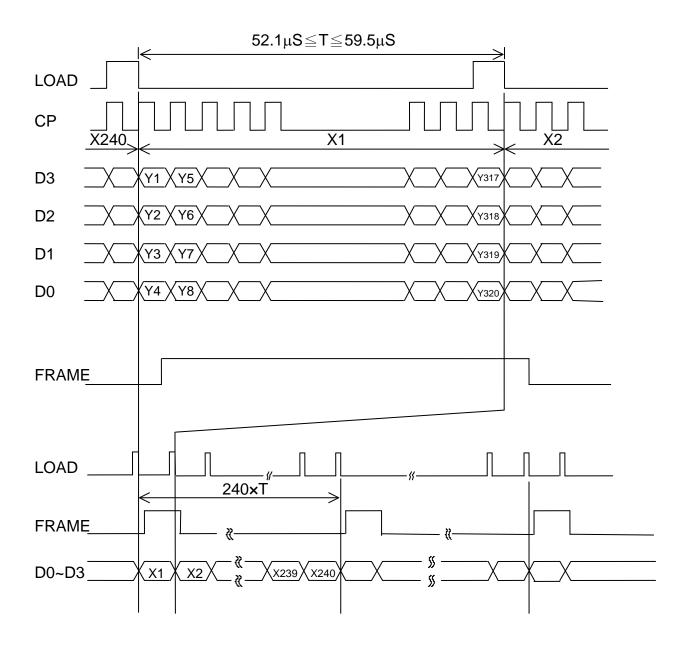
Definition of the brightness tolerance.

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8. INTERFACE TIMING CHART

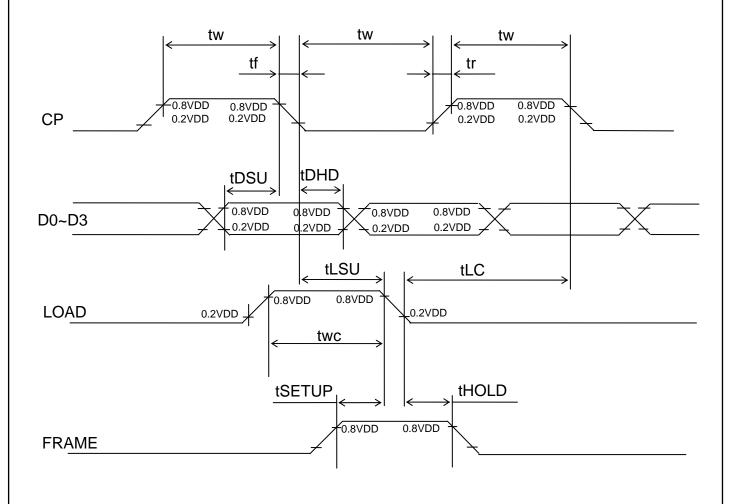
8.1 INTERFACE TIMING CHART



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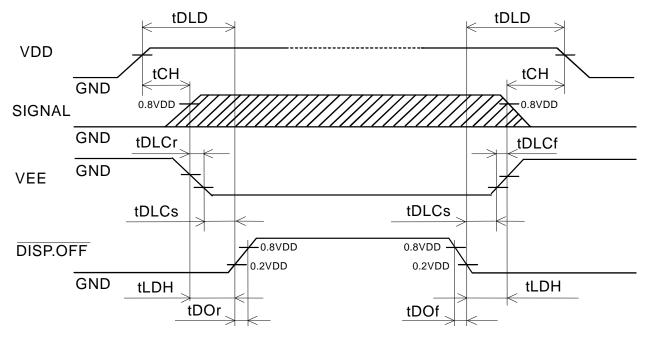
8.2 TIMING CHARACTERISTICS

ITEM	SYMBOL	MIN.	TYP.	MAX.	UMIT
Clock frequency	fCP	-	-	6.5	MHz
Clock pulse width	tW	45	-	1	ns
Clock rise, fall time	tr,tf	•	-	15	ns
Data set up time	tDSU	30	-	1	ns
Data hold time	tDHD	30	-	1	ns
Load set up time	tLSU	80	-	1	ns
Load clock time	tLC	120	-	1	ns
"FRAME" set up time	tSETUP	100	-	1	ns
"FRAME" hold time	tHOLD	100	-	1	ns
"LOAD" pulse width	tWC	125	-	-	ns



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8.3 POWER ON/OFF TIMING SEQUENCE



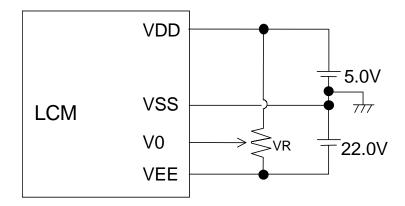
SYMBOL	MIN.	MAX.	UNIT	COMMENT
tDLD	50	-	ms	
tCH	0	30	ms	(Note 1)
tLDH	0	-	ms	
tDOr	-	100	ns	
tDOf	-	100	ns	
tDLCr	0	-	ms	(Note 2)
tDLCf	0	-	ms	
tDLCs	20	-	ms	

Note 1 Please keep the specified sequence because wrong sequence may cause permanent damage to the LCD panel.

Note 2 HITACHI recommends you to use DISP.OFF function.

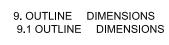
display quality may deteriorate if you don't use DISP.OFF function.

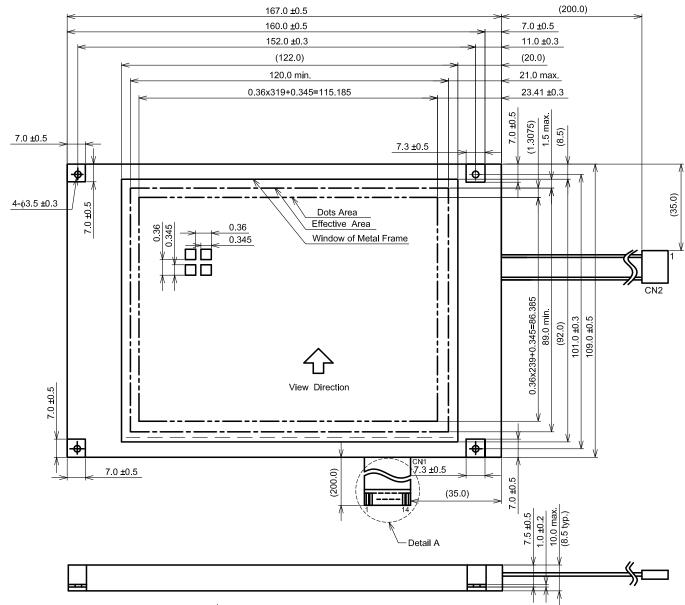
8.4 POWER SUPPLY FOR LCM (EXAMPLE)

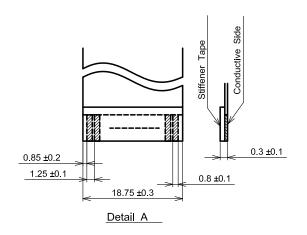


Note 1 : VR : $10k\Omega$

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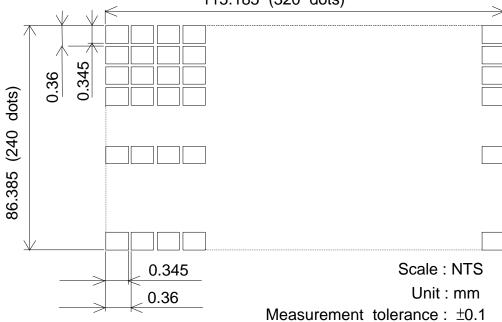
Note(1) Measurement when adding 9.8 x 10 ⁴Pa at the measuring point.

Scale : NTS Unit : mm

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9.2 DISPLAY PATTERN

115.185 (320 dots)



9.3 INTERFACE PIN CONNECTION

FPC: pitch 1.25mm 14 pins

INTER	FACE	PIN No.	SIGNAL	LEVEL	FUNCTION
LCM	CN1	1	D0	H/L	Display Data
		2	D1		
		3	D2		
		4	D3		
		5	DISP.OFF	H/L	H:ON / L:OFF
		6	FRAME	Н	First Line Marker
		7	N.C	-	-
		8	LOAD	H→L	Data Latch
		9	CP	H→L	Data Shift
		10	VDD	-	Power Supply for Logic
		11	VSS	-	GND
		12	VEE	-	Power Supply for LC
	13		V0	-	Operating Voltage LC Driving
		14	VSS	-	GND

INTER	RFACE	PIN No.	SIGNAL	LEVEL	FUNCTION
LCM	LCM CN2 1 VCFL(+		VCFL(+)	-	Power Supply for CFL
	2		N.C	-	-
		3	N.C	-	-
		4	VCFL(-)	-	CFL GND

CFL I/F: J.A.E./ IL - G - 4S - S3C2

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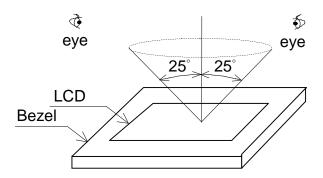
10. APPEARANCE STANDARD

10.1 APPEARANCE INSPECTION CONDITIONS

Visual inspection should be done under the following condition.

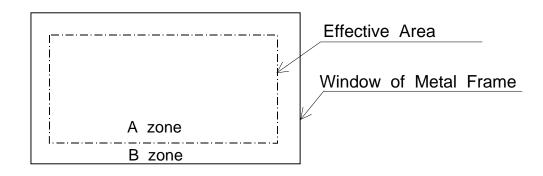
- (1) The inspection should be done under in the dark room.
- (2) The CFL should be lighted with the prescribed inverter.
- (3) The distance between eyes of an inspector and the LCD module is 25cm.
- (4) The viewing zone is shown the figure.

Viewing angle ≤25°



10.2 DEFINITION OF EACH ZONE

A zone: Within the effective area specified at page 9-1/2 of this document. B zone: Area between the window of metal frame and the effective area line specified at page 9-1/2 of this document.



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10.3 APPEARANCE SPECIFICATION

*) If a problem occurs in respect to any of these items, both parties (Customer and HITACHI) will discuss in more detail.

No.	ITEM	CRITERIA						
	Scratches	Distinguished of					*	-
		(To be judged		limit sa	mple)			
	Dent	Same as above					*	-
	Wrinkles in Polarizer	Same as above	/e				*	-
	Bubbles	Average of		Max	ximum	n number		
		D(m			accep			
			≦0.2		Ign			
		0.2 <d< td=""><td></td><td colspan="3">12</td><td></td><td>-</td></d<>		12				-
		0.3 <d< td=""><td></td><td></td><td>3</td><td></td><td></td><td></td></d<>			3			
		0.5<[NO	NE		
	Stains,		+	entous				
	Foreign Materials,	Length Width Maximum number			\bigcirc	-		
	Dark Spot	L(mm)	W(mn	,	а	cceptable		
		L≦2.0	W≦0			Ignore	_	
١,		L≦3.0	0.03 <w≦0< td=""><td></td><td></td><td>6</td><td></td><td></td></w≦0<>			6		
L		L≦2.5 0.05 <w<0.1 1<="" td=""><td></td></w<0.1>						
				und				
		Average diameter		Maximum number		Minimum		
С		D(mm) acceptal				Space	1	
		D<0.2	Ignor	е		-		-
		0.2 ≦D<0.33	8			10mm		
		0.33≦D	None			-		
D		Total	Filamentous					
	-	Those wiped of					0	\bigcirc
	Color Tone	To be judged	•	limit san	nple		0	-
	Color Uniformity	Same as above			_		\bigcirc	-
	Pinhole	Average of				number		
		D(m			accep			
			0.15		Ign		4	
		0.15 <d≦< td=""><td></td><td></td><td><u> </u></td><td></td><td>4</td><td></td></d≦<>			<u> </u>		4	
		+	0.015		Ign		_	
	Contrast	Average	Contrast	Maximu		Minimum		-
	Irregularity	diameter		numb	-	Space		
	(Spot)	D(mm)	- .	accepta				
		D≦0.25	To be	Ignor	е	-	-	
		0.25 <d≤0.35< td=""><td>judged by</td><td>10</td><td></td><td>20mm</td><td>4</td><td></td></d≤0.35<>	judged by	10		20mm	4	
		0.35 <d≤0.5< td=""><td>HITACHI</td><td>4</td><td></td><td>20mm</td><td>-</td><td></td></d≤0.5<>	HITACHI	4		20mm	-	
		0.5 <d< td=""><td></td><td>None</td><td>е</td><td>-</td><td></td><td></td></d<>		None	е	-		

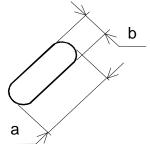
							1
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No.	ITEM		CRIT	ERIA		Α	В
	Contrast Irregularity (Line)	Width D(mm)	Length L(mm)	Maximum number acceptable	Minimum Space		
L	(Filamentous)	W≦0.25	L≦1.2	2	20mm		
С		W≦0.2	L≦1.5	3	20mm		-
D		W≦0.15	L≦2.0	3	20mm		
		W≦0.1	L≦3.0	4	20mm		
		TO	TAL	(6		
	Rubbing Scratch	To be judged	by HITACHI	standard	_		-

No.	ITEM	CRITERIA			
С	Dark Spots, White Spots	D≦0.4 D>0.4		Ignore	
F	Foreign Materials (Spot)			None	
L		W≦0.2	L<2.5	≦1	
	Foreign Materials (Line)	W≦0.2	L>2.5	None	
В		W>	0.2	None	
/		W≦	0.1	Ignore	
L	Scratches	0.1 <w≦0.2< td=""><td>L≦11.0</td><td>≦1</td></w≦0.2<>	L≦11.0	≦1	
	Scratches	0.1 <w≦0.2< td=""><td>L≧11.0</td><td>None</td></w≦0.2<>	L≧11.0	None	
		W<	0.2	None	

Note

(1) Definition of average diameter D

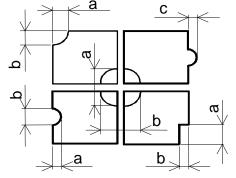


$$D = \frac{a+b}{2}$$

(2) Definition of length L and width W



(3) Definition of pinhole



c : Salience

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11. PRECAUTION IN DESIGN

11.1 LC DRIVING VOLTAGE (VEE) AND VIEWING ANGLE RANGE Setting VEE out of the recommended condition will be a cause for a change of viewing angle range.

11.2 CAUTION AGAINST STATIC CHARGE

As this module contains C-MOS LSIs, it is not strong against electrostatic discharge. Make certain that the operator's body is connected to the ground through a list band etc. And don't touch I/F pins directly.

11.3 POWER ON SEQUENCE

Input signals should not be applied to LCD module before power supply voltage is applied and reaches to specified voltage (VDD).

If above sequence is not kept, C-MOS LSIs of LCD modules may be damaged due to latch up phenomenon

11.4 PACKAGING

- (1) No leaving product is preferable in the place of high humidity for a long period of time. For their storage in the place where temperature is 35 °C or higher, special care to prevent them from high humidity is required. A combination of high temperature and high humidity may cause them polarization degradation as well as bubble generation and polarizer peel-off. Please keep the temperature and humidity within the specified range for use and storage.
- (2) Since polarizers tend to be easily damaged, They should be handled full with care so as not to get them touched, pushed or rubbed.
- (3) As the adhesives used for adhering polarizers are made of organic substances which will be deteriorated by a chemical reaction with such chemicals as acetone, toluene, ethanol and isopropyl-alcohol. The following solvents are recommended for use:

 Normal hexane

Please contact us when it is necessary for you to use chemicals.

(4) Lightly wipe to clean the dirty surface with absorbent cotton waste or other soft material like chamois, soaked in the chemicals recommended without scrubbing it hardly. To prevent the display surface from damage and keep the appearance in good state, it is sufficient, in general, to wipe it with absorbent cotton.

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- (5) Immediately wipe off saliva or water drop attached on the display area because its long period adherence may cause deformation or faded color on the spot.
- (6) Foggy dew deposited on the surface due to coldness will be caused for polarizer damage, stain and dirt on product. When necessary to take out the products from some place at low temperature for test, etc. It is required for them to be warmed up in a container once at the temperature higher than that of room.
- (7) Touching the display area and contact terminals with bare hands and contaminating them are prohibited, because the stain on the display area and poor insulation between terminals are often caused by being touched by bare hands.
 (Some cosmetics are detrimental to polarizers.)
- (8) In general the quality of glass is fragile so that it tends to be cracked or chipped in handling, specially on its periphery. Be careful not to give it sharp shock caused by dropping down, etc.

11.5 CAUTION FOR OPAERATION

- (1) It is an indispensable condition to drive LCDs within the specified voltage limit since the higher voltage than the limit causes the shorter LCD life. An electrochemical reaction due to direct current causes LCDs undesirable deterioration, so that the use of direct current driver should be avoided.
- (2) Response time will be extremely delayed at lower temperature than the operating temperature range and on the other hand at higher temperature LCDs show dark blue color in them. However those phenomena do not mean malfunction or out of order with LCDs which will come back in the specified operating temperature range.
- (3) If the display area is pushed hard during operation, some font will be abnormally displayed but it resumes normal condition after turning off once.
- (4) A slight dew depositing on terminals is a cause for electrochemical reaction resulting in terminal open circuit. Usage under the relative condition of 40°C 50%RH or less is required.

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11.6 STORAGE

In case of storing for a long period of time (for instance, for years) for the purpose of replacement use, the following ways area recommended.

- (1) Storage in a polyethylene bag with the opening sealed, so the fresh air will not be entered from outside.
- (2) Placing in a dark place where neither exposure to direct sunlight nor light is, keeping temperature in the range from 0 $^{\circ}$ C to 35 $^{\circ}$ C.
- (3) Storing with no touch on polarizer surface by anything else. (It is recommended to store them as they have been contained in the inner container at the time of delivery from us.)

11.7 SAFETY

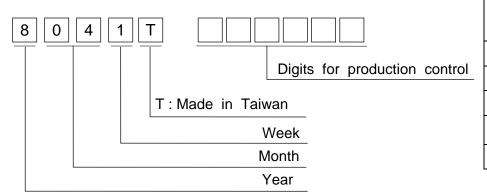
- (1) It is recommendable to crash damaged or unnecessary LCDs into pieces and wash off liquid crystal by either of solvents such as acetone and ethanol, which should be burned up later.
- (2) When any liquid leaked out of a damaged glass cell comes in contact with your hands, please wash it off well with soap and water.

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12. DESIGNATION OF LOT MARK

12.1 LOT MARK

Lot mark is consisted of 5 digits for production lot and 6 digits for production control.



Year	Figure in
	lot mark
2009	9
2010	0
2011	1
2012	2
2013	3

Month	Figure in	Month	Figure in		
Month	lot mark	Month	lot mark		
Jan.	01	Jul.	07		
Feb.	02	Aug.	08		
Mar.	03	Sep.	09		
Apr.	04	Oct.	10		
May	05	Nov.	11		
Jun.	06	Dec.	12		

Week	Figure in
(day in calendar)	lot mark
1~ 7	1
8~14	2
15~21	3
22~28	4
29~31	5

12.2 SERIAL No.

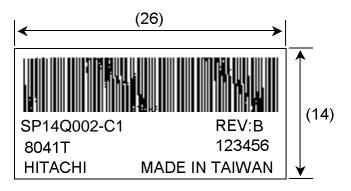
Serial No. is consisted of 6 digits number (000001~999999).

12.3 LOCATION OF LOT MARK

Label is bring attached on the back side of module.

12.4 REVISION(Rev.) CONTROL

Rev No.	ITEM
_	Mcount IC:MN73099HED(Panasonic)
	Transistor:2SA1036K(ROHM)
В	Mcount IC:IT7001M(ITE)
	Transistor:2SA1576(ROHM)



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13. PRECAUTION FOR USE

- 13.1 A limit sample should be provided by the both parties on an occasion when the both parties agreed its necessity. Judgment by a limit sample shall take effect after the limit sample has been established and confirmed by the both parties.
- 13.2 On the following occasions, the handling of the problem should be decided through discussion and agreement between responsible persons of the both parties.
 - (1) When a question is arisen in the specifications.
 - (2) When a new problem is arisen which is not specified in this specifications.
 - (3) When an inspection specifications change or operating condition change in customer is reported to HITACHI, and some problem is arisen in this specification due to the change.
 - (4) When a new problem is arisen at the customer's operating set for sample evaluation in the customer site.

The precaution that should be observed when handling LCM have been explained above. If any points are unclear or if you have any request, please contact HITACHI.

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