HITACHI

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FOR MESSRS. _____ DATE. Mar.06,2009

CUSTOMER'S ACCEPTANCE SPECIFICATIONS

SP14Q002-C1A

CONTENTS

* When products will be discontinued , customers will be informed by HITACHI with twelve months prior announcement.

ACCEPTED BY; _____ PROPOSED BY; Dan Chang

RECORD OF REVISION

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<tr><td>Mar.29.'02</td><td>7B64PS 2704-SP14Q002-C1A-2
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4.1 ELECTRICAL ABSOLUTE MAXIMUM RATINGS
Changed.
Power supply for logic : max. 7.0 → 6.0
Power supply for LC drive : max. 30 → 27.5

4.2 ENVIRONMENTAL ABSOLUTE MAXIMUM RATINGS.

<table>
<tr><th rowspan="2">ITEM</th><th colspan="2">OPERATING</th><th colspan="2">STORAGE</th></tr>
<tr><th>MIN.</th><th>MAX.</th><th>MIN.</th><th>MAX.</th></tr>
<tr><td>Ambient Temperature</td><td>0℃</td><td>60℃</td><td>-20℃</td><td>70℃</td></tr>
</table>
↓ Revised
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<tr><th rowspan="2">ITEM</th><th colspan="2">OPERATING</th><th colspan="2">STORAGE</th></tr>
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</table>
Note (2) Ta at -20℃ ------< 48h, at 60℃ < 168h
↓ Revised
Note (2) Ta at -30℃ ------< 48h, at 80℃ < 168h
</td></tr>
<tr><td></td><td>7B64PS 2705-SP14Q002-C1A-2
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<table>
<tr><th>ITEM</th><th>MIN.</th><th>TYP.</th><th>MAX.</th></tr>
<tr><td rowspan="2">Power Supply Voltage for Logic</td><td>5.0-5%</td><td>5.0</td><td>5.0+5%</td></tr>
<tr><td>3.3-5%</td><td>3.3</td><td>3.3+5%</td></tr>
</table>
↓ Revised
<table>
<tr><th>ITEM</th><th>MIN.</th><th>TYP.</th><th>MAX.</th></tr>
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</table>
</td></tr>
<tr><td></td><td>7B64PS 2706-SP14Q002-C1A-2
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<table>
<tr><th>ITEM</th><th>MIN.</th><th>TYP.</th><th>MAX.</th></tr>
<tr><td>Brightness</td><td></td><td>140</td><td></td></tr>
</table>
↓ Revised
<table>
<tr><th>ITEM</th><th>MIN.</th><th>TYP.</th><th>MAX.</th></tr>
<tr><td>Brightness</td><td></td><td>110</td><td></td></tr>
</table>
</td></tr>
</table>

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<tr><td>Mar.29,'02</td><td>7B64PS 2709-SP14Q002-C1A-2
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FPC : Pitch 1.0mm 4pins
<table>
<tr><th colspan="2">INTERFACE</th><th>PIN NO.</th><th>SIGNAL</th><th>FUNCTION</th></tr>
<tr><td rowspan="4">T/P</td><td rowspan="4">CN3</td><td>1</td><td>X1</td><td>Analog signal from digitizer right</td></tr>
<tr><td>2</td><td>Y1</td><td>Analog signal from digitizer up</td></tr>
<tr><td>3</td><td>X2</td><td>Analog signal from digitizer left</td></tr>
<tr><td>4</td><td>Y2</td><td>Analog signal from digitizer bottom</td></tr>
</table>
Recommend suitable connector : (Hirose)
FH12-10(4)SA-ISH</td></tr>
<tr><td rowspan="4">Apl.19,'02</td><td>7B64PS 2703-SP14Q002-C1A-3
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Transparency : 78% min. → 76% min.</td></tr>
<tr><td>7B64PS 2704-SP14Q002-C1A-3
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↓ Revised
Note (7) Operation temp not include CFL & touch panel</td></tr>
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<table>
<tr><th>ITEM</th><th>SYMBOL</th><th>CONDITION</th><th>TYP.</th><th>UNIT</th></tr>
<tr><td rowspan="3">Recommended LC Driving Voltage Note 3</td><td rowspan="3">VDD-V0</td><td>Ta=0℃ φ=0°</td><td>22.0</td><td>V</td></tr>
<tr><td>Ta=25℃ φ=0°</td><td>21.0</td><td>V</td></tr>
<tr><td>Ta=50℃ φ=0°</td><td>20.0</td><td>V</td></tr>
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</table>
5.2 ELECTRICAL CHARACTERISTICS OF BACKLIGHT
Deleted :
Note The half brightness life time of backlight.
CFL : 50,000h(average)</td></tr>
<tr><td>7B64PS 2706-SP14Q002-C1A-3
PAGE 6-1/2</td><td>6.1 OPTICAL CHARACTERISTICS
Response(rise) tr : 120 → (336)
Response(fall) tf : 150 → (148)</td></tr>
</table>

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<table>
<tr><th>DATE</th><th>SHEET No.</th><th>SUMMARY</th></tr>
<tr><td rowspan="3">Jul.11,'02</td><td>7B64PS 2703-SP14Q002-C1A-4 PAGE 3-1/1</td><td>(10) BACK LIGHT TYPE
Added :
The half brightness life time of backlight
CFL : 50,000h(average)</td></tr>
<tr><td>7B64PS 2704-SP14Q002-C1A-4 PAGE 4-1/1</td><td>4.2 ENVIRONMENTAL ABSOLUTE MAXIMUM RATINGS.
<table>
<tr><th>ITEM</th><th colspan="2">OPERATING</th><th colspan="2">STORAGE</th></tr>
<tr><td rowspan="2">Ambient Temperature</td><td>MIN.</td><td>MAX.</td><td>MIN.</td><td>MAX.</td></tr>
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<tr><td>0℃</td><td>50℃</td><td>-20℃</td><td>60℃</td></tr>
</table>
Note(2) Ta at -30℃-----<48h, at 80℃ < 168h
↓ Revised
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<tr><td>Ta=50℃ ϕ=0°</td><td>20.0</td><td>V</td></tr>
</table>
Note 4 VDD-V0=(24.0)V → (21.0)V

5.2 ELECTRICAL CHARACTERISTICS OF BACKLIGHT
Starting discharge voltage min. (1000) → 1000

Deleted :
Note The half brightness life time of backlight.
CFL : 50,000h(average)</td></tr>
</table>

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Revised
Response(rise) tr : (336) → 120
Response(fall) tf : (148) → 150</td></tr>
<tr><td rowspan="4">Jul.16,'02</td><td>7B64PS-2709-
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PAGE 3-1/1</td><td>(10).Back Light Type
Cold cathode fluorescent lamp.
The half brightness life time of backlight
CFL : 50,000h(average)
↓ Revised
Cold cathode fluorescent lamp.
CFL life time : 50,000h(average)
Note : CFL life time = life time for half of CFL brightness.</td></tr>
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Dimensions express revised</td></tr>
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2.LCM CFL I/F1 Revised → LCM CN2</td></tr>
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B zone : edge line of LCD glass
↓ Revised
B zone : Window of Metal Frame</td></tr>
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<tr><td rowspan="2">Ambient Temperature</td><td>MIN.</td><td>MAX.</td><td>MIN.</td><td>MAX.</td></tr>
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</table>
Note 2 Ta at -20℃ < 48h, at 60℃ < 168h.
↓ Revised
Note 2 Ta at -30℃ < 48h, at 80℃ < 168h.</td></tr>
<tr><td>Sep.26,'02</td><td>7B64PS-2714-
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XR-XL 230~980Ω Revised → 150~1300Ω
YU-YB 200~520Ω Revised → 150~1300Ω</td></tr>
</table>

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Revised
Response Time (Rise) tr : 120 → 336
Response Time (Fall) tf : 150 → 148</td></tr>
<tr><td></td><td>7B64PS 2708-
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Revised
tDLD min. 200 → 50
tCH max. 200 → 30</td></tr>
<tr><td>May.14,'04</td><td>7B64PS 2705-
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Page 5-1/2</td><td>5.1 ELECTRICAL CHARACTERISTICS
Added
<table>
<tr><th>ITEM</th><th>SYMBOL</th><th>MIN.</th><th>TYP.</th><th>MAX</th></tr>
<tr><td>Power Supply Voltage Logic</td><td>VDD-VSS</td><td>3.2</td><td>3.3</td><td>3.4</td></tr>
<tr><td rowspan="3">Recommend LC Driving Voltage</td><td rowspan="3">VDD-VO</td><td>21.0</td><td>22.0</td><td>23.0</td></tr>
<tr><td>20.0</td><td>21.0</td><td>22.0</td></tr>
<tr><td>19.0</td><td>20.0</td><td>21.0</td></tr>
</table></td></tr>
<tr><td></td><td>7B64PS 2705-
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Page 5-2/2</td><td>5.2 ELECTRICAL CHARACTERISTICS OF BACKLIGHT
Canceled
Note 5:When ICFL is used over 5.5 mA ,it may cause uneven contrast near CFL location ,due to heart dispersion from CFL.</td></tr>
<tr><td></td><td>7B64PS 2706-
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Page 6-2/2</td><td>6.2 OPTICAL CHARACTERISTICS OF BACKLIGHT
Added
The LCD driving voltage should be adjusted at the voltage where the peak contrast is obtained.</td></tr>
<tr><td></td><td>7B64PS 2710-
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Revised 45°→25°</td></tr>
<tr><td></td><td>7B64PS2714
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<tr><td>May.13,'08</td><td>7B64PS2714
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Page 14-1/4</td><td>14.1.2 OPERATING CONDITIONS
Changed
<table>
<tr><th>ITEM</th><th>SPECIFICATION</th></tr>
<tr><td>Actuation Force</td><td>80g max. (R8,Silicone rubber)</td></tr>
</table>
↓
<table>
<tr><th>ITEM</th><th>SPECIFICATION</th></tr>
<tr><td>Actuation Force</td><td>1.2N max. (R8,Silicone rubber)</td></tr>
</table></td></tr>
<tr><td>Mar.06,'09</td><td>7B64PS 2712
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Revised reversion from REV. — to REV.B</td></tr>
</table>

3. GENERAL SPECIFICATIONS

(1)	Part Name	SP14Q002-C1A
(2)	Module Size	167.0(W)mm×109.0(H)mm×11.4(D)mm(max.)
(3)	Effective Display Area	120(W)mm min. × 89(H)mm min.
(4)	Dot Size	0.345(W)min. × 0.345(H)min.
(5)	Dot Pitch	0.360(W)mm × 0.360(H)mm
(6)	Dot Number (Resolution)	320 (W) × 240 (H) dots
(7)	Duty Ratio	1/240
(8)	LCD Type	Transmissive type F-STN With glare type upper polarizer
(9)	Viewing Direction	6 O'clock
(10)	Back Light Type	Cold cathode fluorescent lamp. CFL life time : 50,000h(average) Note : CFL life time = life time for half of CFL brightness.
(11)	Touch Panel	Analog resistive Transparency：76% min. Surface type：anti glare

4. ABSOLUTE MAXIMUM RATINGS

4.1 ELECTRICAL ABSOLUTE MAXIMUM RATINGS

VSS=0V : STANDARD

ITEM	SYMBOL	MIN.	MAX.	UNIT	COMMENT
Power Supply for Logic	VDD-VSS	0	6.0	V	
Power Supply for LC Driving	VDD-VEE	0	27.5	V	
Input Signal Voltage	Vi	-0.3	VDD+0.3	V	Note 1
Input Signal Current	Ii	0	1	A	
Static Electricity	VESD0	-	±100	V	Note 2,3,4
	VESD1	-	±10	kV	Note 2,3,5

Note 1 $\overline{\text{DISP.OFF}}$, FRAME , LOAD , CP , D0~D3.
Note 2 Make certain you are grounded when handling LCM.
Note 3 Energy storage capacitance 200pF , discharge resistance 250Ω Ta=25℃ , 60%RH.
Note 4 Contact discharge to I/F connector pins.
Note 5 Contact discharge to front metal bezel.

4.2 ENVIRONMENTAL ABSOLUTE MAXIMUM RATINGS

ITEM	OPERATING		STORAGE		OMMNT
	MIN.	MAX.	MIN.	MAX.	
Ambient Temperature	-20℃	70℃	-30℃	80℃	Note 2,3,7
Humidity	Note 1		Note 1		Without Condensation
Vibration	-	2.45m/s^2 (0.25G)	-	11.76m/s^2 (1.2G) Note 5	Note 4 1h max.
Shock	-	29.4m/s^2 (3 G)	-	490.0m/s^2 (50 G) Note 5	X、Y、Z Directions
Corrosive Gas	Not Acceptable		Not Acceptable		

Note 1 Ta≦40℃ : 85%RH max.
Ta>40℃ : Absolute humidity must be lower than the humidity of 85%RH at 40℃
Note 2 Ta at -30℃ ------< 48h , at 80℃ < 168h.
Note 3 Background color changes slightly depending on ambient temperature.
This phenomenon is reversible.
Note 4 5Hz~100Hz (Except resonance frequency)
Note 5 This module should be operated normally after finish the test.
Note 6 When LCM will be operated at 0℃ , the life time of CFL will be reduced.
Please make sure that characteristics of the inverter meet the CFL specification.
Note 7 Operation temp not include CFL & touch panel.

5. ELECTRICAL CHARACTERISTICS

5.1 ELECTRICAL CHARACTERISTICS

I T E M	SYMBOL	CONDITION	MIN.	TYP.	MAX.	UNIT
Power Supply Voltage for Logic	VDD-VSS	-	4.75	5.0	5.25	V
			3.2	3.3	3.4	
Power Supply Voltage for LC Driving	VEE-VSS	-	-23.1	-22.0	-20.9	V
Input Signal Voltage Note 1	Vi	H LEVEL	0.8VDD	-	VDD	V
		L LEVEL	0	-	0.2VDD	V
Power Supply Current for Logic Note 2	IDD	VDD-VSS=5.0V VEE-VSS= -22.0V	-	6.0	-	mA
Power Supply Current for LC Driving Note 2	IEE	VDD-VSS=5.0V VEE-VSS= -22.0V	-	5.0	-	mA
Recommended LC Driving Voltage Note 3	VDD-V0	Ta= 0℃ , ϕ= 0°	21.0	22.0	23.0	V
		Ta=25℃ , ϕ= 0°	20.0	21.0	22.0	V
		Ta=50℃ , ϕ= 0°	19.0	20.0	21.0	V
FRAME Frequency Note 4	fFRAME	-	70	75	80	Hz

Note 1 $\overline{\text{DISP.OFF}}$, FRAME , LOAD , CP , D0~D3.

Note 2 : FLM=75Hz , test pattern is all “Q”.
VDD-V0=21.0V , Ta=25℃

Note 3 : Recommended LC driving voltage may fluctuate about ±1.0V by each module.
Test pattern is all “Q”

Note 4 : Please set the frame frequency so as to avoid flicker and rippling on the display.

5.2 ELECTRICAL CHARACTERISTICS OF BACKLIGHT

ITEM	SYMBOL	MIN.	TYP.	MAX.	UNIT	NOTE
Lamp Voltage	VL	-	(300)	-	Vrms	Ta=25℃
Frequency	fL	-	70	85	kHz	Ta=25℃
Lamp Current	IL	4	5	6	mArms	Ta=25℃
Starting Discharge Voltage	VS	1000	-	-	Vrms	Ta=25℃

Note 1 : Please make sure that your inverter is designed to meet the above specifications.

Note 2 : Starting discharge voltage is increased when LCM is operating at lower temperature , please check the characteristics of your inverter , so as to ensure discharge at low temperature.

Note 3 : Average life time of CFL will be decreased when LCM is operating at lower temperature.

Note 4 : Lower driving frequency of CFL inverter may cause mechanical noise of the backlight system .
Before designing the inverter , please consider the driving frequency of noise .

6. OPTICAL CHARACTERISTICS

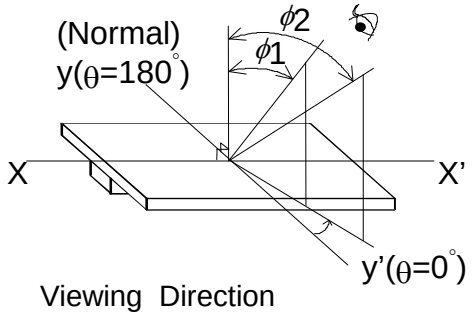
6.1 OPTICAL CHARACTERISTICS OF LCD

Ta=25℃(Backlight on)

ITEM	SYMBOL	CONDITIONAL	MIN.	TYP.	MAX.	UNIT	NOTE
Viewing Area	$\phi 2$-$\phi 1$	K≧2.0	-	40	-	deg	1,2
Contrast Ratio	K	ϕ=0° , θ=0°	-	25	-	-	3
Response Time (Rise)	tr	ϕ=0° , θ=0°	-	336	-	ms	4
Response Time (Fall)	tf	ϕ=0° , θ=0°	-	148	-	ms	4

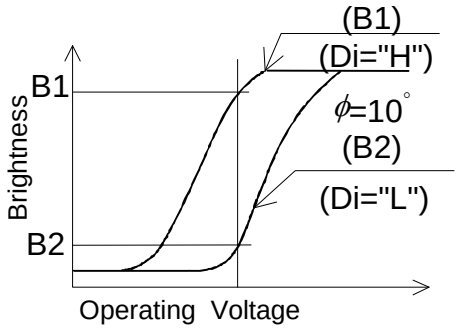
(Measure condition by HITACHI)

Note 1. Definition of θ and ϕ

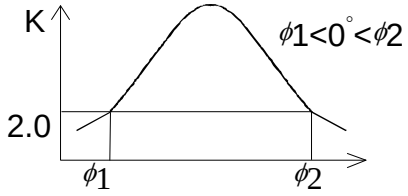


Note 3. Definition of contrast "K"

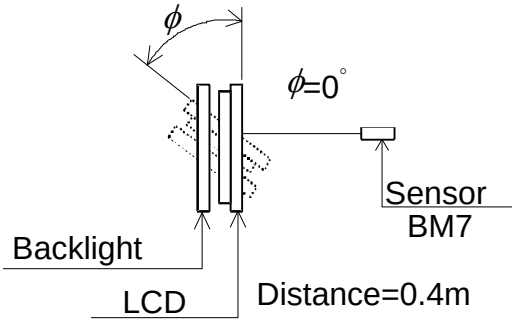
$$K=\frac{\text{Brightness on selected dot (B1)}}{\text{Brightness on non-selected dot (B2)}}$$



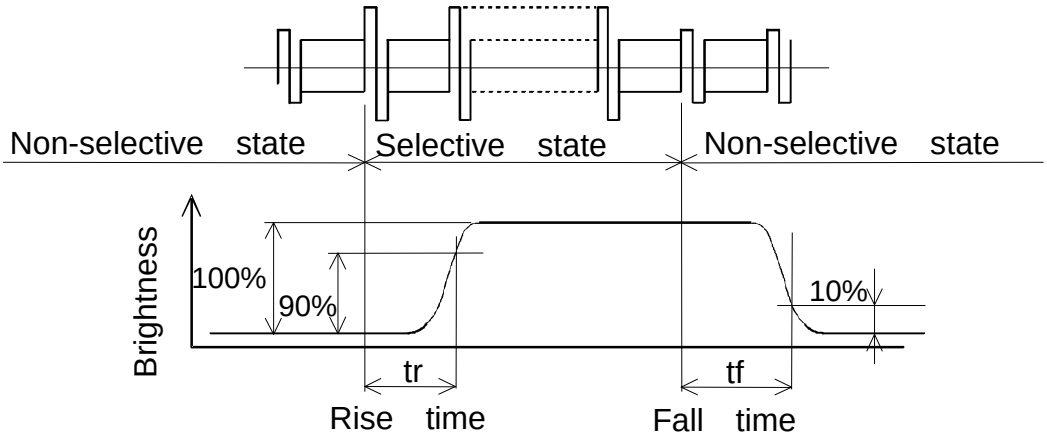
Note 2. Definition of viewing angle $\phi 1$ and $\phi 2$.



Contrast ratio K vs viewing angle ϕ



Note 4. Definition of optical response



6.2 OPTICAL CHARACTERISTICS OF BACKLIGHT

I T E M	MIN.	TYP.	MAX.	UNIT	NOTE
Brightness	-	110	-	cd/m^2	IL=5mA Note 1,2
Rise Time	-	5	-	minute	IL=5mA Brightness 80%
Brightness Uniformity	-	-	±30	%	Note 1,3

CFL : Initial, Ta=25℃

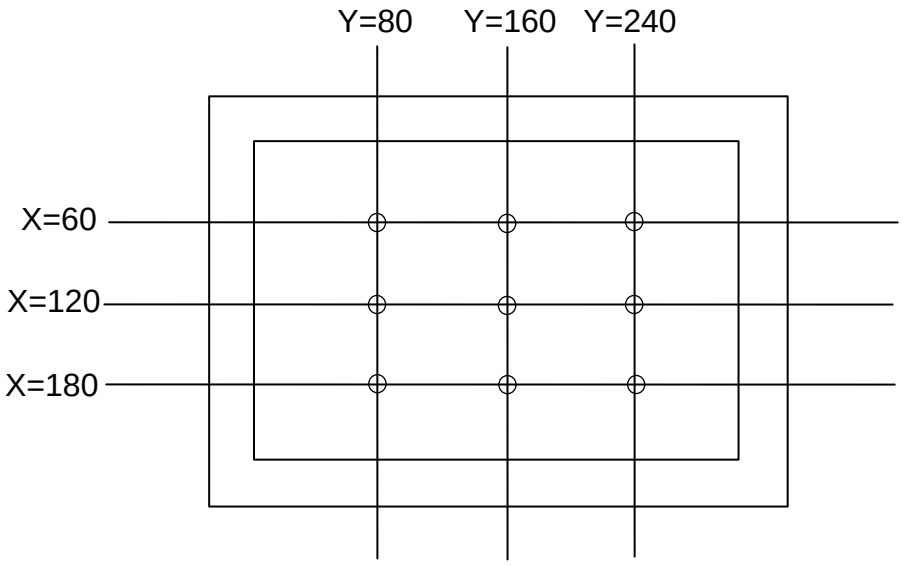
Display data should be all “ON”.

The LCD driving voltage should be adjusted at the voltage where the peak contrast is obtained.

Note 1 Measurement after 10 minutes of CFL operating.

Note 2 Brightness control : 100%

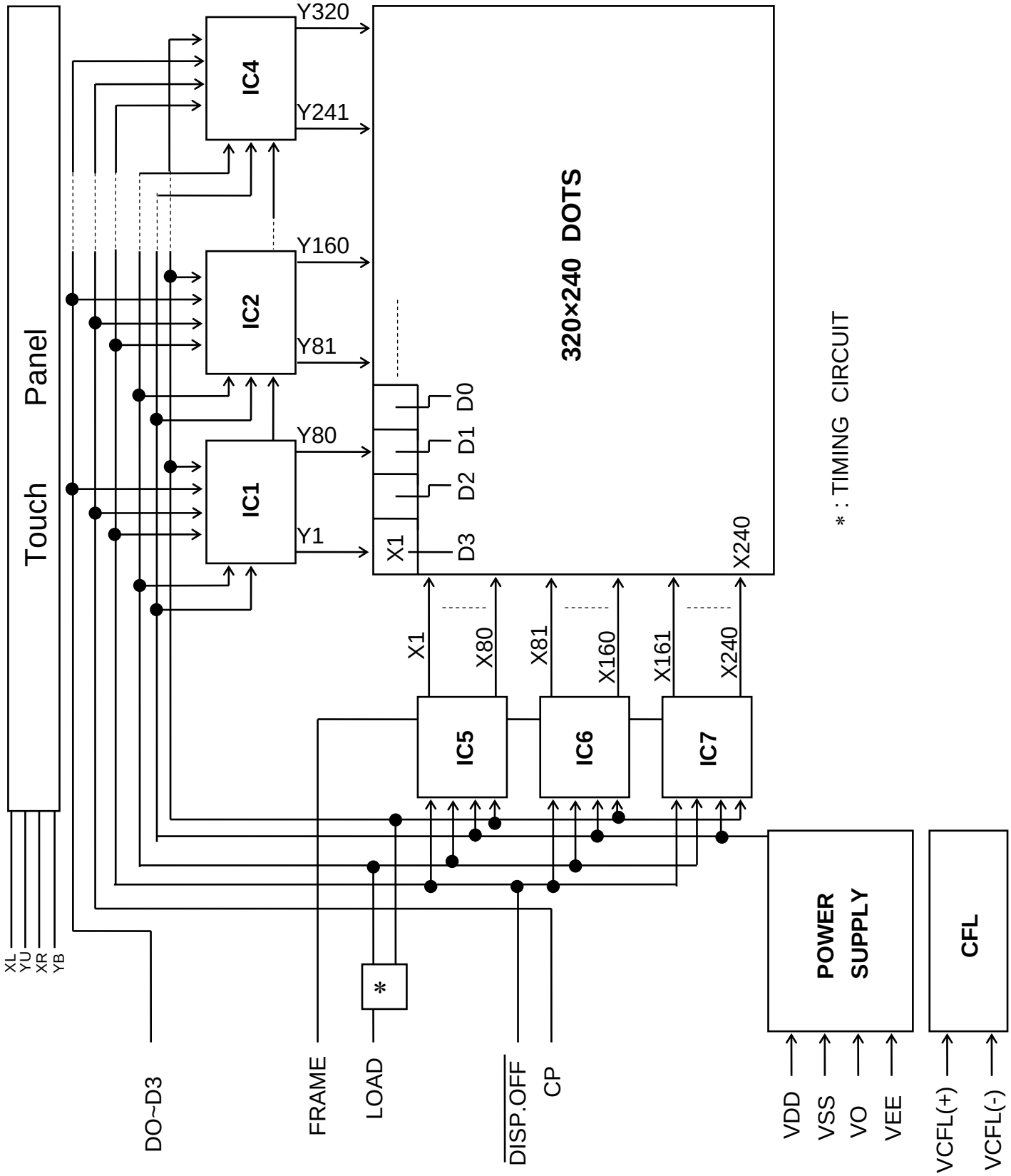
Note 3 Measure of the following 9 places on the display.



Definition of the brightness tolerance.

$$\left(\frac{\text{max. or min. Brightness - Average Brightness}}{\text{Average Brightness}} \right) \text{x } 100\%$$

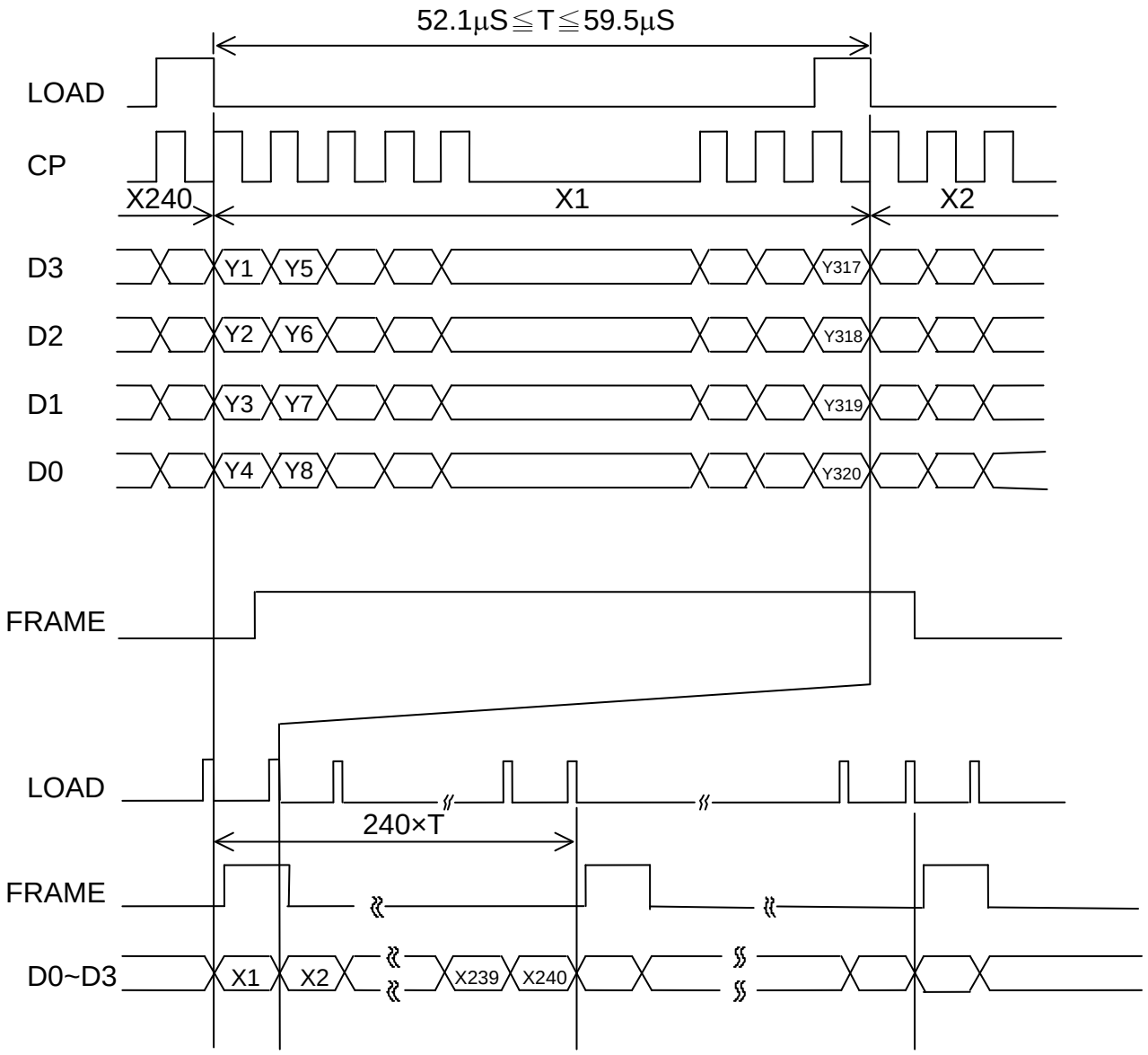
7. BLOCK DIAGRAM



KAOHSIUNG HITACHI ELECTRONICS CO.,LTD.	DATE	Mar.06,'09	Sh. No.	7B64PS 2707-SP14Q002-C1A-11	PAGE	7-1/1

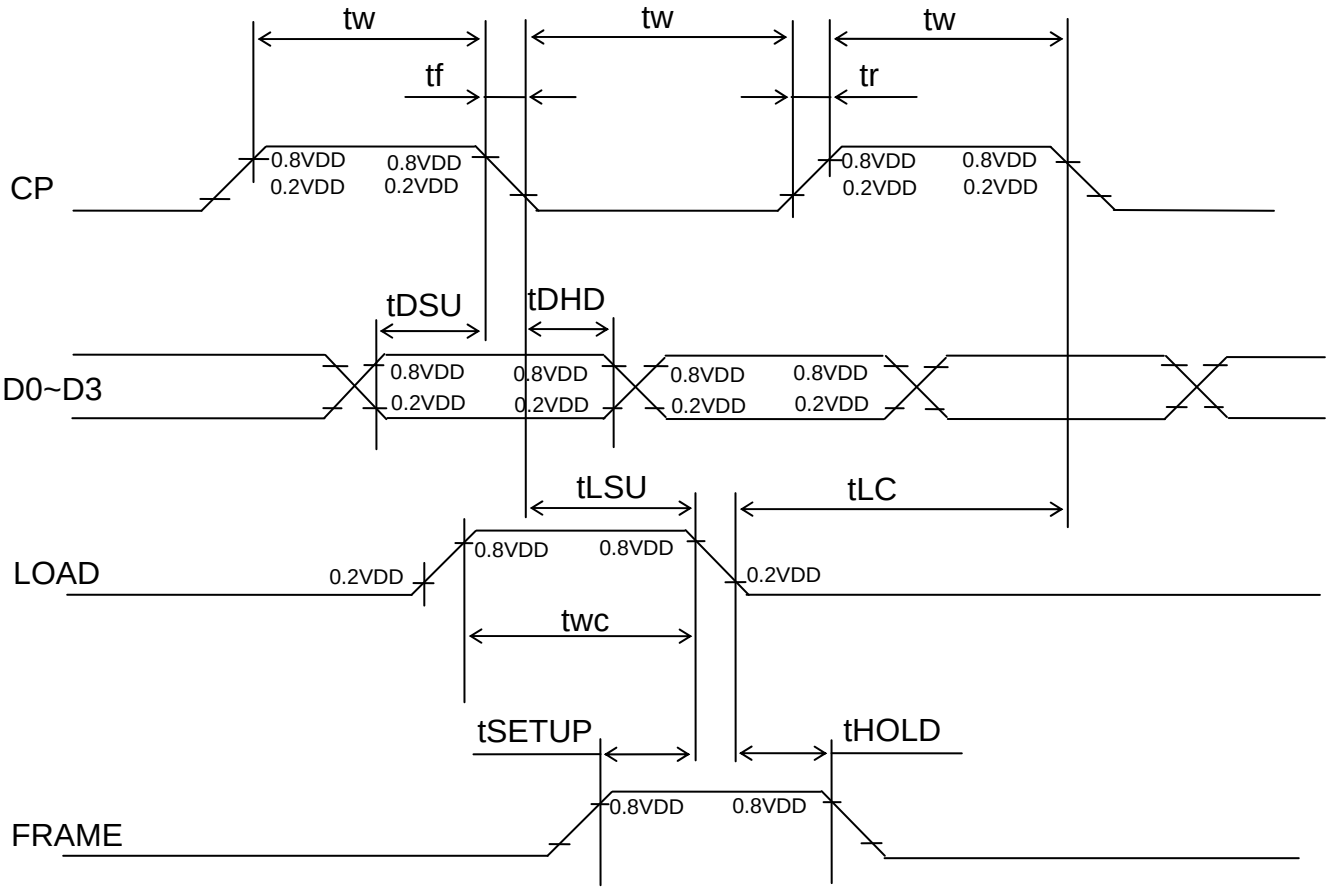
8. INTERFACE TIMING CHART

8.1 INTERFACE TIMING CHART

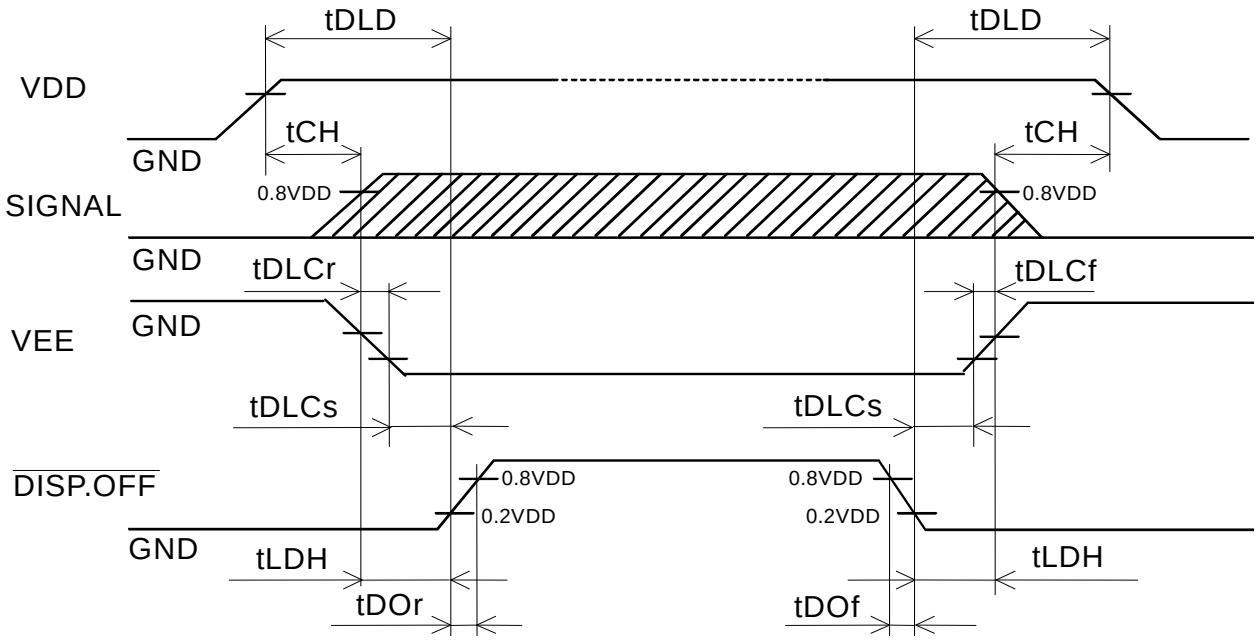


8.2 TIMING CHARACTERISTICS

ITEM	SYMBOL	MIN.	TYP.	MAX.	UMIT
Clock frequency	fCP	-	-	6.5	MHz
Clock pulse width	tW	45	-	-	ns
Clock rise, fall time	tr , tf	-	-	15	ns
Data set up time	tDSU	30	-	-	ns
Data hold time	tDHD	30	-	-	ns
Load set up time	tLSU	80	-	-	ns
Load clock time	tLC	120	-	-	ns
“FRAME” set up time	tSETUP	100	-	-	ns
“FRAME” hold time	tHOLD	100	-	-	ns
“LOAD” pulse width	tWC	125	-	-	ns



8.3 POWER ON/OFF TIMING SEQUENCE

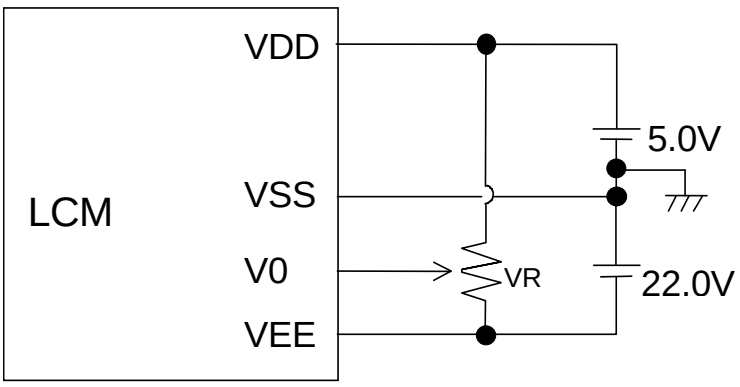


SYMBOL	MIN.	MAX.	UNIT	COMMENT
tDLD	50	-	ms	(NOTE 1)
tCH	0	30	ms	
tLDH	0	-	ms	
tDOr	-	100	ns	(NOTE 2)
tDOf	-	100	ns	
tDLCr	0	-	ms	
tDLCf	0	-	ms	
tDLCs	20	-	ms	

Note 1 Please keep the specified sequence because wrong sequence may cause permanent damage to the LCD panel.

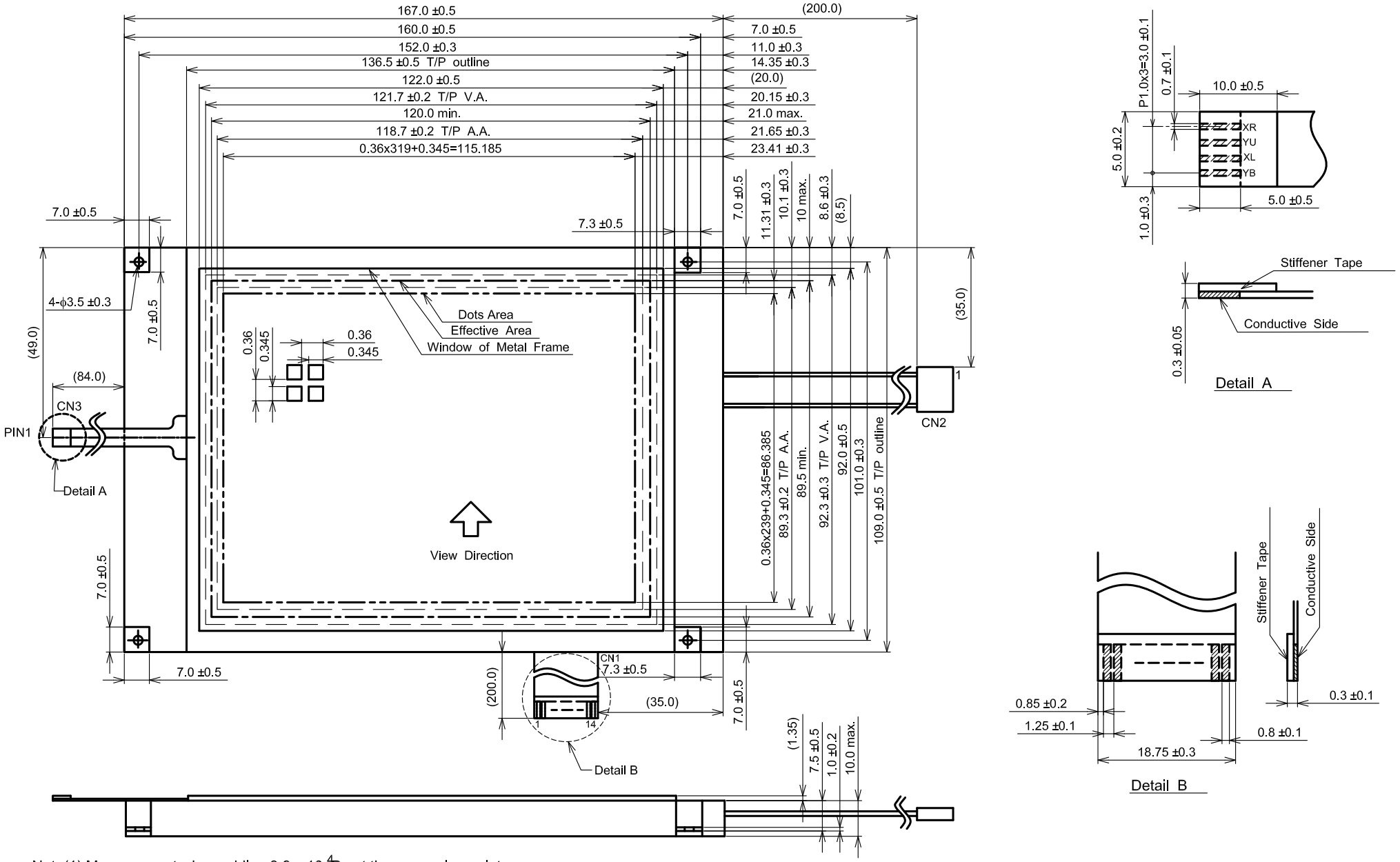
Note 2 HITACHI recommends you to use $\overline{\text{DISP.OFF}}$ function. display quality may deteriorate if you don't use $\overline{\text{DISP.OFF}}$ function.

8.4 POWER SUPPLY FOR LCM (EXAMPLE)



Note 1 : VR : 10kΩ

9. OUTLINE DIMENSIONS
9.1 OUTLINE DIMENSIONS



Detail A

Detail B

Note(1) Measurement when adding 9.8 x 10^4 Pa at the measuring point.

Scale : NTS
Unit : mm

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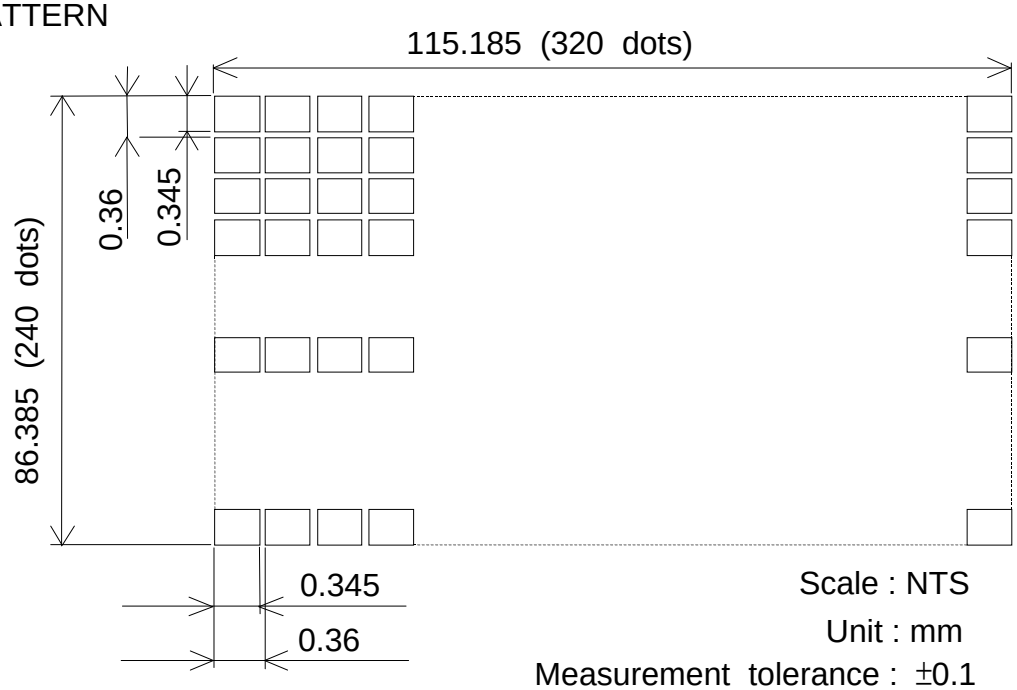
9.2 DISPLAY PATTERN



Scale : NTS

Unit : mm

Measurement tolerance : ±0.1

9.3 INTERFACE PIN CONNECTION

FPC：pitch 1.25mm 14 pins

INTERFACE		PIN No.	SIGNAL	LEVEL	FUNCTION
LCM	CN1	1	D0	H/L	Display Data
		2	D1		
		3	D2		
		4	D3		
		5	$\overline{\text{DISP.OFF}}$	H/L	H : ON / L : OFF
		6	FRAME	H	First Line Marker
		7	N.C	-	-
		8	LOAD	H→L	Data Latch
		9	CP	H→L	Data Shift
		10	VDD	-	Power Supply for Logic
		11	VSS	-	GND
		12	VEE	-	Power Supply for LC
		13	V0	-	Operating Voltage LC Driving
		14	VSS	-	GND

INTERFACE		PIN No.	SIGNAL	LEVEL	FUNCTION
LCM	CN2	1	VCFL(+)	-	Power Supply for CFL
		2	N.C	-	-
		3	N.C	-	-
		4	VCFL(-)	-	CFL GND

CFL I/F : J.A.E./ IL – G – 4S –S3C2

FPC : pitch 1.0mm 4pins

INTERFACE		PIN No.	SIGNAL	FUNCTION
T/P	CN3	1	XR	Analog Signal from Digitizer Right
		2	YU	Analog Signal from Digitizer Up
		3	XL	Analog Signal from Digitizer Left
		4	YB	Analog Signal from Digitizer Bottom

Recommend suitable connector : (HIROSE) FH12-10(4)SA-ISH

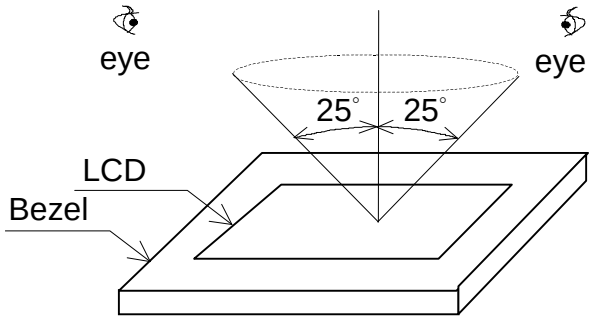
10. APPEARANCE STANDARD

10.1 APPEARANCE INSPECTION CONDITION

Visual inspection should be done under the following condition.

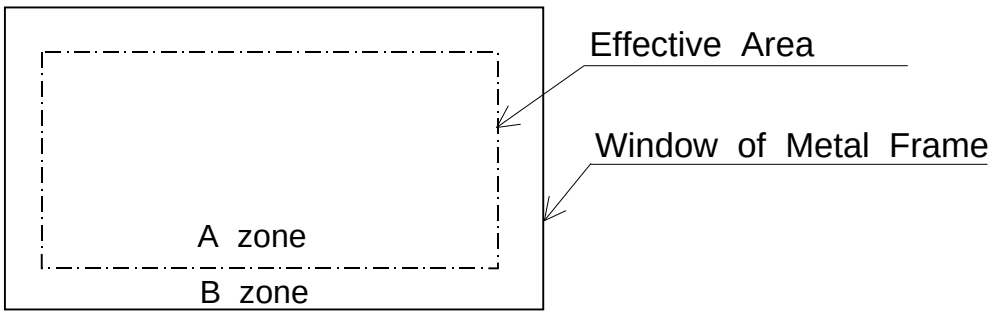
(1) The inspection should be done under in the dark room.
(2) The CFL should be lighted with the prescribed inverter.
(3) The distance between eyes of an inspector and the LCD module is 25cm.
(4) The viewing zone is shown the figure .

Viewing angle $\leqq 25^{\circ}$



10.2 DEFINITION OF EACH ZONE

A zone : Within the effective area specified at page 9-1/2 of this document.
B zone : Area between the window of metal frame and the effective area line specified at page 9-1/2 of this document.



10.3 APPEARANCE SPECIFICATION

*) If a problem occurs in respect to any of these items , both parties (Customer and HITACHI) will discuss in more detail.

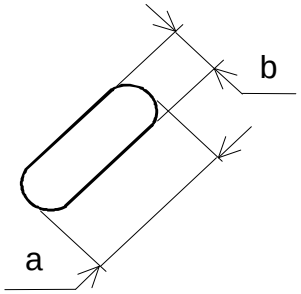
No.	ITEM	CRITERIA					A	B
L C D	Scratches	Distinguished one is not acceptable (To be judged by HITACHI limit sample)					*	-
	Dent	Same as above					*	-
	Wrinkles in Polarizer	Same as above					*	-
	Bubbles	Average diameter D(mm)		Maximum number acceptable				
		D≦0.2		Ignore				
		0.2<D≦0.3		12			○	-
		0.3<D≦0.5		3				
		0.5<D		None				
	Stains, Foreign Materials, Dark spot	Filamentous						
		Length L(mm)	Width W(mm)	Maximum number acceptable			○	-
		L≦2.0	W≦0.03	Ignore				
		L≦3.0	0.03<W≦0.05	6				
		L≦2.5	0.05<W≦0.1	1				
		Round						
		Average diameter D(mm)	Maximum number acceptable	Minimum space				
		D<0.2	Ignore	-			○	-
		0.2 ≦D<0.33	8	10mm				
		0.33≦D	None	-				
		Total	Filamentous + Round = 10					
		Those wiped out easily are acceptable					○	○
	Color Tone	To be judged by HITACHI limit sample					○	-
	Color Uniformity	Same as above					○	-
	Pinhole	Average diameter D(mm)		Maximum number acceptable				
		D≦0.15		Ignore				
		0.15<D≦0.3		10				
		C≦0.015		ignore				
	Contrast Irregularity (Spot)	Average diameter D(mm)	Contrast	Maximum number acceptable	Minimum space		○	-
		D≦0.25	To be judged by HITACHI	Ignore	-			
		0.25<D≦0.35		10	20mm			
		0.35<D≦0.5		4	20mm			
		0.5 <D		None	-			

No.	ITEM	CRITERIA				A	B
LCD	Contrast Irregularity (Line) (Filamentous)	Width D(mm)	Length L(mm)	Maximum number acceptable	Minimum space	○	-
		W≦0.25	L≦1.2	2	20mm		
		W≦0.2	L≦1.5	3	20mm		
		W≦0.15	L≦2.0	3	20mm		
		W≦0.1	L≦3.0	4	20mm		
		TOTAL		6			
	Rubbing Scratch	To be judged by HITACHI standard				○	-

No.	ITEM	CRITERIA		
CFL B/L	Dark Spots, White Spots	D≦0.4		Ignore
	Foreign Materials (Spot)	D>0.4		None
	Foreign Materials (Line)	W≦0.2	L<2.5	≦1
		W≦0.2	L>2.5	None
		W>0.2		None
	Scratches	W≦0.1		Ignore
		0.1<W≦0.2	L≦11.0	≦1
		0.1<W≦0.2	L≧11.0	None
		W<0.2		None

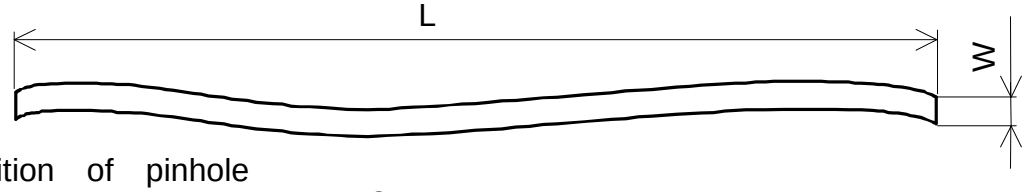
Note

(1) Definition of average diameter D

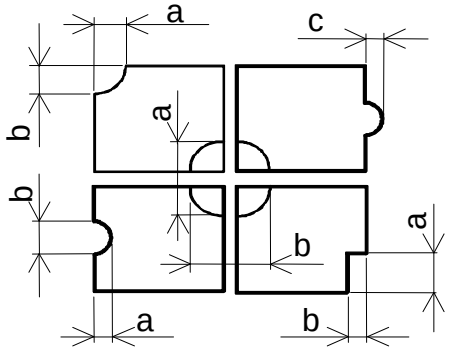


$$D = \frac{a+b}{2}$$

(2) Definition of length L and width W



(3) Definition of pinhole



c : Salience

11. PRECAUTION IN DESIGN

11.1 LC DRIVING VOLTAGE (VEE) AND VIEWING ANGLE RANGE

Setting VEE out of the recommended condition will be a cause for a change of viewing angle range.

11.2 PRECAUTIONS AGAINST STATIC CHARGE

As this module contains C-MOS LSIs , it is not strong against electrostatic discharge. Make certain that the operator's body is connected to the ground through a list band etc. And don't touch I/F pins directly.

11.3 POWER ON SEQUENCE

Input signals should not be applied to LCD module before power supply voltage is applied and reaches to specified voltage (VDD).

If above sequence is not kept, C-MOS LSIs of LCD modules may be damaged due to latch up phenomenon.

11.4 PACKAGING

(1) No leaving product is preferable in the place of high humidity for a long period of time. For their storage in the place where temperature is 35 °C or higher, special care to prevent them from high humidity is required. A combination of high temperature and high humidity may cause them polarization degradation as well as bubble generation and polarizer peel-off. Please keep the temperature and humidity within the specified range for use and storage.

(2) Since polarizers tend to be easily damaged, They should be handled full with care so as not to get them touched, pushed or rubbed.

(3) As the adhesives used for adhering polarizers are made of organic substances which will be deteriorated by a chemical reaction with such chemicals as acetone, toluene, ethanol and isopropyl-alcohol. The following solvents are recommended for use:

Normal hexane

Please contact us when it is necessary for you to use chemicals.

(4) Lightly wipe to clean the dirty surface with absorbent cotton waste or other soft material like chamois , soaked in the chemicals recommended without scrubbing it hardly. To prevent the display surface from damage and keep the appearance in good state, it is sufficient, in general, to wipe it with absorbent cotton.

(5) Immediately wipe off saliva or water drop attached on the display area because its long period adherence may cause deformation or faded color on the spot.

(6) Foggy dew deposited on the surface due to coldness will be caused for polarizer damage, stain and dirt on product. When necessary to take out the products from some place at low temperature for test, etc. It is required for them to be warmed up in a container once at the temperature higher than that of room.

(7) Touching the display area and contact terminals with bare hands and contaminating them are prohibited, because the stain on the display area and poor insulation between terminals are often caused by being touched by bare hands. (Some cosmetics are detrimental to polarizers.)

(8) In general the quality of glass is fragile so that it tends to be cracked or chipped in handling, specially on its periphery. Be careful not to give it sharp shock caused by dropping down, etc.

11.5 CAUTION FOR OPAERATION

(1) It is an indispensable condition to drive LCDs within the specified voltage limit since the higher voltage than the limit causes the shorter LCD life. An electrochemical reaction due to direct current causes LCDs undesirable deterioration , so that the use of direct current driver should be avoided.

(2) Response time will be extremely delayed at lower temperature than the operating temperature range and on the other hand at higher temperature LCDs show dark blue color in them. However those phenomena do not mean malfunction or out of order with LCDs which will come back in the specified operating temperature range.

(3) If the display area is pushed hard during operation, some font will be abnormally displayed but it resumes normal condition after turning off once.

(4) A slight dew depositing on terminals is a cause for electrochemical reaction resulting in terminal open circuit. Usage under the relative condition of 40 °C 50%RH or less is required.

11.6 STORAGE

In case of storing for a long period of time (for instance , for years) for the purpose of replacement use , the following ways area recommended.

(1) Storage in a polyethylene bag with the opening sealed , so the fresh air will not be entered from outside .

(2) Placing in a dark place where neither exposure to direct sunlight nor light is , keeping temperature in the range from 0 degree C to 35 ˚C.

(3) Storing with no touch on polarizer surface by anything else. (It is recommended to store them as they have been contained in the inner container at the time of delivery from us.)

11.7 SAFETY

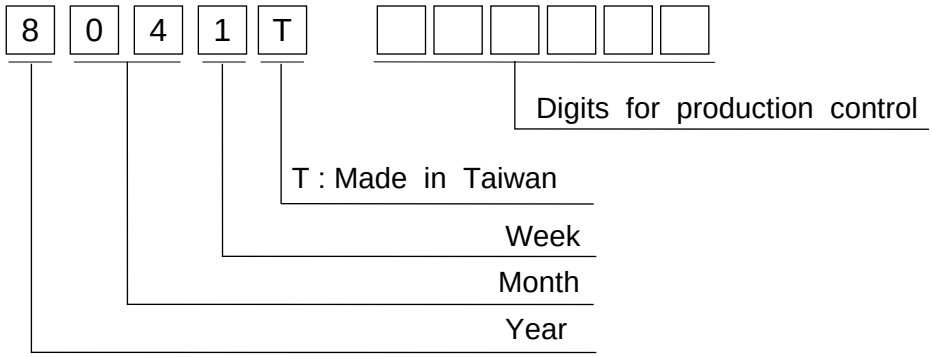
(1) It is recommendable to crash damaged or unnecessary LCDs into pieces and wash off liquid crystal by either of solvents such as acetone and ethanol , which should be burned up later.

(2) When any liquid leaked out of a damaged glass cell comes in contact with your hands , please wash it off well with soap and water.

12. DESIGNATION OF LOT MARK

12.1 LOT MARK

Lot mark is consisted of 5 digits for production lot and 6 digits for production control.



Year	Figure in lot mark
2009	9
2010	0
2011	1
2012	2
2013	3

Month	Figure in lot mark	Month	Figure in lot mark
Jan.	01	Jul.	07
Feb.	02	Aug.	08
Mar.	03	Sep.	09
Apr.	04	Oct.	10
May	05	Nov.	11
Jun.	06	Dec.	12

Week (day in calendar)	Figure in lot mark
1~ 7	1
8~14	2
15~21	3
22~28	4
29~31	5

12.2 SERIAL No.

Serial No. is consisted of 6 digits number (000001~999999).

12.3 LOCATION OF LOT MARK

Label is bring attached on the back side of module.

12.4 REVISION(Rev.) CONTROL

Rev No.	ITEM
_	Mcount IC:MN73099HED(Panasonic) Transistor:2SA1036K(ROHM)
B	Mcount IC:IT7001M(ITE) Transistor:2SA1576(ROHM)

13. PRECAUTION FOR USE

13.1 A limit sample should be provided by the both parties on an occasion when the both parties agreed its necessity. Judgment by a limit sample shall take effect after the limit sample has been established and confirmed by the both parties.

13.2 On the following occasions, the handling of the problem should be decided through discussion and agreement between responsible persons of the both parties.

(1) When a question is arisen in the specifications.

(2) When a new problem is arisen which is not specified in this specifications.

(3) When an inspection specifications change or operating condition change in customer is reported to HITACHI, and some problem is arisen in this specification due to the change.

(4) When a new problem is arisen at the customer‘s operating set for sample evaluation in the customer site.

The precaution that should be observed when handling LCM have been explained above. If any points are unclear or if you have any request , please contact HITACHI.

14. DIGITIZER TECHNICAL SPECIFICATION

14.1 RATINGS

14.1.1 ABSOLUTE MAXIMUM RATINGS

ITEM	SPECIFICATION	COMMENT
Operating Voltage	7V	Without Condensation
Contact Current	20mA	
Operating Temperature	0~50℃ 80%RH max.	
Storage Temperature	-20~70℃ 90%RH max.	

14.1.2 OPERATING CONDITIONS

ITEM	SPECIFICATION
Operating Voltage	5.0 / 3.3 VDC
Contact Current	10 ~ 20 mA
Actuation Force	1.2N max. (R8,Silicone rubber)

14.2 SURFACE HARDNESS

2H

14.3 OPTICAL CHARACTERISTICS

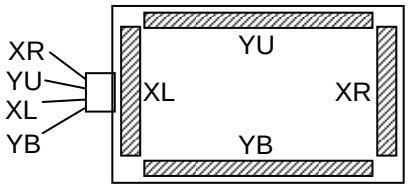
14.3.1 TRANSPARENCY : 76%.min.

14.3.2 WAVE LENGTH : 450 ~ 700nm

14.4 ELECTRICAL CHARACTISTICS

14.4.1 CONDUCTIVE RESISTANCE

TERMINAL	CONDUCTIVE RESISTANCE
XR-XL	150~1300Ω
YU-YB	150~1300Ω



14.4.2 INSULATION RESISTINCE

TERMINAL	INSULATION RESISTANCE	TESTING VOLTAGE
X-Y	20MΩ	25VDC

14.4.3 BOUNCE CHATTERING

10ms max.

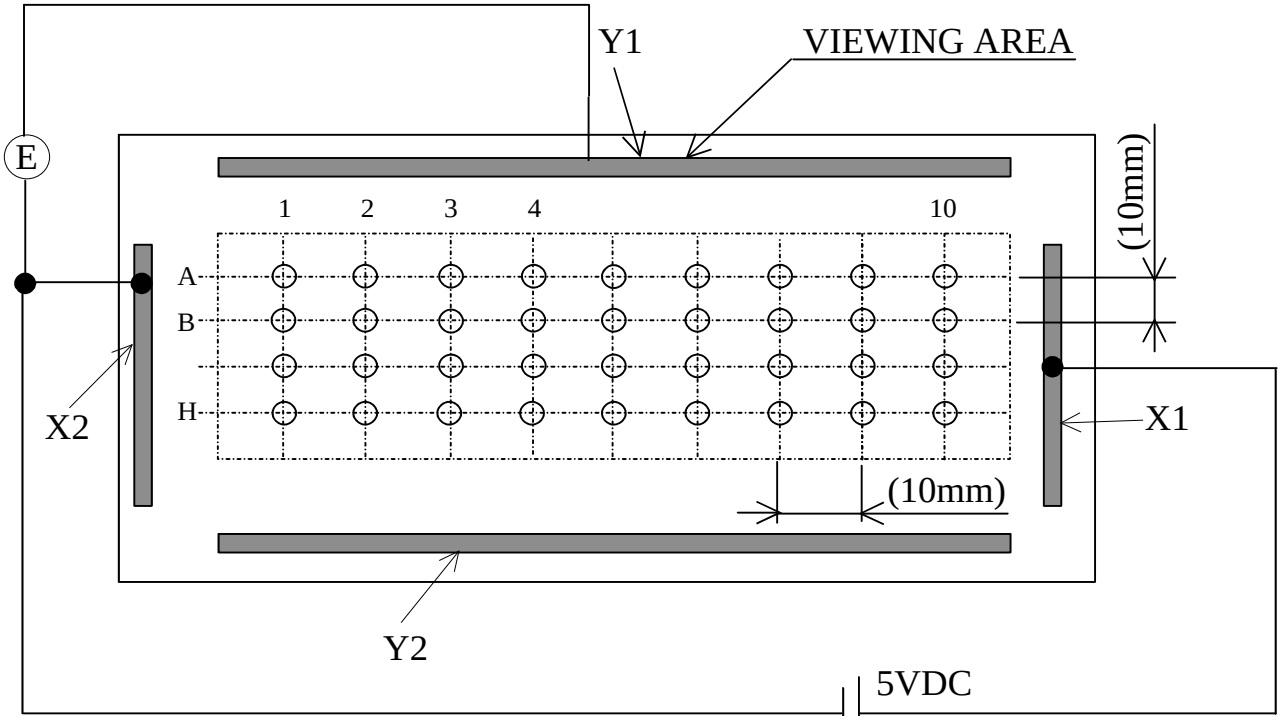
14.4.4 LINEARITY

(1) LINEARITY

Linearity Deviation : 2% max.

(2) TESTING CIRCUIT

(a) X axis linearity testing method , 100g ,VXR-VXL=5V , VOUT=VYU.

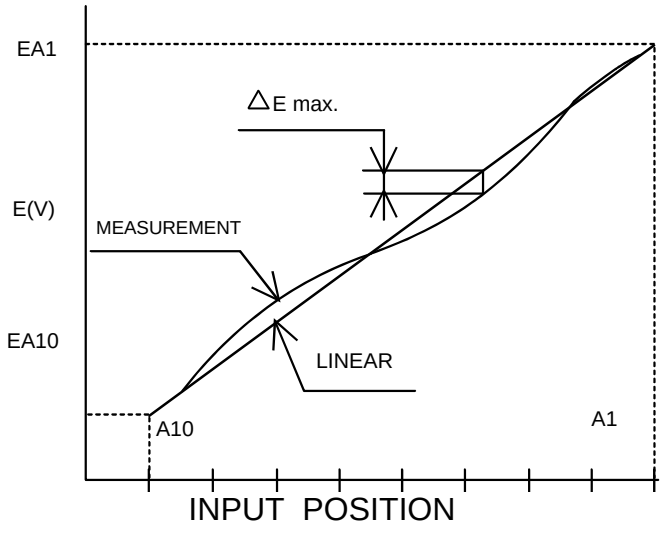


(b) Y axis linearity method , 100g VYU – VYB = 5V , VOUT = VXR

(3) CALCULATION

(a) X axis linearity

$$\text{LINEARITY} = \frac{\triangle \text{ E max.}}{\text{E A1} - \text{E A10}} \times 100(\%)$$



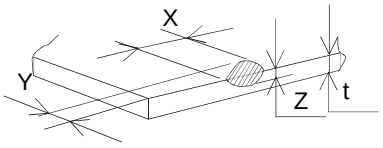
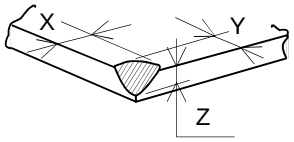
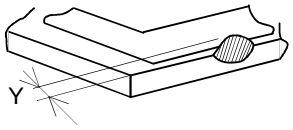
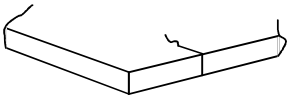
14.5 ENVIRONMENTAL TESTING

ITEM	CONDITIONS	CRITERIA
High Temperature Storage	60℃ : 120h & 25℃: 24h	After testing must to meet the specifications of the Electrical, Mechanical & Optical Characteristics.
Low Temperature Storage	-20℃ : 120h & 25℃ : 24h	
Temperature Cycle	-20℃ ←→ 70℃ : 10 Cycles within (30) (60) (30) : minutes & 25℃ : 24h (Without Condensation)	
Humidity Storage	60°C , 90%RH. 120h	
Durability for Keystroke	150g , R8, HS40 Silicon Rubber (Speed : 330mm/sec) : 1000000 Activations	

14.6 APPEARANCE SPECIFICATION

No.	ITEM	CRITERIA			A	B
T/P	Hair Flaws	FILAMENTOUS			O	-
		Length L(mm)	Width W(mm)	maximum number acceptable		
		L≦12	W≦0.05	Ignore		
		L≦5	0.05<W≦0.1	3		
		L>2	0.1 <W	None		
	Dot-shaped Impurities	Average diameter D(mm)		Maximum number acceptable	O	-
		D≦0.1		Ignore		
		0.1<D≦0.3		5		
		0.3<D		None		
	Scratch	FILAMENTOUS			O	-
		Length L(mm)	Width W(mm)	Maximum number acceptable		
		L≦12	W≦0.05	Ignore		
		L≦12	0.05<W≦0.1	5		
		L>12	0.1<W	None		

14.6.3 GLASS INDENTATION

ITEM	SPECIFICATIONS
Common Indentation	 X: ≦5.0, Y: ≦3.0, Z: ≦t But , indentation can not including seal area. t : Glass thickness.
Corner Broken	 X: ≦2.0, Y: ≦5.0, Z: ≦t But , indentation can not including seal area.
Indentation Within Pattern	 Y≦1 Is ignore. But , Must to meet the specification of conducting pattern indentation.
Proceeding Crack	 None

14.6.4 BLISTERING (PUFFINESS): 0.4mm max.