HITACHI

KAOHSIUNG HITACHI ELECTRONICS CO.,LTD P.O. BOX 26-27 2,13TH EAST ST. K.E.P.Z. KAOHSIUNG TAIWAN R.O.C. TEL:(07) 8215811 (7 LINE) FAX:(07) 8215815

FOR	MESSRS:	DATE: Mar.06,2009

CUSTOMER'S ACCEPTANCE SPECIFICATIONS

SP14Q006-ZZA CONTENTS

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14	TOUCH PANEL SPECIFICATION	7B64PS 2714- SP14Q006-ZZA -6	14-1/4~4/4

* When products will be discontinued, customers will be informed by HITACHI with twelve months prior announcement.

ACCEPTED BY;	PROPOSED BY; Jan Ching

KAOHSIUNG HITACHI	Sh.	7B64PS 2701- SP14Q006-ZZA -6	PAGE	1_1/1
ELECTRONICS CO.,LTD.	No.	7 DO41 3 2701- 31 14Q000-22A-0	I AGE	1-1/1

RECORD OF REVISION

DATE	SHEET No.	SUMMARY						
Mar.31,'04	7B64PS 2708- SP14Q006-ZZA-2 Page 8-3/3	8.3 POWER ON/OFF TIMING SEQUENCE Revised tDLD min. 200 → 50 Revised tCH max. 200 → 30						
Jun.04,'04	7B64PS 2705-	5.1 ELECTRICAL CHARACTERISTICS Added						
	SP14Q006-ZZA-3 Page 5-1/2	ITEM SYMBOL MIN. TYP. MAX						
	1 age 5-1/2	Power Supply Voltage Logic VDD-VSS 3.2 3.3 3.4						
		21.0 22.0 23.0						
		Recommend LC Driving Voltage VDD-V0 20.0 21.0 22.0						
		19.0 20.0 21.0						
	7B64PS 2706- SP14Q006-ZZA-3 Page 6-3/3	6.2 OPTICAL CHARACTERISTICS OF BACKLIGHT Added The LCD driving voltage should be adjusted at the voltage where the peak contrast is obtained.						
	7B64PS 2710- SP14Q006-ZZA-3 Page 10-1/3	10.1 APPEARANCE INSPECTION CONDITION Revised 45°→25°						
	7B64PS2711	14.1.2 OPERATING CONDITIONS						
	SP14Q006-ZZA-3	Revised Operating Voltage : 5VDC→5.0 /3.3 VDC						
	Page 14-1/4							
Jul.13,'07	7B64PS2703 SP14Q006-ZZA-4 Page 3-1/1	3. GENERAL SPECIFICATIONS Added (11) Backlight Type LED(Color : white)						
		(11) Backlight Type LED(Color : white) Life time : 40Kh @ 25°C Note : Life time for half of initial brightness						
	7B64PS2705 SP14Q006-ZZA-4 Page 5-2/2	5.2 ELECTRICAL CHARACTERISTICS OF LED BACKLIGHT Revised The control of finite of fini						
(AOHSIUN	G HITACHI	Sh. 7B64PS 2702- SP14Q006-ZZA -6 PAGE 2-1/2						

DATE Mar.06,'09

ELECTRONICS CO.,LTD.

7B64PS 2702- SP14Q006-ZZA -6 PAGE 2-1/2

RECORD OF REVISION SHEET No. DATE SUMMARY Jul.13,'07 12. DESIGNATION OF LOT MARK 7B64PS2712 Added SP14Q006-ZZA-4 REV No. ITEM Page 12-1/1 Backlight life time: 40kh May.13,'08 7B64PS 2714-14.1.2 OPERATING CONDITIONS Changed: SP14Q006-ZZA-5 Page 14 - 1/4 **SPECIFICATION** ITEM 80g max. (R8,Silicone rubber) Actuation Force **ITEM SPECIFICATION** Actuation Force 1.2N max. (R8,Silicone rubber) Mar.06,'09 12. DESIGNATION OF LOT MARK 7B64PS 2712-Revised reversion from REV. A to REV.B SP14Q006-ZZA-6 Page 12 - 1/1

KAOHSIUNG HITACHI			Sh.	 7B64PS 2702- SP14Q006-ZZA -6	DACE	2 2/2
ELECTRONICS CO.,LTD.	DATE	Mar.06,'09	No.	7664P3 2702- SP14Q006-2ZA -6	PAGE	Z-Z/Z

3. GENERAL SPECIFICATIONS

(1) Part Name SP14Q006-ZZA

(2) Outer Dimensions 167.0(W)mm×109.0(H)mm×11.4(D) mm max.

(3) Effective Area 120(W)mm min. $\times 89(H)$ mm min.

(4) Dot Size 0.345(W)min. × 0.345(H)min.

(5) Dot Pitch 0.360(W)mm × 0.360(H)mm

(6) Dot Number (Resolution) 320 (W) \times 240 (H) dots

(7) Duty Ratio 1/240

Transmissive type F-B/W STN (8) LCD Type

With glare type upper polarizer

(9) Viewing Direction 6 O'clock

Viewing Angle in Rear - Front (10) Viewing Angle

(12:00)(6:00)

R-F=90 °(Typ.)

LED(Color: white) (11) Backlight Type

Life time: 40Kh @ 25°C

Note: Life time for half of initial brightness

(12) Touch Panel Analog resistive

Transparency: 76% min.

Surface type: anti glare

4. ABSOLUTE MAXIMUM RATINGS

4.1 ELECTRICAL ABSOLUTE MAXIMUM RATINGS

ITEM	SYMBOL	MIN.	MAX.	UNIT	COMMENT
Power Supply for Logic	VDD-VSS	0	6.0	V	
Power Supply for LC DrivING	VDD-VEE	0	27.5	V	
Input Signal Voltage	Vi	-0.3	VDD+0.3	V	(Note 1)
Static Electricity	VESD0	-	±100	V	(Note 2,3,4)
	VESD1	-	±10	kV	(Note 2,3,5)

VSS=0V: STANDARD

Note 1 DOFF, FLM, CL1, CL2, D0~D3.

Note 2 Make certain you are grounded when handling LCM.

Note 3 Energy storage capacitance 200pF, discharge resistance 250 Ω Ta=25 $^{\circ}$ C, 60%RH.

Note 4 Contact discharge to I/F connector pins.

Note 5 Contact discharge to front metal bezel.

4.2 ENVIRONMENTAL ABSOLUTE MAXIMUM RATINGS

ITEM	OPERATING		STO	RAGE	COMMENT	
I I EIVI	MIN. MAX.		MIN. MAX.		COMMENT	
Ambient Temperature	-20 ℃	70 ℃	-30 ℃	80 °C	(Note 2,3,6,7)	
Humidity	(Not	e 1)	(No	te 1)	Without Condensation	
Vibration	-	2.45m/s ² (0.25G)	-	11.76m/s ² (1.2G) (Note 5)	(Note 4) 1h max.	
Shock	-	29.4m/s ² (3 G)	-	490.0m/s ² (50 G) (Note 5)	X、Y、Z Directions	
Corrosive Gas	Not Acc	ceptable	Not Ac	ceptable		

Note 1 : Ta ≤ 40°C : 85%RH max.

Ta>40°C: Absolute humidity must be lower than the humidity of 85%RH at 40°C

Note 2 : Ta at -30° C ---< 48h , at 80° C ---< 168h.

Note 3: Background color changes slightly depending on ambient temperature. This phenomenon is reversible.

Note 4:5Hz~100Hz (Except resonance frequency)

Note 5: This module should be operated normally after finish the test.

Note 6: The response time will be slower under low temperature.

Note 7: Operation temp not include touch panel.

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ELECTRONICS CO.,LTD.			No.		1 7.02	T 1/ 1

5. ELECTRICAL CHARACTERISTICS

5.1 ELECTRICAL CHARACTERISTICS

ITEM	SYMBOL	CONDITION	MIN.	TYP.	MAX.	UNIT	COMMENT
Power Supply Voltage	VDD-VSS	_	4.75	5.0	5.25	V	
for Logic	VDD-V33	-	3.2	3.3	3.4	V	
Power Supply Voltage for LC Driving	VEE-VSS	-	-23.1	-22.0	-20.9	V	
Input Signal Voltage1	Vi	H LEVEL	0.8VDD	-	VDD	V	(Note1)
	VI	L LEVEL	0	-	0.2VDD	V	(Note I)
Power Supply Current	IDD	VDD-VSS=5.0V		6.0	-	mA	(Note2)
for Logic	טטו	VEE-VSS= -22.0V	-				(INOLEZ)
Power Supply Current	IEE	VDD-VSS=5.0V		5.0		mA	(Note2)
for LC Driving	ICC	VEE-VSS= -22.0V	-	5.0	-	ША	(NOIEZ)
Recommended LC		Ta= 0° C , $\phi = 0^{\circ}$	21.0	22.0	23.0	V	
Driving Voltage	VDD-V0	Ta=25 $^{\circ}$ C , ϕ = 0 $^{\circ}$	20.0	21.0	22.0	V	(Note3)
		Ta=50°C, $\phi = 0$ °	19.0	20.0	21.0	V	
Frame Frequency	fFLM	-	70	75	80	Hz	(Note4)

Note 1: DOFF, FLM, CL1, CL2, D0~D3.

Note 2 : FLM=75Hz , test pattern is all "Q". VDD-V0=21.0V , Ta=25 $^{\circ}\mathrm{C}$

Note 3 : Recommended LC driving voltage may fluctuate about $\pm 1.0 V$ by each module. Test pattern is all "Q"

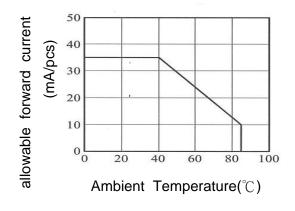
Note 4: Please set the frame frequency so as to avoid flicker and rippling on the display.

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5.2 ELECTRICAL CHARACTERISTICS OF LED BACKLIGHT

ITEM	SYMBOL	CONDITION	MIN.	TYP.	MAX.	UNIT	COMMENT
Power Supply Voltage for LED	VLED	1	1	5.0	5.2	٧	
Power Supply Current for LED	ILED	VLED=5.0V	-	160	-	mA	(Note 1)

Note 1: The ILED changes depending on ambient temperature.



6. OPTICAL CHARACTERISTICS

6.1 OPTICAL CHARACTERISTICS

Ta=25°C (Backlight on)

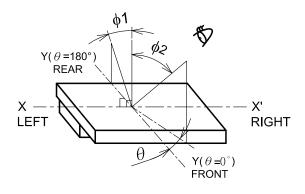
ITEM	SYMBOL	CONDITION	MIN.	TYP.	MAX.	UNIT	NOTE
Viewing Area	-	K≧2.0 θ=0°	-	90	-	deg.	1
Viewing 741ca	ı	K≧2.0 <i>θ</i> =90°	-	80		deg.	1
Contrast Ratio	K	φ=0°, θ=0°	-	25	-	ı	2,3
Response Time (Rise)	tr	$\phi = 0^{\circ}, \ \theta = 0^{\circ}$	-	(336)	ı	ms	4
Response Time (Fall)	tf	φ=0°, θ=0°	-	(148)	-	ms	4

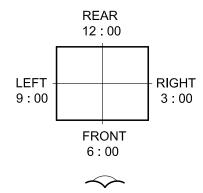
Note 1 : Definition of θ and f

(Measure condition by Hitachi)

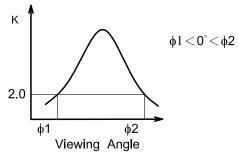
(Normal)

Viewing direction





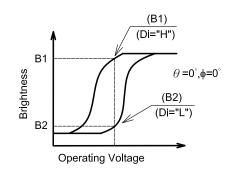
Note 2 : Definition of viewing angle ϕ 1 and ϕ 2

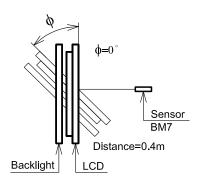


Contrast ratio K vs viewing angle ϕ

Note 3: Definition of contrast"K"

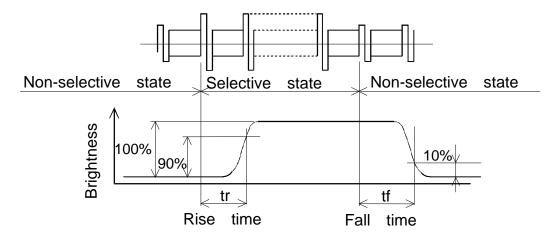
K= Brightness on selected dot (B1)
Brightness on non-selected dot (B2)





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Note 4: Definition of optical response



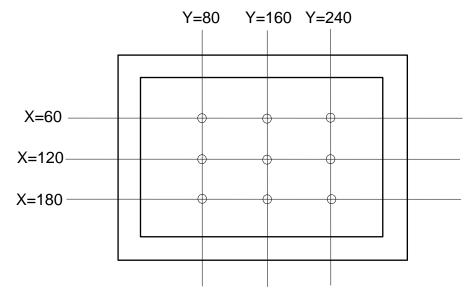
6.2 OPTICAL CHARACTERISTICS OF BACKLIGHT

ITEM	MIN.	TYP.	MAX.	UNIT	NOTE
Brightness	-	110	-	cd/m²	ILED=160mA
Brightness Uniformity	-	-	±30	%	(Note 1)

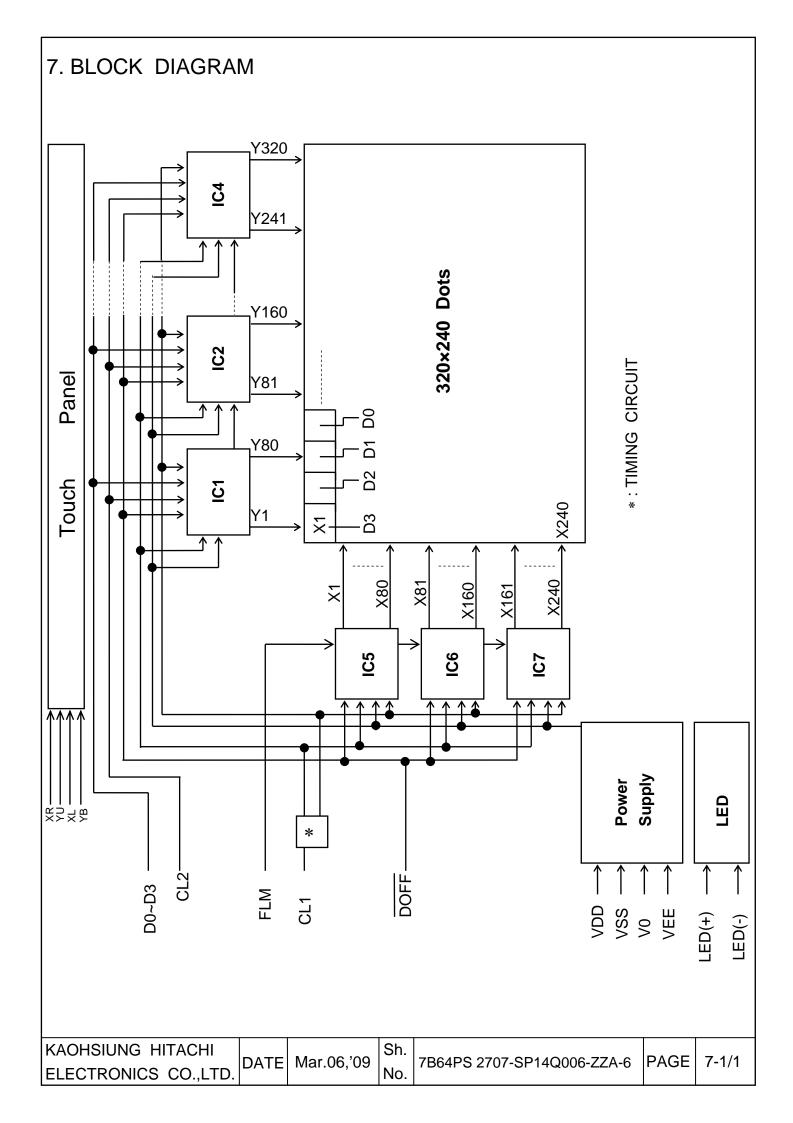
Ta=25°C, Display data should be all "ON".

The LCD driving voltage should be adjusted at the voltage where the peak contrast is obtained.

Note 1: Measure of the following 9 places on the display.



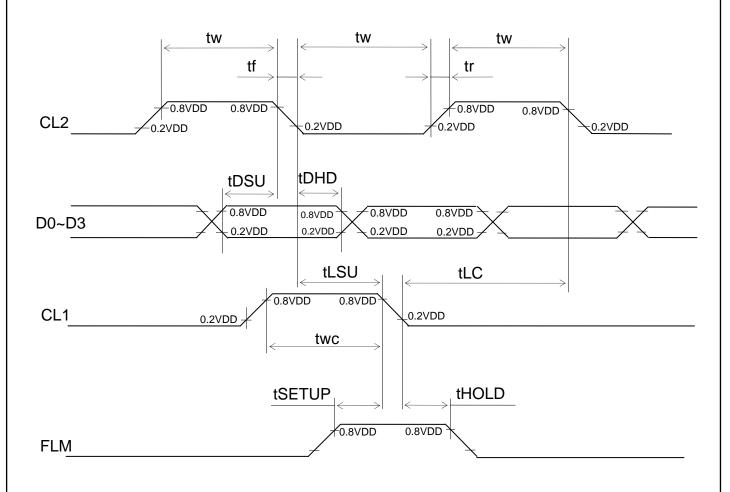
Definition of the brightness tolerance.



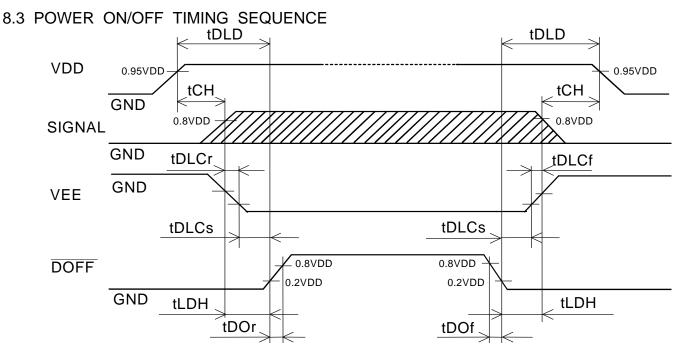
8. INTERFACE TIMING CHART 8.1 INTERFACE TIMING CHART $52.1 \mu s \le T \le 59.5 \mu s$ CL1 CL2 <u>X1</u> X240> Y1 XY5 > . Y317 D3 $\overline{(Y2)}\overline{(Y6)}$ Y318 D2 (Y3 XY7 Y319 D1 (Y4 XY8) D0 FLM CL1 240×T FLM **((**) X1 X239 X240 D0~D3 KAOHSIUNG HITACHI Sh. DATE | Mar.06,'09 7B64PS 2708-SP14Q006-ZZA -6 | PAGE | 8-1/3 ELECTRONICS CO.,LTD. No.

8.2 TIMING CHARACTERISTICS

ITEM	SYMBOL	MIN.	TYP.	MAX.	UNIT
CL2 frequency	fCP	1	-	6.5	MHz
CL2 pulse width	tw	45	-	1	ns
CL2 rise, fall time	tr,tf	1	-	15	ns
Data set up time	tDSU	30	-	1	ns
Data hold time	tDHD	30	-	-	ns
CL1 set up time	tLSU	80	-	•	ns
CL1 clock time	tLC	120	-	-	ns
"FLM" set up time	tSETUP	100	-	•	ns
"FLM" hold time	tHOLD	100	-	1	ns
"CL1" pulse width	twc	125	-	1	ns



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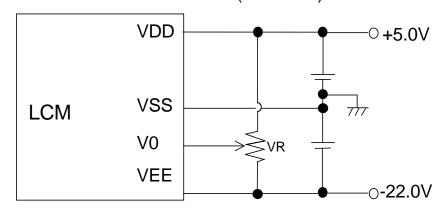
SYMBOL	MIN.	MAX.	UNIT	COMMENT
tDLD	50	-	ms	
tCH	0	30	ms	(Note 1)
tLDH	0	-	ms	
tDOr	-	100	ns	
tDOf	-	100	ns	
tDLCr	0	-	ms	(Note 2)
tDLCf	0	-	ms	
tDLCs	20	-	ms	

Note 1 : Please keep the specified sequence because wrong sequence may cause permanent damage to the LCD panel.

Note 2 : HITACHI recommends you to use $\overline{\text{DOFF}}$ function.

Display quality may deteriorate if you don't use $\overline{\text{DOFF}}$ function.

8.4 POWER SUPPLY FOR LCM (EXAMPLE)



Note 1 : $VR : 10k\Omega$

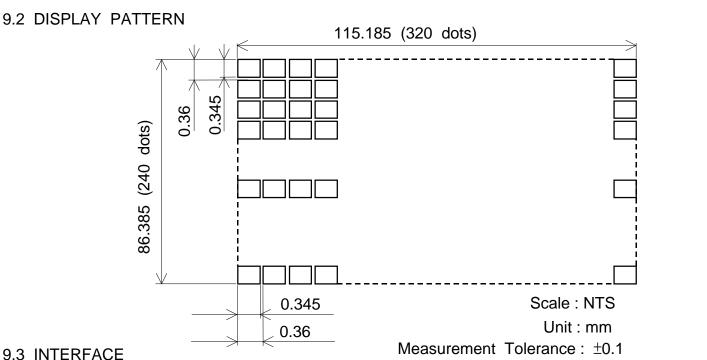
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KAOHSIUNG HITACHI			Sh.			
	DATE	Mar.06,'09	• • • • • • • • • • • • • • • • • • • •	7B64PS 2708- SP14Q006-ZZA -6	PAGE	8-3/3
ELECTRONICS CO.,LTD.	DAIL		No.	7 DO-1 0 2700- 01 1-0000-22A -0		0 0/0
LELCTROMICS CO., LTD.			140.			

9. OUTLINE DIMENSIONS 9.1 OUTLINE DIMENSIONS (200.0)167.0 ± 0.5 160.0 ± 0.5 7.0 ± 0.5 P1.0x3=3.0 ± 152.0 ± 0.3 11.0 ± 0.3 136.5 ± 0.5 T/P outline 14.35 ± 0.3 122.0 ± 0.5 (20.0)10.0 ± 0.5 121.7 ± 0.2 T/P V.A. 20.15 ± 0.3 120.0 min. 21.0 max. 0.2 118.7 ± 0.2 T/P A.A. 21.65 ± 0.3 0.36x319+0.345=115.185 23.41 ± 0.3 5.0 ± 11.31 ± 0.3 10.1 ± 0.3 10 max. 7.0 ± 0.5 8.6± 5.0 ± 0.5 7.0 ± 0.5 7.3 ± 0.5 ф • Stiffener Tape (35.0) $4-\phi 3.5 \pm 0.3$ Dots Area 7.0 ± Conductive Side Effective Area (49.0) 0.3 ± 0.05 0.36 Window of Metal Frame 0.345 (84.0)Detail A 92.3± 0.3 T/P V.A. 92.0± 0.5 101.0± 0.3 109.0 ± 0.5 T/P outline CN2 0.36x239+0.345=86.385 89.3 ±0.2 T/P A.A. 89.0 min. PIN1 Detail A 0.5 Viewing Direction ф • CN1 7.3 ± 0.5 7.0 ± 0.5 (200.0) 7.0 ± 0.5 0.3 ± 0.1 (35.0) 0.85 ± 0.2 1.25 ± 0.1 0.8 ± 0.1 18.75 ± 0.3 Detail B Detail B

Note1 : Measurement when adding 9.8 x 10⁴Pa at the measuring point.

Scale : NTS Unit : mm

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9.3 INTERFACE PIN CONNECTION

FPC: pitch 1.25mm 14 pins

INTER	RFACE	PIN No.	SIGNAL	LEVEL	FUNCTION
LCM	CN1	1	D0	H/L	Display Data
		2	D1		
		3	D2		
		4	D3		
		5	DOFF	H/L	H:ON / L:OFF
		6	FLM	Н	First Line Marker
		7	N.C	-	-
		8	CL1	H→L	Data Latch
		9	CL2	H→L	Data Shift
		10	VDD	-	Power Supply for Logic
		11	VSS	-	GND
		12	VEE	-	Power Supply for LC
		13	V0	-	Operating Voltage LC Driving
		14	VSS	-	GND

INTER	RFACE	PIN No.	SIGNAL	LEVEL	FUNCTION
LCM	CN2	1	VLED(+)	-	Power Supply for LED
		2	N.C	-	-
		3	N.C	-	-
		4	VLED(-)	-	LED GND

LED I/F: J.A.E/IL-G-4S-S3C2-SA

INTER	RFACE	PIN No.	SIGNAL	FUNCTION
		1	XR	Analog Signal from Digitizer Right
T/P	CNIO	2	YU	Analog Signal from Digitizer Up
1/P	CN3	3	XL	Analog Signal from Digitizer Left
		4	YB	Analog Signal from Digitizer Bottom

FPC: Pitch 1.0mm 4pins

Recommend Suitable connector: (HIROSE) FH12-10(4)SA-ISH

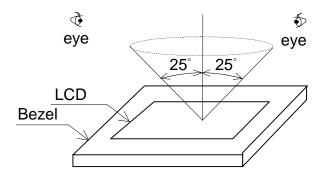
KAOHSIUNG HITACHI		Mar 06 '00	Sh.	 7B64PS 2709- SP14Q006-ZZA -6	DACE	0.2/2
ELECTRONICS CO.,LTD.	DATE	Mar.06,'09	No.	11 D04F3 2109- 3F14Q000-ZZA -6	FAGE	3-2/2

10. APPEARANCE STANDARD

10.1 APPEARANCE INSPECTION CONDITION

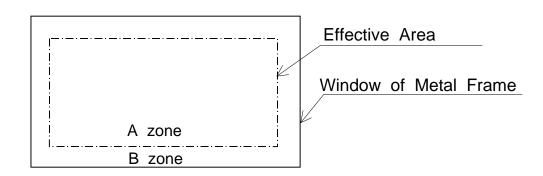
Visual inspection should be done under the following condition.

- (1) The inspection should be done under in the dark room.
- (2) The CFL should be lighted with the prescribed inverter.
- (3) The distance between eyes of an inspector and the LCD module is 25cm.
- (4) The viewing zone is shown the figure . Viewing angle $\leq 25^{\circ}$



10.2 DEFINITION OF EACH ZONE

A zone: Within the effective area specified at page 9-1/2 of this document. B zone: Area between the window of metal frame and the effective area line specified at page 9-1/2 of this document.



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10.3 APPEARANCE SPECIFICATION

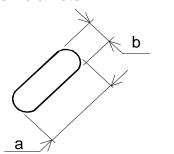
*) If a problem occurs in respect to any of these items, both parties(Customer and HITACHI) will discuss in more detail.

No.	ITEM				actan	•	Α	В
	Scratches	Distinguished one					*	-
					ple)			
	Dent	Same as Above	-				*	-
	Wrinkles in Polarizer	Same as Above	Diameter Maximum Number Acceptable D≤0.2 Ignore D≤0.3 12 D≤0.5 3 D None Filamentous Width Maximum Number Acceptable W≤0.03 Ignore 0.03 < W≤0.05 6 0.05 < W≤0.1 1 Round Maximum Number Acceptable Space Ignore - 3 8 10mm Space Ignore - 5 8 8 10mm Space Filamentous + Round = 10 Out easily are acceptable by HITACHI limit sample Ove Diameter Maximum Number Acceptable Diameter Maximum Number Acceptable O.15 Ignore 0.3 10 0.015 Ignore Contrast Maximum Minimum Number Acceptable Contrast Maximum Minimum Number Space Filamore - O.015 Ignore Contrast Maximum Minimum Number Acceptable Diamore - O.015 Ignore Contrast Maximum Minimum Number Space Acceptable O. To be Ignore -	*	-			
	Bubbles	Average D	iameter	Ma	ximum	n Number		
			<i>'</i>		Accep	otable		
			•					
								-
			≦0.5					
		0.5 <d< td=""><td></td><td></td><td>No</td><td>ne</td><td></td><td></td></d<>			No	ne		
	Stains,	Same as Above rinkles in Polarizer Same as Above Average Diameter D(mm) D ≤ 0.2 0.2 < D ≤ 0.3 0.3 < D ≤ 0.5 0.5 < D Ains, reign Materials, ark Spot Length L(mm) L≤2.0 L≤2.0 W≤ Average Maximun Diameter D(mm) D < 0.2 R Average D(mm) D < 0.2 D < 0.3 ≤ D < 0.33 0.33 ≤ D Total Filamento Those wiped out easily are slor Tone Filor Uniformity Same as Above	+					
	Foreign Materials,	_					\bigcirc	-
	Dark Spot	, ,			A			
L		L≦2.5				1		
					l			
С						Space		
			Ignore		_			_
				5		10mm		_
				2		-		
D					d = 10			
								\bigcirc
	Color Tone						$\overline{\bigcirc}$	-
	Color Uniformity						\bigcirc	-
	Pinhole	1		Ma	ximum	Number		
		D≦0.′	15					
		0.15 <d≦0.3< td=""><td>3</td><td></td><td>1</td><td>0</td><td></td><td></td></d≦0.3<>	3		1	0		
		C≦0.0	015		lgn	ore		
	Contrast	Average	Contrast	Maxim	num	Minimum	\bigcirc	-
	Irregularity	Diameter		Numb	per	Space		
	(Spot)	D(mm)						
		D≦0.25				-		
		0.25 < D ≤ 0.35		10				
		0.35 <d≦0.5< td=""><td>HITACHI</td><td></td><td></td><td>20mm</td><td></td><td></td></d≦0.5<>	HITACHI			20mm		
		0.5 < D		Non	е	-		

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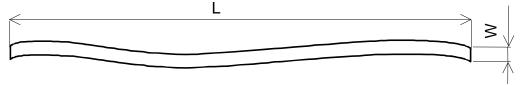
No.	ITEM		CRITERIA						
	Contrast Irregularity (Line)	Width D(mm)	Length L(mm)	Maximum Number Acceptable	Minimum Space				
L	(Filamentous)	W≦0.25	L≦1.2	2	20mm				
С		W≦0.2	L≦1.5	3	20mm		-		
D		W≦0.15	L≦2.0	3	20mm				
		W≦0.1	L≦3.0	4	20mm				
		To	ital	6	6				
	Rubbing Scratch	To be judged	by HITACHI	standard			-		

Note 1: Definition of average diameter D

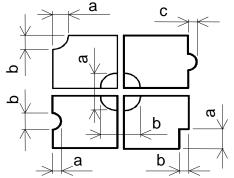


$$D = \frac{a+b}{2}$$

Note 2: Definition of length L and width W



Note 3 : Definition of pinhole



c : Salience

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11. PRECAUTION IN DESIGN

11.1 LC DRIVING VOLTAGE (VEE) AND VIEWING ANGLE RANGE
Setting VEE out of the recommended condition will be a cause for a change of viewing angle range.

11.2 PRECAUTIONS AGAINST STATIC CHARGE

As this module contains C-MOS LSIs, it is not strong against electrostatic discharge. Make certain that the operator's body is connected to the ground through a list band etc. And don't touch I/F pins directly.

11.3 POWER ON SEQUENCE

Input signals should not be applied to LCD module before power supply voltage is applied and reaches to specified voltage (VDD). If above sequence is not kept, C-MOS LSIs of LCD modules may be damaged due to latch up problem.

11.4 PACKAGING

- (1) No leaving product is preferable in the place of high humidity for a long period of time. For their storage in the place where temperature is 35 °C or higher ,special care to prevent them from high humidity is required. A combination of high temperature and high humidity may cause them polarization degradation as well as bubble generation and polarizer peel-off. Please keep the temperature and humidity within the specified range for use and storage.
- (2) Since polarizers tend to be easily damaged, They should be handled full with care so as not to get them touched, pushed or rubbed.
- (3) As the adhesives used for adhering polarizers are made of organic substances which will be deteriorated by a chemical reaction with such chemicals as acetone, toluene, ethanol and isopropyl alcohol. The following solvents are recommended for use: normal hexane

Please contact us when it is necessary for you to use chemicals.

- (4) Lightly wipe to clean the dirty surface with absorbent cotton waste or other soft material like chamois, soaked in the chemicals recommended without scrubbing it hardly. To prevent the display surface from damage and keep the appearance in good state, it is sufficient, in general, to wipe it with absorbent cotton.
- (5) Immediately wipe off saliva or water drop attached on the display area because its long period adherence may cause deformation or faded color on the spot.
- (6) Foggy dew deposited on the surface due to coldness will be caused for polarizer damage, stain and dirt on product. When necessary to take out the products from some place at low temperature for test, etc. It is required for them to be warmed up in a container once at the temperature higher than that of room.
- (7) Touching the display area and contact terminals with bare hands and contaminating them are prohibited, because the stain on the display area and poor insulation between terminals are often caused by being touched by bare hands. (Some cosmetics are detrimental to polarizers.)

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(8) In general the quality of glass is fragile so that it tends to be cracked or chipped in handling, specially on its periphery. Be careful not to give it sharp shock caused by dropping down, etc.

11.5 CAUTION FOR OPAERATION

- (1) It is an indispensable condition to drive LCDs within the specified voltage limit since the higher voltage than the limit causes the shorter LCD life. An electrochemical reaction due to direct current causes LCDs undesirable deterioration, so that the use of direct current driver should be avoided.
- (2) Response time will be extremely delayed at lower temperature than the operating temperature range and on the other hand at higher temperature LCDs show dark blue color in them. However those phenomena do not mean malfunction or out of order with LCDs which will come back in the specified operating temperature range.
- (3) If the display area is pushed hard during operation, some font will be abnormally displayed but it resumes normal condition after turning off once.
- (4) A slight dew depositing on terminals is a cause for electrochemical reaction resulting in terminal open circuit. Please operate the LCD module under the relative condition of 40° C 85%RH.

11.6 STORAGE

In case of storing for a long period of time (for instance, for years) for the purpose of replacement use, the following ways area recommended.

- (1) Storage in a polyethylene bag with the opening sealed, so the fresh air will not be entered from outside.
- (2) Placing in a dark place where neither exposure to direct sunlight nor light is , keeping temperature in the range from 0 $^{\circ}$ C to 35 $^{\circ}$ C.
- (3) Storing with no touch on polarizer surface by anything else. (It is recommended to store them as they have been contained in the inner container at the time of delivery from us.)

11.7 SAFETY

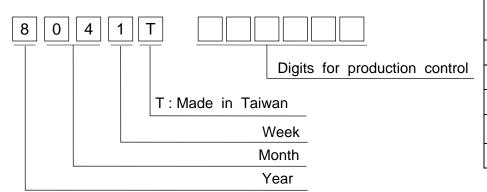
- (1) It is recommendable to crash damaged or unnecessary LCDs into pieces and wash off liquid crystal by either of solvents such as acetone and ethanol, which should be burned up later.
- (2) When any liquid leaked out of a damaged glass cell comes in contact with your hands, please wash it off well with soap and water.

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12. DESIGNATION OF LOT MARK

12.1 LOT MARK

Lot mark is consisted of 5 digits for production lot and 6 digits for production control.



Voor	Figure in
Year	Figure in
	lot mark
2009	9
2010	0
2011	1
2012	2
2013	3
	_

Month	Figure in	Month	Figure in
Month	lot mark	Month	lot mark
Jan.	01	Jul.	07
Feb.	02	Aug.	08
Mar.	03	Sep.	09
Apr.	04	Oct.	10
May	05	Nov.	11
Jun.	06	Dec.	12

Week	Figure in
(day in calendar)	lot mark
1~ 7	1
8~14	2
15~21	3
22~28	4
29~31	5

12.2 SERIAL No.

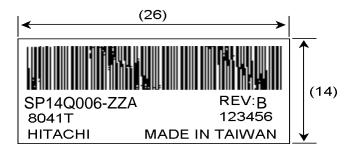
Serial No. is consisted of 6 digits number (000001~999999).

12.3 LOCATION OF LOT MARK

Label is bring attached on the back side of module.

12.4 REVISION(Rev.) CONTROL

Rev No.	ITEM
	Backlight life time: 40kh
Α	Mcount IC :MN73099HED(Panasonic)
	Transistor :2SA1036K(ROHM)
	Backlight life time: 40kh
В	Mcount IC :IT7001M(ITE)
	Transistor :2SA1576(ROHM)



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13. PRECAUTION FOR USE

- 13.1 A limit sample should be provided by the both parties on an occasion when the both parties agreed its necessity. Judgment by a limit sample shall take effect after the limit sample has been established and confirmed by the both parties.
- 13.2 On the following occasions, the handling of the problem should be decided through discussion and agreement between responsible persons of the both parties.
 - (1) When a question is arisen in the specifications.
 - (2) When a new problem is arisen which is not specified in this specifications.
 - (3) When an inspection specifications change or operating condition change in customer is reported to HITACHI, and some problem is arisen in this specification due to the change.
 - (4) When a new problem is arisen at the customer's operating set for sample evaluation in the customer site.

The precaution that should be observed when handling LCM have been explained above. If any points are unclear or if you have any request, please contact HITACHI.

14. TOUCH PANEL SPECIFICATION

14.1 RATINGS

14.1.1 ABSOLUTE MAXIMUM RATINGS

ITEM	SPECIFICATION	COMMENT
Operating Voltage	7V	Without
Contact Current	20mA	Condensation

14.1.2 OPERATING CONDITIONS

ITEM	SPECIFICATION
Operating Voltage	5.0 / 3.3 VDC
Contact Current	10 ~ 20 mA
Actuation Force	1.2N max. (R8,Silicone rubber)

14.2 SURFACE HARDNESS 2H

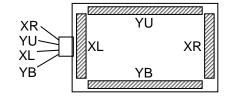
14.3 OPTICAL CHARACTERISTICS 14.3.1 TRANSPARENCY: 76%.min.

(WAVE LENGTH : 450 ~ 700nm)

14.4 ELECTRICAL CHARACTISTICS

14.4.1 CONDUCTIVE RESISTANCE

TERMINAL	CONDUCTIVE RESISTANCE				
XR-XL	150~1300Ω				
YU-YB	150~1300Ω				



14.4.2 INSULATION RESISTINCE

TERM	IINAL IN	SULATION RESISTANG	TESTING VOLTAGE
X-	Υ	20 Μ Ω	25VDC

14.4.3 BOUNCE CHATTERING 10ms max.

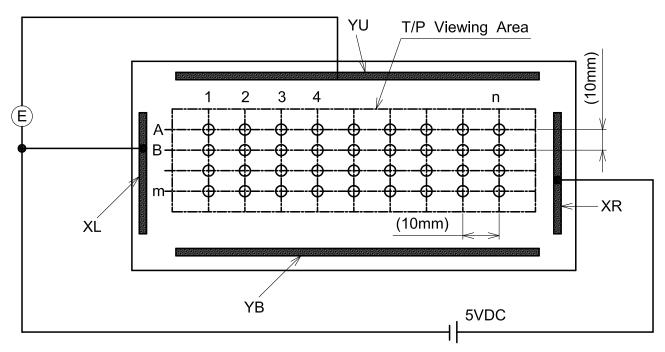
KAOHSIUNG HITACHI	D 4 T E	NA 00 100	Sh.	ZD04D0 0744 0D440000 774 0 F) ^ C E	4.4.4.4
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14.4.4 LINEARITY

(1) LINEARITY

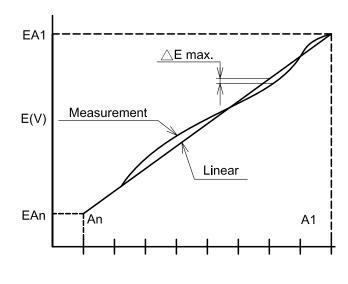
Linearity Deviation: 2% max.

- (2) TESTING CIRCUIT
 - (a) X axis linearity testing method ,100g , VXR-VXL=5V , VOUT=VYU.



- (b) Y axis linearity method, 100g VYU-VYB=5V, VOUT=VXR
- (3) CALCULATION
 - (a) X axis linearity

LINEARITY=
$$\frac{\triangle \text{ E max.}}{\text{EA1} - \text{EAn}}$$
 x100(%)



Input Position

14.5 ENVIRONMENTAL TESTING

ITEM	CONDITIONS	CRITERIA
High Temperature	60℃ : 120h & 25℃: 24h	
Storage		
Low Temperature	-20℃ : 120h & 25℃ : 24h	
Storage		After testing must to
Temperature -20°C ←→ 70°C : 10 Cycles within		After testing must to
Cycle	(30) (60) (30) : minutes & 25°C	meet the specifications of the Electrical,
	: 24h (Without Condensation)	Mechanical & Optical
Humidity Storage	60℃ , 90%RH. 120h	Characteristics.
Durability for	150g , R8, HS40 Silicon Rubber	Characteristics.
Keystroke	(Speed : 330mm/sec)	
	: 1000000 Activations	

14.6 APPEARANCE SPECIFICATION

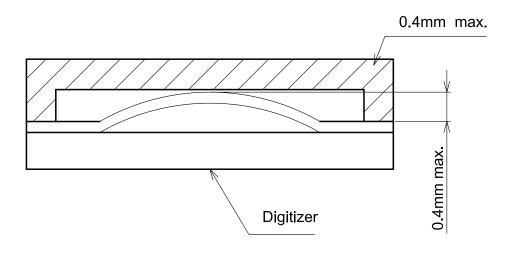
No.	ITEM	CRITERIA				Α	В
		FILAMENTOUS					
	Hair Flaws	Length L(mm)	Width W(mm)		Maximum Number Acceptable	0	-
		L≦12	W≦	0.05	Ignore		
		L≦5	0.05 < \	W≦0.1	3		
		L>2	0.1	I < W	None		
Т	Dot-shaped Impurities	Average Dian D(Mm)	eter Maximum Number Acceptable				
/		D≦0.1		Ignore			
Р		0.1 <d≦0.< td=""><td>3</td><td></td><td>5</td><td>0</td><td>_ </td></d≦0.<>	3		5	0	_
		0.3 <d< td=""><td colspan="2">None</td><td></td><td></td></d<>		None			
		FILAMENTOUS					
	Scratch	Length L(mm)		dth nm)	Maximum Number Acceptable		
		L≦12		0.05	Ignore	0	-
		L≦12	0.05 < \	W <u>≦0.1</u>	5		
		L>12 0.		<w none<="" td=""><td></td><td></td></w>			

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14.6.3 GLASS INDENTATION

ITEM	SPECIFICATIONS	
Common Indentation	X Z t	$\begin{array}{ c c c }\hline X & Y & Z \\ \leq 5.0 & \leq 3.0 & \leq t \\ \hline \\ But \ , indentation \ can \ not \\ including \ seal \ area. \\ t : Glass \ thuickness. \\ \hline \end{array}$
Corner Broken	X Z	$\begin{array}{ c c c c }\hline X & Y & Z \\ \leq 2.0 & \leq 5.0 & \leq t \\ \hline \\ \text{But , indentation can not including seal area.} \\ \\ \end{array}$
Indentation Witnin Pattern		Y≦1 Is ignore But , Must to meet the specification of conducting pattern indentation.
Proceeding Crack		None

14.6.4 BLISTERING (PUFFINESS): 0.4 mm max.



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