LCD MODULE SPECIFICATION
FOR CUSTOMER'S APPROVAL

CUSTOMER : **Standard**

MODULE TYPE : **MTG-F24064GMNHSCW**

APPROVED BY: (FOR CUSTOMER USE ONLY)

Approved By	Checked By	Prepared By	MT File No	Date Issued

CONTENTS

SPECIFICATION FOR

LIQUID CRYSTAL DISPLAY MODULE

MODEL NO. :   MTG-F24064GMNHSCW

View Direction	☑ 6 O'clock			☐ 12 O'clock	
LCD Type	☐ FSTN Positive		☑ FSTN Negative		
	☐ STN Gray		☐ STN Yellow Green	☐ STN Blue	
Rear Polarizer	☐ Reflective		☐ Transflective	☑ Transmissive	
Backlight Type	☐ LED	☐ Internal Power	☐ EL	☐ 5V input	
		☑ External Power	☑ CCFL	☐ 12V input	
Backlight Color	☑ White	☐ Amber	☐ Blue Green	☐ Yellow Green	☐ Other
Temperature Range	☐ Normal		☑ Wide	☐ Super Wide	
EL Driver IC	☐ Build-in		☑ Not Build-in		
Touch Screen	☐ With		☑ Without		

TO BE VERY CAREFUL !

The LCD driver ICs are made of CMOS process, which is very easy to be damaged by static charge, make sure the user is grounded when handling the LCM.

C (View)

35±0.3

66±0.5

56.8±0.3

54±0.3

39±0.2

33.9

100±0.1

200±0.5

155±0.3

132±0.2

127.18

193±0.3

20±0.1

P2.54x(20-1)=48.26

20

20-ø1.0

2.5

4-ø3.5 Padø6.0

182.5±5

16±0.3

1.6

23Max.

Dots

0.51

0.51

0.02

0.02

GENERAL SPECIFICATION

Item	Content
Display Resolution	240(H)× 64(W)
Dimensional Outline(mm)	200.0 (W)× 66.0 (W)× 23.0(D) max
Display mode	Transmissive Type
Circuit	Controller IC, Common-Driver IC, Segment-driver IC
Interface	Data (DB0～DB7), C/D, WR, RD, CE, RES, FS, V_{EE}.

ABSOLUTE MAXIMUN RATING

(1) Electrical Absolute Ratings

Item	Symbol	Min.	Max.	Unit	Note
Power Supply for Logic	V_{DD}-V_{SS}	-0.3	5.5	Volt	
Power Supply for LCD	V_{EE}-V_{SS}	0	26.0	Volt	
Input Voltage	V1	-0.3	V_{DD}	Volt	
Static Electricity	-	-	-	-	Note 1

Note 1 : Operator should be grounded during handling LCM.

(2) Environmental Absolute Maximum Ratings

Item	Normal Temperature				Wide Temperature			
	Operating		Storage		Operating		Storage	
	Max,	Min.	Max,	Min.	Max,	Min.	Max,	Min.
Ambient Temperature	0℃	+50℃	-20℃	+70℃	-20℃	+70℃	-30℃	+80℃
Humidity(without condensation)	Note 2,4		Note 3,5		Note 4,5		Note 4,6	

Note 2　Ta≦50℃: 80% RH max

　　　　Ta>50℃: Absolute humidity must be lower than the humidity of 85%RH at 50℃

Note 3　Ta at -20℃　will be<48hrs at 70℃will be <120hrs when humidity is higher than 75%

Note 4　Background color changes slightly depending on ambient temperature. This phenomenon
　　　　is reversible.

Note 5　Ta≦70℃: 75RH max

　　　　Ta>70℃: absolute humidity must be lower than the humidity of 75%RH at 70℃

Note 6　Ta at -30℃　will be <48hrs, at 80 ℃　will be <120hrs when humidity is higher than 75%

ELECTRICAL CHARACTERISTICS

Item	Symbol	Condition	Min.	Typ	Max.	Unit	note
Power Supply for Logic	V_{DD}-V_{SS}	-	4.5	5.0	5.5	Volt	
Input Voltage	V_{IL}	L level	V_{SS}	0.2 V_{DD}	-	Volt	
	V_{IH}	H level	0.8 V_{DD}	V_{DD}	-	Volt	
LCM Recommend LCD Module Driving Voltage	V_{DD} -V_{EE}	Ta＝-20℃	-	-	-	Volt	
		Ta＝25℃	12.4	12.8	13.2		
		Ta＝70℃	-	-	-		
Power Supply Current for LCM	I_{DD}	V_{DD} =5.0V V_{EE}-V_{SS} =12.8V FLM=64Hz Ta=25℃	-	10.0	15	mA	
	I_{EE}		-	2.0	3.0		
CCFL Starting Voltage	V_{FLS}	-	-	750	-	Vrms	
CCFL driving Voltage	V_{CCFL}	V_{CCFL}=450Vrms f_{CCFL}=30kHz Ta=25℃	-	360	-	Vrms	
CCFL driving current	I_{CCFL}		-	5.0	-	mA	
CCFL driving frequency	f_{CCFL}		15	30	85	KHz	
CCFL Saturation Time	t_{SAT}			60	-	Second	

OPTICAL CHARACTERISTICS

Item	Symbol	Condition	Min.	Typ	Max.	Unit	note
Viewing angle range	Φf(12 o'clock)	When Cr≧2	-	41	-	Degree	9,10
	Φb(6 o'clock)		-	34	-		
	Φl(9 o'clock)		-	35	-		
	Φr(3 o'clock)		-	30	-		
Rise Time	Tr	V_{DD}-V_{EE} =12.8V		120		mS	
Fall Time	Tf			250			
Frame frequency	Frm		-	64	-	Hz	8,10
Contrast	Cr	-	-	5.34	-		7

MECHANICAL SPECIFICATIONS

ITEM		DESCRIPTION
Product No.		MTG-F24064GMNHSCW
Module Size		200.0(W)mm× 66.0(H)mm× 23.0(D)mm Max
Dot Size		0.51(W)mm× 0.51(H)mm
Dot Pitch		0.53(W)mm× 0.53(H)mm
Resolution		240(W)× 64(H) Dots Matrix
Duty Ratio		1/64 Duty
LCD Display Mode	STN	☐Gray Mode ☐Yellow Mode ☐Blue Mode
	FSTN	☐ Black & White(Normally White/Positive Image) ☑ Black & White(Normally White/Negative Image)
	Rear Polarizer:	☐ Reflective ☐ Transflective ☑ Transmissive ☐ Transflective(High Transmissive)
Viewing Direction		☑6 O'clock ☐12 O'clock ☐3 O'clock ☐9 O'clock
Backlight		☐W/O ☑CCFL ☐EL ☐LED
Controller		Toshiba T6963C
DC/DC Converter		Without
CCFL Inverter		Optional

INTERFACE PIN ASSIGNMENT

PIN NO.	PIN OUT	FUNCTION DESCRIPTION
1	FGND	Frame Ground
2	V_{SS}	GND
3	V_{DD}	Logic supply voltage
4	V_{EE}	LCD driver supply voltage
5	WR	Write Data
6	RD	Read Data
7	CE	Chip Enable
8	C/D	Command/Data Register select
9	NC	No Connection
10	RES	Reset
11	DB0	Data Bus
12	DB1	
13	DB2	
14	DB3	
15	DB4	
16	DB5	
17	DB6	
18	DB7	
19	FS	Font Select. H=6x8 dot matrix, L=8x8 Dot matrix
20	NC	No Connection

[Note 7] Definition of Operation Voltage (Vop)

[positive type]

[Negative type]
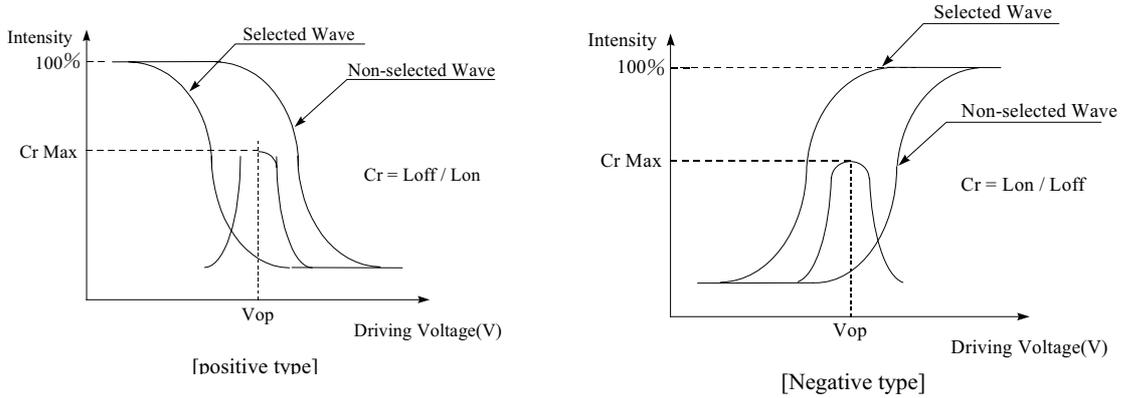
[Note 8] Definition of Response Time (Tr, Tf)

[positive type]

[Negative type]
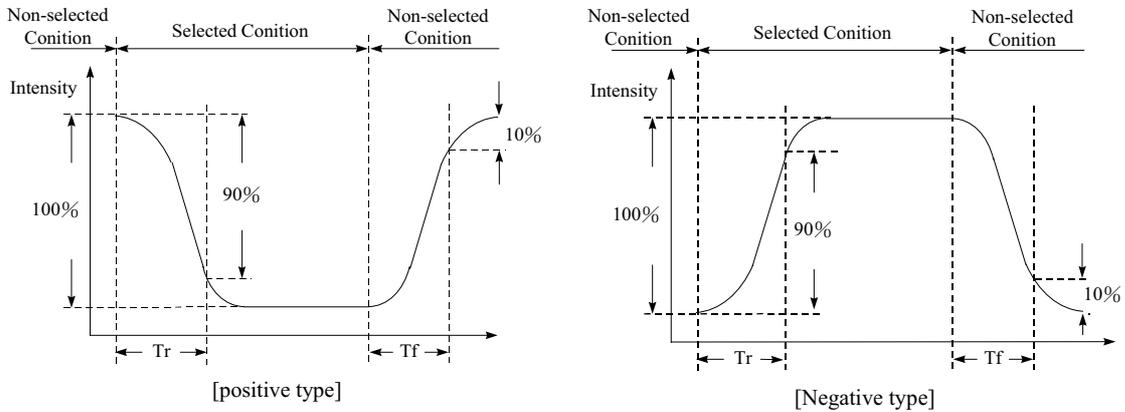
Conditions:

Operating Voltage : Vop

Frame Frequency : 64 Hz

Viewing Angle(θ, φ): 0° , 0°

Driving Wave form : 1/N duty, 1/a bias

[Note 9] Definition of Viewing Direction
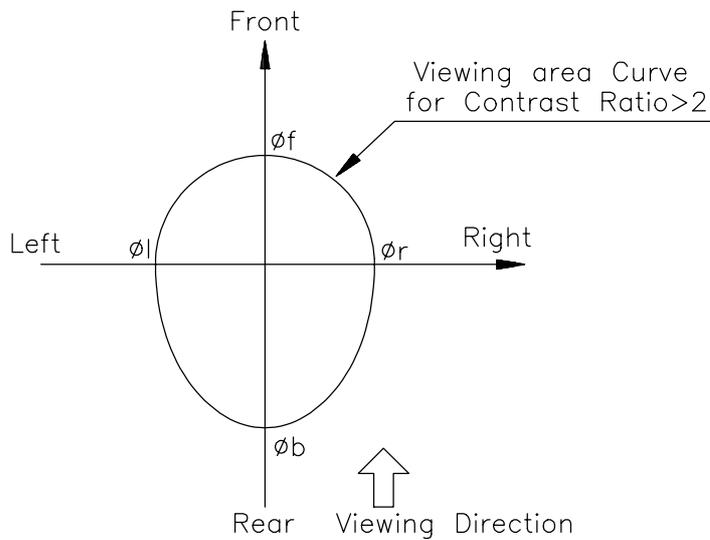
[Note 10] Definition of viewing angle
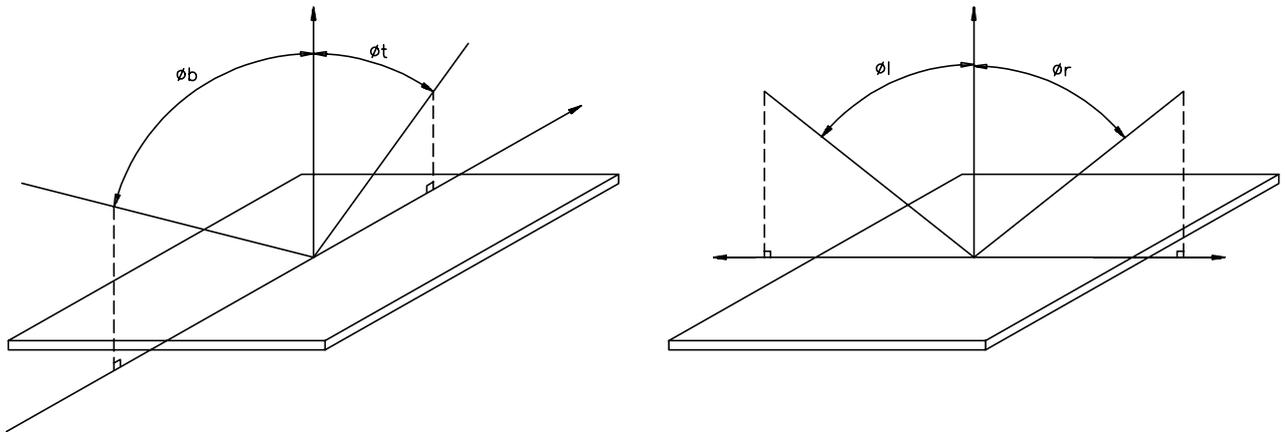
[Note 11] Description of Measuring Equipment

Reflective type

Transflective type

BLOCK DIAGRAM

Left side pins: FGND, D0~D7, WR, RD, CE, C/D, Reset, FS, GND, VDD, VEE

Blocks: LCD Controller, Data, Address Bus, 8Kx8 bits SRAM, Bias Circuit, Seg IC, Seg IC, Seg IC, Com IC, Com IC, 240x64 LCD, EL Driver CKT, DC-to-DC inverter

POWER SUPPLY

LCD Module — VDD, VEE, VSS

VR : 20~30K OHM

VDD, −8V

CCFL Backlight

V_{BKL} : CCFL, 400Vrms/30KHz AC
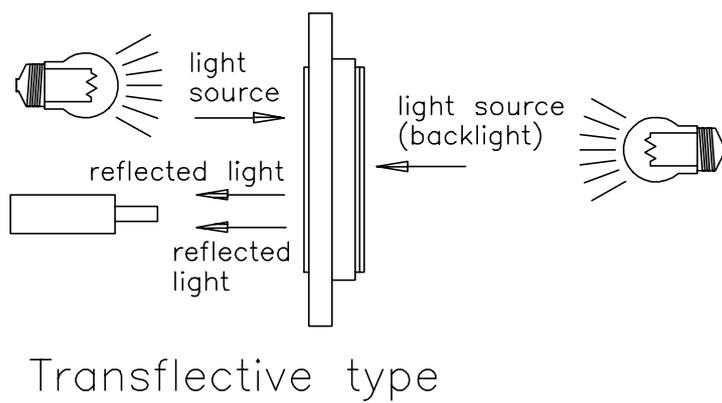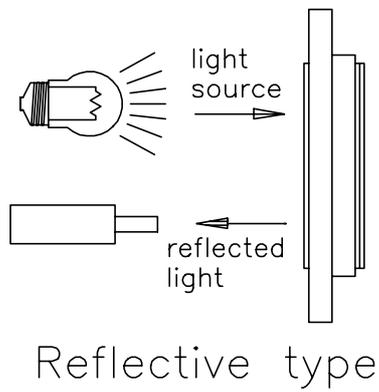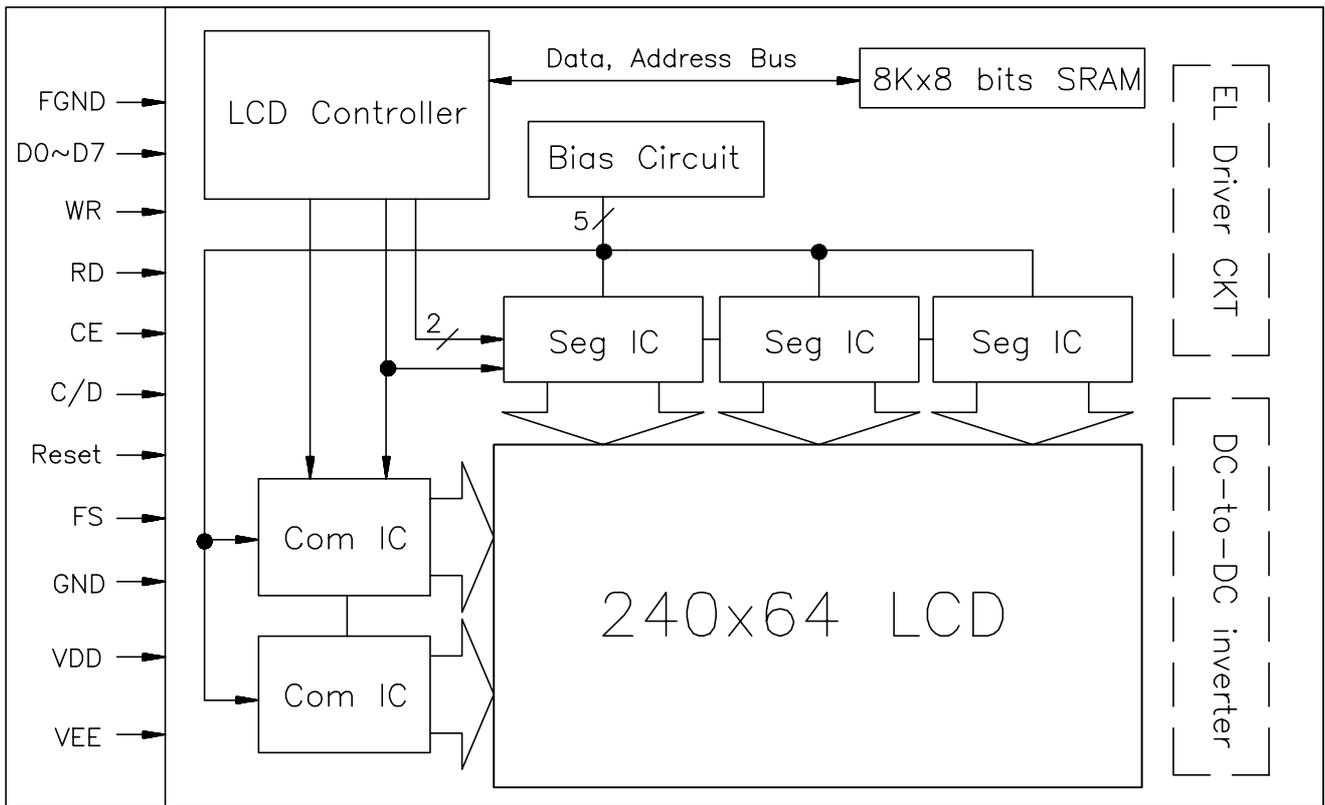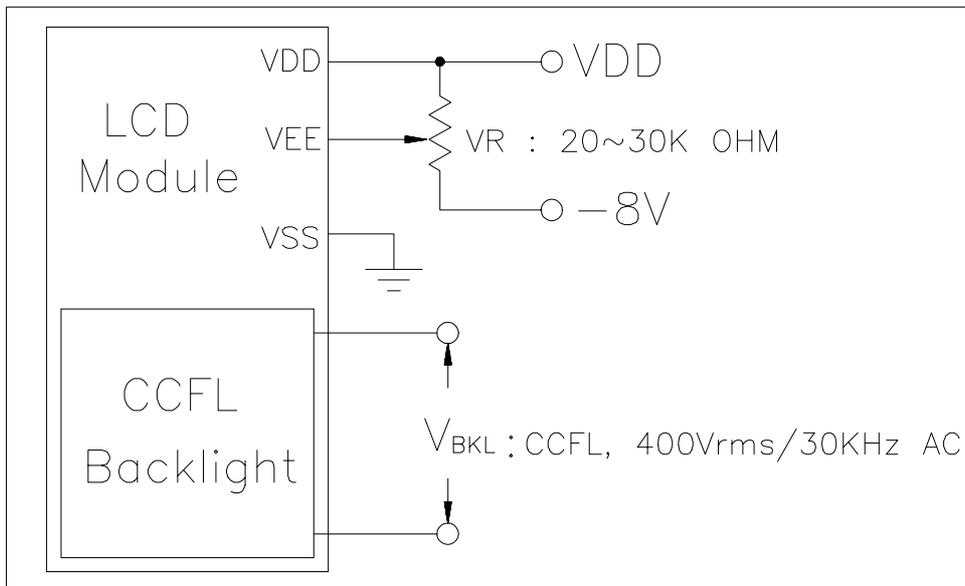
AC CHARACTERISTIC

(V_{DD}=5.0V± 10％, V_{SS}=0V, Ta=0 to 50℃)

Item	Symbol	Test Condition	Min.	Max.	Unit
C/D setup time	t_{CDS}	—	100	—	
C/D Hold time	t_{CDH}	—	10	—	
CE, RD, WR pulse width	t_{CDS}, t_{CDS}, t_{CDS},	—	80	—	
Data setup time	t_{DS}	—	80	—	nS
Data hold time	t_{DH}	—	40	—	
Access time	t_{ACC}	—	—	150	
Output hold time	t_{OH}	—	10	50	

Timing Chart
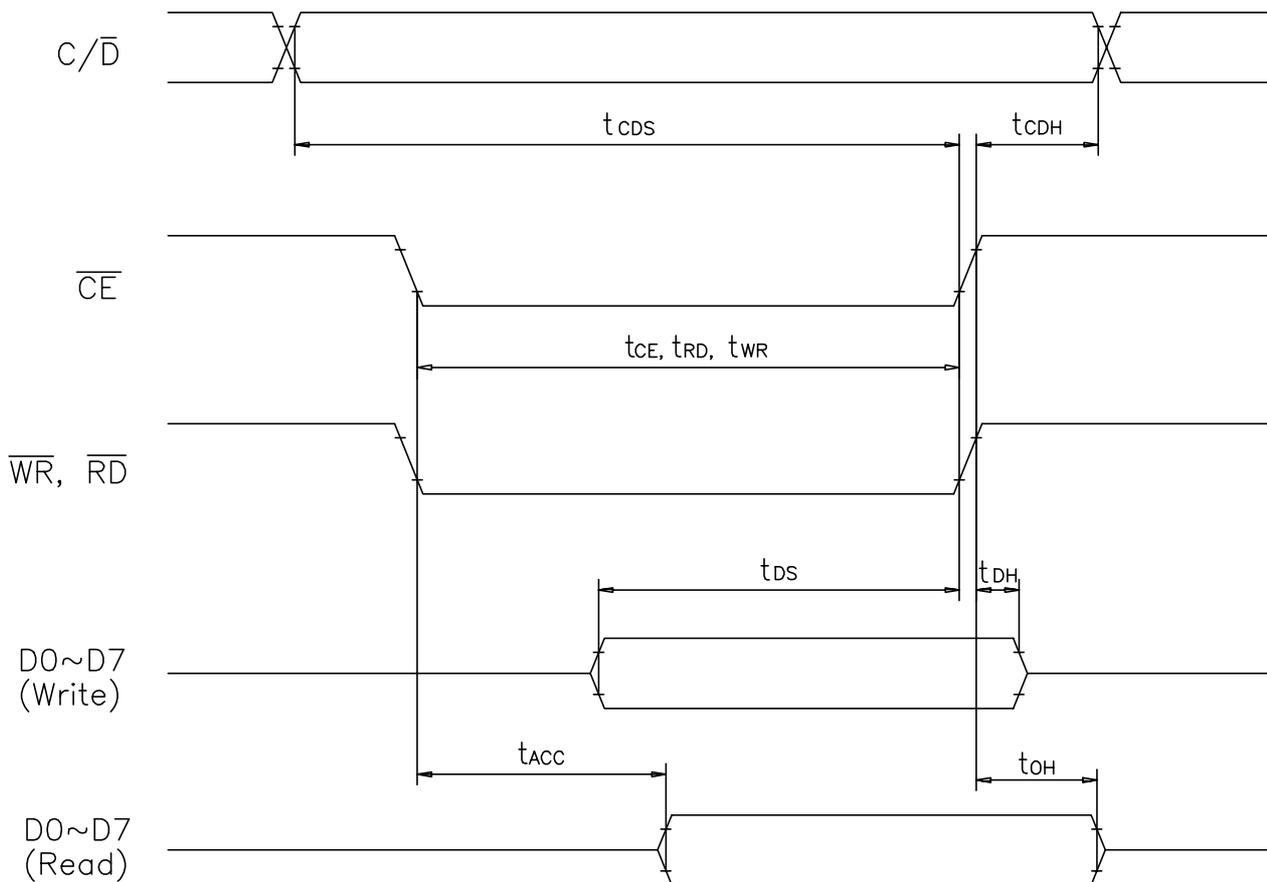
Command List

Command	Code	D1	D2	Function
Register Set	00100001	X address Data Low Address	X address 00H High Address	Cursor pointer set Offset register set Address pointer set
Control Word Set	01000000 01000001 01000010 01000011	Low address Columns Low address Columns	High address 00H high address 00H	Text home address set Text area set Graphic home address set Graphic area set
Mode Set	1000x000 1000x001 1000x011 1000x100 10000xxx 100111xx	—	—	'OR' mode 'EXOR' mode 'AND' mode 'Text attribute' mode Internal CG ROM mode External CG ROM mode
Display Mode	10010000 1001xx10 1001xx11 100101xx 100110xx 100111xx	—	—	Display off Cursor on, blink off Cursor on, blink on Text on, graphic off Text off, graphic on Text on, graphic on
Cursor Pattern Select	10100000 1010000 10100010 10100011 10100100 10100101 10100110 10100111	—	—	1 line cursor 2 lines cursor 3 lines cursor 4 lines cursor 5 lines cursor 6 lines cursor 7 lines cursor 8 lines cursor
Data Auto Read/Write	10110000 10110001 10110010	—	—	Data auto write set Data auto read set Auto read
Data Read/Write	11000000 11000001 11000010 11000011 11000100 11000101	Data - Data - Data -		Data write and ADP increment Data read and ADP increment Data write and ADP decrement Data read and ADP decrement Data write and ADP nonvariable Data read and ADP nonvariable
Screen Peak	11100000	—	—	Screen peak
Screen Copy	11101000	—	—	Screen copy
Bit Set/Reset	11110xxx 11111xxx 1111x000 1111x001 1111x010 1111x011 1111x100 1111x101 1111x110 1111x111	—	—	Bit reset Bit set Bit0(LSB) Bit1 Bit2 Bit3 Bit4 Bit5 Bit6 Bit7(MSB)

'x' means 'Don't care'

Setting Registers

Code	HEX.	Function	D1	D2
00100001	21H	Set cursor point	X ADRS	Y ADRS
00100010	22H	Set offset register	Data	00H
00100100	24H	Set address pointer	LOW ADRS	HIGH ADRS

(1) Set Cursor Pointer

The position of the cursor is specified by X ADRS and Y ADRS. The cursor position can only moved by this command. Data read/write from MPU never change the cursor pointer. X ADRS and Y ADRS are specified as follows:

X ADRS   00H to 4FH(lower 7 bits are valid)
Y ADRS   00H to 1FH(lower 5 bits are valid)

a) Single-scan
   X ADRS 00 to 4FH

Y ADRS 00H to 0FH

b) Dual-scan
   X ADRS 00 to 4FH

Y ADRS 00H to 0FH Upper Screen

Y ADRS 00H to 0FH Lower Screen

(2) Set offset register

The offset register is used to determine the external character generator RAM area. The T6963C has a 16-bit address bus as follows:

MSB   LSB

ad15	ad14	ad13	ad12	ad11	ad10	ad9	ad8	ad7	ad6	ad5	ad4	ad3	Ad2	ad1	ad0

       Offset Register Data   Character Code   Line Scan
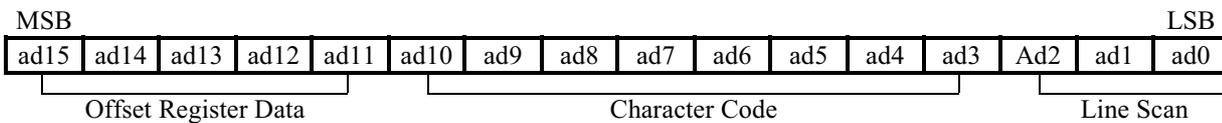
T6963C assign external character generator, when character code set 80H to FFH in using internal character generator. Character codes 00H to 80H assign External character generator, when External generator mode.
The senior 5 bits define the start address in external memory of CG RAM area. The next 8 bits represent the character code of character. In internal CG ROM mode, character codes 00H to 7FH represent the predefined "internal" CG ROM characters, and codes 80H to FFH represent the user's own "external" characters. The 3 lease significant bits indicate one of the 8 rows of 8 dots that define the character's shape.

The relationship between display RAM address and offset register

Offset register data	CG RAM hex. Address (start to end)
00000	0000H to 07FFH
00001	0800H to 0FFFH
00010	1000H to 17FFH
11100	E000H to E7FFH
11101	E800H to EFFFH
11110	F000H to F7FFH
11111	F800H to FFFFH

(Example 1)

Offset register	02H
Character code	80H
Character generator RAM start address	0001 0100 0000 0000
	1 4 0 0 H

Address	Data
1400H	00H
1401H	1FH
1402H	04H
1403H	04H
1404H	04H
1405H	04H
1406H	04H
1407H	00H

Text

(Example 2)

The relationship between display RAM data and display characters

A B γ D E ζ G H I J K L M

(RAM Data)	(Character)
21H	A
22H	B
83H	γ
24H	D
25H	E
86H	ζ

γ and ζ are displayed by Character Generator RAM.

(3) Set Address Pointer
The Set Address Pointer command is used to indicate the start address for writing to (or reading from) external RAM.
The Flowchart for Set Address Pointer command:

```
        ( Set Address Pointer )
                 |
        [ Status Check ]        STA0, 1
                 |
        [ Set Address Data ]
        [  (lower 8 bits)  ]
                 |
        [ Status Check ]
                 |
        [ Set Address Data ]
        [  (upper 8 bits)  ]
                 |
        [ Status Check ]
                 |
        [ Send Command 24H ]    Send Set Address Pointer Command
                 |
             ( End )
```
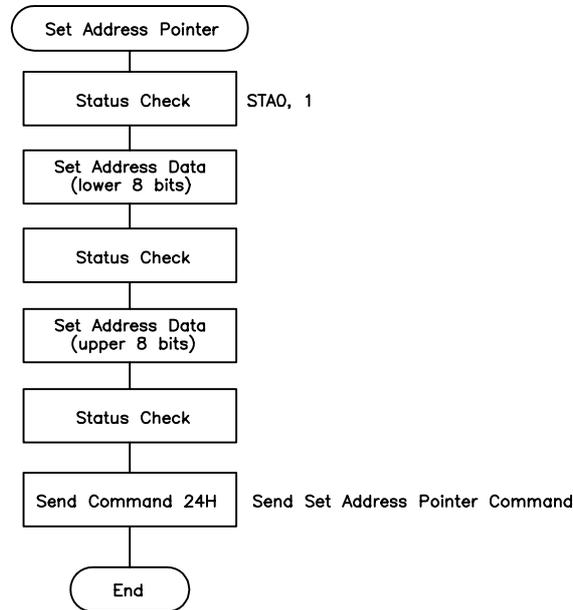
Set Control Word

Code	HEX.	Function	D1	D2
01000000	40H	Set Text Home Address	Lower Address	High Address
01000001	41H	Set Text Area	Columns	00H
01000010	42H	Set Graphic Home Address	Lower Address	High Address
01000011	43H	Set Graphic Area	Columns	00H

The home address and column size are defined by this command.

(1) Set Text Home Address

The starting address in the external display RAM for text display is defined by this command. The text home address indicates the leftmost and uppermost position.

The relationship between external display RAM address and display position

TH		TH+CL
TH+TA		TH+TA+CL
(TH+TA)+TA		(TH+TA)+TA+CL
(TH+2TA)+TA		(TH+2TA)+TA+CL
TH+(N-1)TA		TH+(N-1)TA+CL

TH：Text home address

TA：Text area number(Columns)

CL：Column are fixed by hardware(pin-programmable).

(Example)

Text home address	：0000H		
Text area	：0020H		
MD2=H, MD3=H	：32 columns		
DUAL=H, MDS=L, MD0=L, MD1=H	：4 lines		

0000H	0001H		001EH	001FH
0020H	0021H		003EH	003FH
0040H	0041H		005EH	005FH
0060H	0061H		007EH	007FH

(2)Set Graphic Home address
The starting address of the external display RAM used for graphic display is defined by this command. The graphic home address indicates the leftmost and uppermost position.

The relationship between external display RAM address and display position

GH		GA+CL
GH+GA		GH+GA+CL
(GH+GA)+GA		(GH+GA)+GA+CL
(GH+2GA)+GA		(GH+2GA)+GA+CL
GH+(N-1)GA		GTH+(N-1)GA+CL

GH：Graphic home address

GA：Graphic area number(Columns)

CL：Column are fixed by hardware(pin-programmable).

(Example)

Graphic Mode address	：0000H
Graphic Area	：0020H
MD2=H, MD3=H	：32 columns
DUAL=H, MDS=L, MD0=H, MD1=H	：2 lines

0000H	0001H	……..	001EH	001FH
0020H	0021H	……..	003EH	003FH
0040H	0041H	……..	005EH	005FH
0060H	0061H	……..	007EH	007FH
0080H	0081H	……..	009EH	009FH
00A0H	00A1H	……..	00BEH	00BFH
00C0H	00C1H	……..	00DEH	00DFH
00E0H	00E1H	……..	00FEH	00FFH
0100H	0101H	……..	011EH	011FH
0120H	0121H	……..	013EH	013FH
0140H	0141H	……..	015EH	015FH
0160H	0161H	……..	017EH	017FH
0180H	0181H	……..	019EH	019FH
01A0H	01A1H	……..	01BEH	01BFH
01C0H	01C1H	……..	01DEH	01DFH
01E0H	01E1H	……..	01FEH	01FFH

(3)Set Text Area

The display columns are defined by the hardware setting. The command can be used to adjust the columns of the display.

(Example)

LCD Size	：20 columns, 4 lines
Text home address	：0000H
Text Area	：0014H
MD2=H, MD3=H	：32 columns
DUAL=H, MDS=L, MD0=L, MD1=H	：4 lines

0000	0001	……..	0013	0014	……..	001F
0014	0015	……..	0027	0028	……..	0033
0028	0029	……..	003B	003C	……..	0047
003C	003D	……..	004F	0050	……..	005B
LCD						

(4)Set Graphic Area

The display columns are defined by the hardware setting. The command can be used to adjust the columns of the graphic display.

(Example)

LCD Size	：20 columns, 2 lines
Graphic home address	：0000H
Graphic Area	：0014H
MD2=H, MD3=H	：32 columns
DUAL=H, MDS=L, MD0=H, MD1=H	：2 lines

0000	0001	……..	0013	0014	……..	001F
0014	0015	……..	0027	0028	……..	0033
0028	0029	……..	003B	003C	……..	0047
003C	003D	……..	004F	0050	……..	005B
0050	0051	……..	0063	0064	……..	006F
0064	0065	……..	0077	0078	……..	0083
0078	0079	……..	008B	008C	……..	0097
008C	008D	……..	009F	00A0	…….	00AB
00A0	00A1	……..	00B3	00B4	……..	00BF
00B4	00B5	……..	00C7	00C8	……..	00D3
00C8	00C9	……..	00DB	00DC	……..	00E7
00DC	00DD	……..	00EF	00F0	……..	00FB
00F0	00F1	……..	0103	0104	…….	011F
0104	0105	……..	0127	0128	……..	0123
0128	0129	……..	013B	013C	……..	0147
013C	013D	…….	014F	0150	…….	015B
LCD						

If the graphic area setting is set to match the desire number of columns on the LCD, the addressing scheme will be automatically modified so that the start address of each line equals the end address of the previous line + 1.

Mode Set

Code	Function	Operand
1000x000	OR mode	-
1000x001	EXOR mode	-
1000x011	AND mode	-
1000x100	TEXT ATTRIBUTE mode	-
10000xxx	Internal character generator mode	-
10001xxx	External character generator mode	-

The display mode is defined by this command. The display mode does not change until the next command is sent. The logical OR, EXOR, AND of text or graphic display can be displayed. In internal character generator mode, character codes 80H to FFH are automatically assigned the build-in character generator ROM. The character codes 80H to FFH are automatically assigned to the external character generator RAM.

(Example)
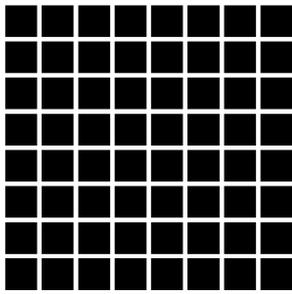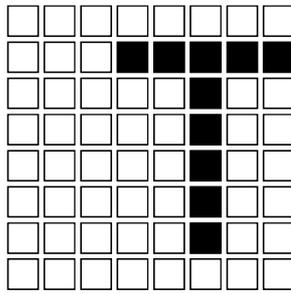
Graphic   Text
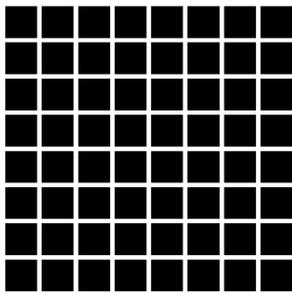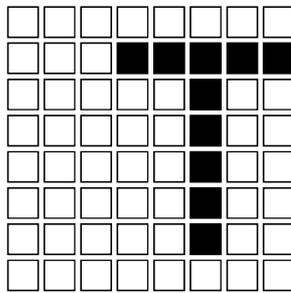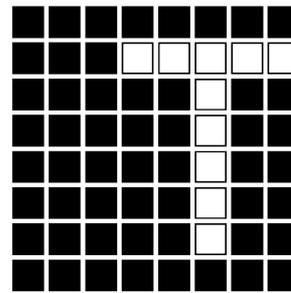
"OR"   "AND"   "EXOR"

(Note):
Attribute functions can only be applied to text display, since the attribute data is placed in the graphic RAM area.

Attribute function

The attribute operations are Reverse display, Character blink and Inhibit. The attribute data is written into the graphic area which was defined by the Set Control Word command. Only text display is possible in Attribute Function mode, graphic display is automatically disabled. However, the Display Mode command must be used to turn both Text and Graphic on in order for the Attribute function to be available.

The attribute data for each character in the text area is written into the same address in the graphic area. The Attribute function is defined as follows.

Attribute RAM 1 byte

x	x	x	x	d3	d2	d1	d0

d3	d2	d1	d0	
0	0	0	0	Normal display
0	1	0	1	Reverse display
0	0	1	1	Inhibit display
1	0	0	0	Blink of normal display
				Blink of reverse display
				Blink of inhibit display

Display Mode

Code	Function	Operand
1001000	Display Off	-
1001xx10	Cursor on, blink off	-
1001xx11	Cursor on, blink on	-
100101xx	Text on, graphic off	-
100110xx	Text off, graphic on	-
100111xx	Text on, graphic on	-

1	0	0	1	D3	D2	D1	D0

D3: Cursor blink   1: on,   0:off
D2: Cursor display   1: on,   0:off
D3: Text display   1: on,   0:off
D3: Graphic display   1: on,   0:off

(Note)

It is necessary to turn on "Text display" and "Graphic display" in the following cases.
a)Combination of text/graphic display
b) Attribute function

Cursor pattern select

Code	Function	operand
10100000	1-line cursor	-
10100001	2-line cursor	-
10100010	3-line cursor	-
10100011	4-line cursor	-
10100100	5-line cursor	-
10100101	6-line cursor	-
10100110	7-line cursor	-
10100111	8-line cursor	-

When cursor display is on, this command selects the cursor pattern in the range 1-line to 8-line. The cursor address is defined by the Cursor Pointer Set command.
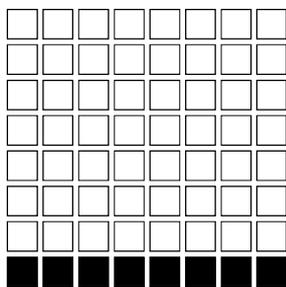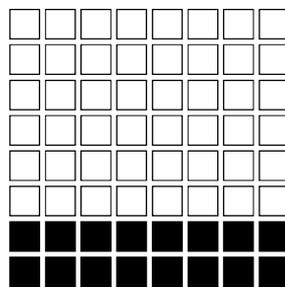
1-line cursor   2-line cursor   8-line cursor
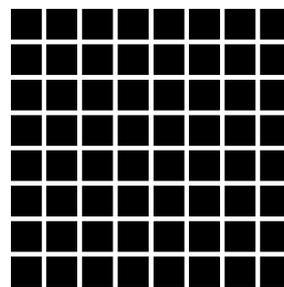
Data Auto Read/Write

Code	HEX.	Function	Operand
10110000	B0H	Set Data Auto Write	-
10110001	B1H	Set Data Auto Read	-
10110010	B2H	Auto Reset	-

This command is convenient for sending a full screen of data from the external display RAM. After setting auto mode, a Data Write (or Read) command must be sent between each datum. In Auto mode, the LCM cannot accept any other command.
The Auto Reset command must be sent to the LCM after all data has been sent, to clear Auto mode.

(Note)
a status check for Auto mode
(STA2, STA3 should be checked between sending of each datum. Auto Reset should be performed after checking STA3=1(STA2=1). Refer to the flowchart next page.

Auto Read Mode

```
┌─────────────────────┐
│   Auto Read Start   │
└─────────────────────┘
┌─────────────────────┐
│ Set address pointer │
└─────────────────────┘
┌─────────────────────┐
│   Status Check 1    │   STA0, 1
└─────────────────────┘
┌─────────────────────┐
│   Auto read B1H     │
└─────────────────────┘
┌─────────────────────┐
│   Status Check 2    │   STA2
└─────────────────────┘
┌─────────────────────┐
│     Data read       │
└─────────────────────┘
┌─────────────────────┐
│   Status Check 2    │
└─────────────────────┘
┌─────────────────────┐
│     Data read       │
└─────────────────────┘
┌─────────────────────┐
│   Status Check 2    │
└─────────────────────┘
┌─────────────────────┐
│   Auto reset B2H    │
└─────────────────────┘
┌─────────────────────┐
│        End          │
└─────────────────────┘
```

Auto Write Mode

```
┌─────────────────────┐
│   Auto Write Start  │
└─────────────────────┘
┌─────────────────────┐
│ Set address pointer │
└─────────────────────┘
┌─────────────────────┐
│   Status Check 1    │   STA0, 1
└─────────────────────┘
┌─────────────────────┐
│   Auto write B0H    │
└─────────────────────┘
┌─────────────────────┐
│   Status Check 2    │   STA3
└─────────────────────┘
┌─────────────────────┐
│     Data write      │
└─────────────────────┘
┌─────────────────────┐
│   Status Check 2    │
└─────────────────────┘
┌─────────────────────┐
│     Data write      │
└─────────────────────┘
┌─────────────────────┐
│   Status Check 2    │
└─────────────────────┘
┌─────────────────────┐
│   Auto reset B2H    │
└─────────────────────┘
┌─────────────────────┐
│        End          │
└─────────────────────┘
```

Set Address Pointer

```
┌──────────────────────────┐
│   Set Address Pointer    │
└──────────────────────────┘
┌──────────────────────────┐
│     Status Check 1       │   STA0, 1
└──────────────────────────┘
┌──────────────────────────┐
│    Set Address Data      │
│     (lower 8 bits)       │
└──────────────────────────┘
┌──────────────────────────┐
│     Status Check 1       │
└──────────────────────────┘
┌──────────────────────────┐
│    Set Address Data      │
│     (upper 8 bits)       │
└──────────────────────────┘
┌──────────────────────────┐
│     Status Check 1       │
└──────────────────────────┘
┌──────────────────────────┐
│   Set Address Pointer    │   Send Set Address Pointer Command
└──────────────────────────┘
┌──────────────────────────┐
│          End             │
└──────────────────────────┘
```

Status Check 1

```
   ┌──────────────────┐
   │  Status Check 1  │
   └──────────────────┘
        ┌───────────┐
   N    │  STA0=1,  │
 ◄──────│  STA1=1?  │
        └───────────┘
             │ Y
   ┌──────────────────┐
   │     Return       │
   └──────────────────┘
```

Status Check 2

```
   ┌──────────────────┐
   │  Status Check 2  │
   └──────────────────┘
        ┌───────────┐
   N    │  STA2=1,  │
 ◄──────│ (STA3=1)? │
        └───────────┘
             │ Y
   ┌──────────────────┐
   │     Return       │
   └──────────────────┘
```

Data Read/Write

Code	HEX.	Function	Operand
11000000	C0H	Data Write and increment ADP	Data
11000001	C1H	Data Read and increment ADP	-
11000010	C2H	Data Write and decrement ADP	Data
11000011	C3H	Data Read and decrement ADP	-
11000100	C4H	Data Write and Nonvariable ADP	Data
11000101	C5H	Data Read and Nonvariable ADP	-

This command is used for writing data from the MPU to external display RAM, and reading data from external display RAM to the MPU. Data Read should be executed after setting address using Set Address Pointer command. The address pointer can be automatically incremented or decremented using this command.

(Note)
   This command is necessary for each 1-bit datum.
   Refer to the following flowchart:

```
        ╭─────────────────────╮
        │  Data  Write  Start │
        ╰─────────────────────╯
                   │
        ┌─────────────────────┐   (Example)
        │ Set Address Pointer │   Address Pointer = 1000H
        └─────────────────────┘
                   │
        ┌─────────────────────┐
        │   Status  Check  1  │   STA0,1
        └─────────────────────┘
                   │
        ┌─────────────────────┐   (Example)
        │   Set  Write  Data  │   Data = AAH
        └─────────────────────┘
                   │
        ┌─────────────────────┐
        │   Status  Check  1  │
        └─────────────────────┘
                   │
        ┌─────────────────────┐   AAH is written in 1000H address
        │   Data  Write  C0H  │   Address Pointer is 1001H
        └─────────────────────┘
                   │
           ╭───────────────╮
           │      End      │
           ╰───────────────╯
```
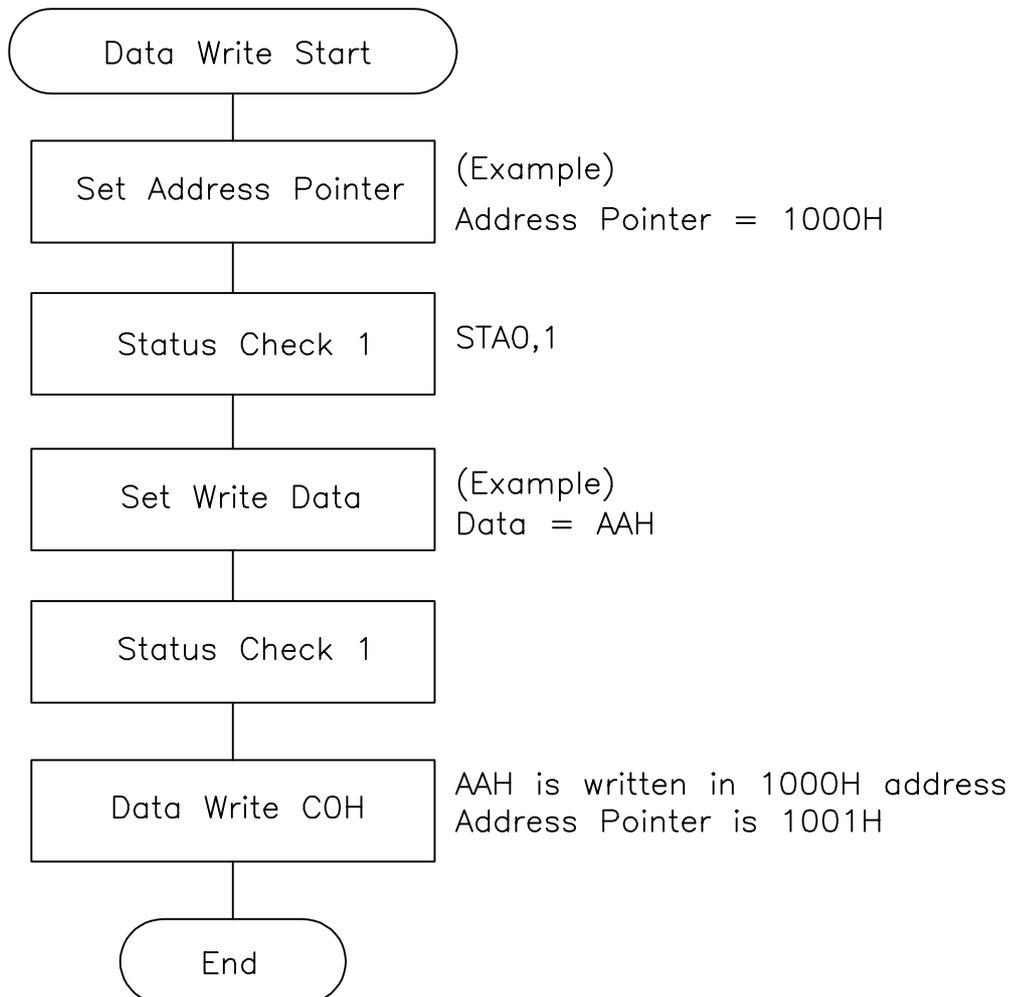
Screen Peak

Code	HEX.	Function	Operand
11100000	E0H	Screen Peek	-

This command is used to transfer 1 byte of display data to the data stack; this byte can then be read from MPU by data access. The logical combination text and graphic display data on the LCD screen can be read by this command. The status (STA6) should be checked just after the Screen Peek command. If the address determined by the Set Address Pointer command is not in the graphic area, this command is ignored and a status (STA6) flag is set. Refer to the following flowchart:
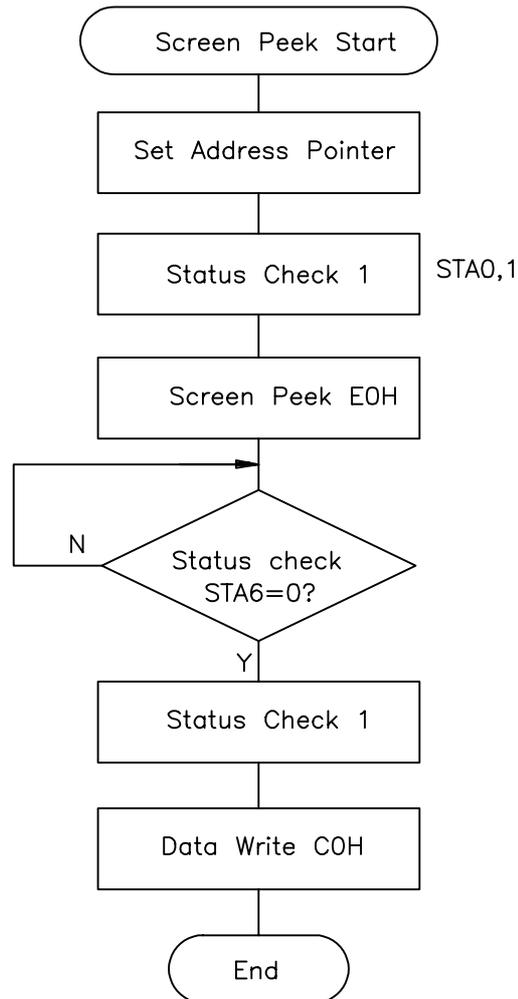
```
                  ╭─────────────────────╮
                  │  Screen Peek Start  │
                  ╰─────────────────────╯
                            │
                  ┌─────────────────────┐
                  │ Set Address Pointer │
                  └─────────────────────┘
                            │
                  ┌─────────────────────┐
                  │  Status Check 1     │   STA0,1
                  └─────────────────────┘
                            │
                  ┌─────────────────────┐
                  │  Screen Peek E0H    │
                  └─────────────────────┘
                            │
            ┌──────────────▶│
            │           ╱─────────────╲
         N  │          ╱  Status check  ╲
            └─────────◀     STA6=0?      ▶
                       ╲               ╱
                        ╲─────────────╱
                            │ Y
                  ┌─────────────────────┐
                  │  Status Check 1     │
                  └─────────────────────┘
                            │
                  ┌─────────────────────┐
                  │  Data Write C0H     │
                  └─────────────────────┘
                            │
                  ╭─────────────────────╮
                  │        End          │
                  ╰─────────────────────╯
```

(Note)
This command is available when hardware column number and software column number are the same. Hardware column number is related to Set Text Area and Set Graphic Area command.

Screen Copy

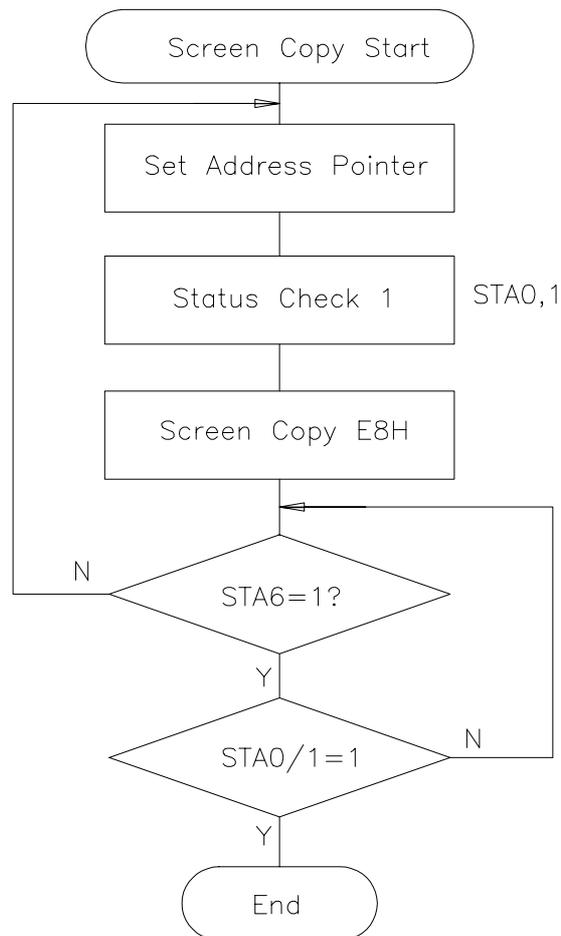| Code	HEX.	Function	Operand
11101000	E8H	Screen Copy	-

This command copies a single raster line of data to the graphic area.
The start point must be using the Set Address Pointer command.
(Note 1)

If the attribute function is being used, this command is not available.

(With attribute data is graphic area data)
(Note 2)

This command is not working for Dual-Scan because the controller IC T6963C cannot separate the upper screen data and lower screen.

```
         ( Screen Copy Start )
                  │
         ┌────────▷──────────┐
         │  Set Address Pointer │
         └──────────────────────┘
                  │
         ┌──────────────────────┐
         │   Status Check 1     │  STA0,1
         └──────────────────────┘
                  │
         ┌──────────────────────┐
         │   Screen Copy E8H    │
         └──────────────────────┘
                  │
      N      ◁────┤
    ┌────────< STA6=1? >
    │             │ Y
    │        < STA0/1=1 >───N──┐
    │             │ Y          │
    │        ( End )           │
```
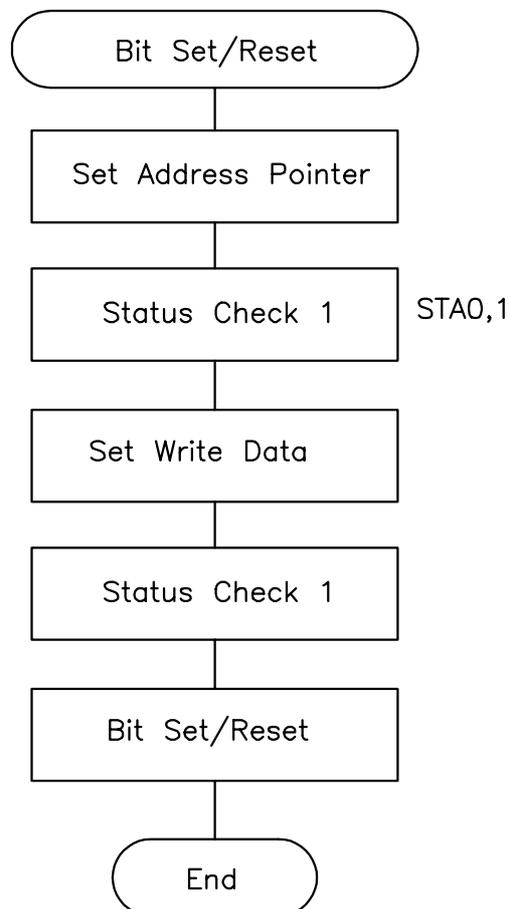
(Note)

This command is available when hardware column number and software column number are the same. Hardware column number is related to MD2 and MD3 setting. Software column number is related to Set Text Area and Set Graphic Area command.

Bit Set/Reset

Code	Function	Operand
11110xxx	Bit Reset	-
11111xxx	Bit Set	-
1111x000	Bit 0 (LSB)	-
1111x001	Bit 1	-
1111x010	Bit 2	-
1111x011	Bit 3	-
1111x100	Bit 4	-
1111x101	Bit 5	-
1111x110	Bit 6	-
1111x111	Bit 7	-

This command use to set or reset a bit of byte specified by the address pointer. Only 1 bit can be set/reset at a time. Refer to the following flowchart:

```
      ( Bit  Set/Reset )
              |
    [ Set Address  Pointer ]
              |
    [ Status  Check  1 ]    STA0,1
              |
    [ Set  Write  Data ]
              |
    [ Status  Check  1 ]
              |
    [ Bit  Set/Reset ]
              |
          ( End )
```

CG-ROM CHARACTER CODE MAP

LSB/MSB	0	1	2	3	4	5	6	7	8	9	A	B	C	D	E	F
0		!	"	#	$	%	&	'	(	)	*	+	,	-	.	/
1	0	1	2	3	4	5	6	7	8	9	:	;	<	=	>	?
2	@	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O
3	P	Q	R	S	T	U	V	W	X	Y	Z	[	\	]	^	_
4	`	a	b	c	d	e	f	g	h	i	j	k	l	m	n	o
5	p	q	r	s	t	u	v	w	x	y	z	{	|	}	~	
6	Ṣ	ü	é	å	ä	à	å	ś	ë	ë	ë	ï	ï	ï	Ä	Å
7	É	æ	Æ	ö	ö	ö	ü	ü	ÿ	Ö	Ü	¢	£	¥	₨	ƒ

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DSIPLAY PATTERN

	seg1	seg2	seg3	seg4	seg5	seg6	seg7	seg8		seg233	seg234	seg235	seg236	seg237	seg238	seg239	seg240
com1	D7	D6	D5	D4	D3	D2	D1	D0	---------	D7	D6	D5	D4	D3	D2	D1	D0
com2	D7	D6	D5	D4	D3	D2	D1	D0	---------	D7	D6	D5	D4	D3	D2	D1	D0

The first data

240x64 Dots Matrix

com63	D7	D6	D5	D4	D3	D2	D1	D0	---------	D7	D6	D5	D4	D3	D2	D1	D0
com64	D7	D6	D5	D4	D3	D2	D1	D0	---------	D7	D6	D5	D4	D3	D2	D1	D0

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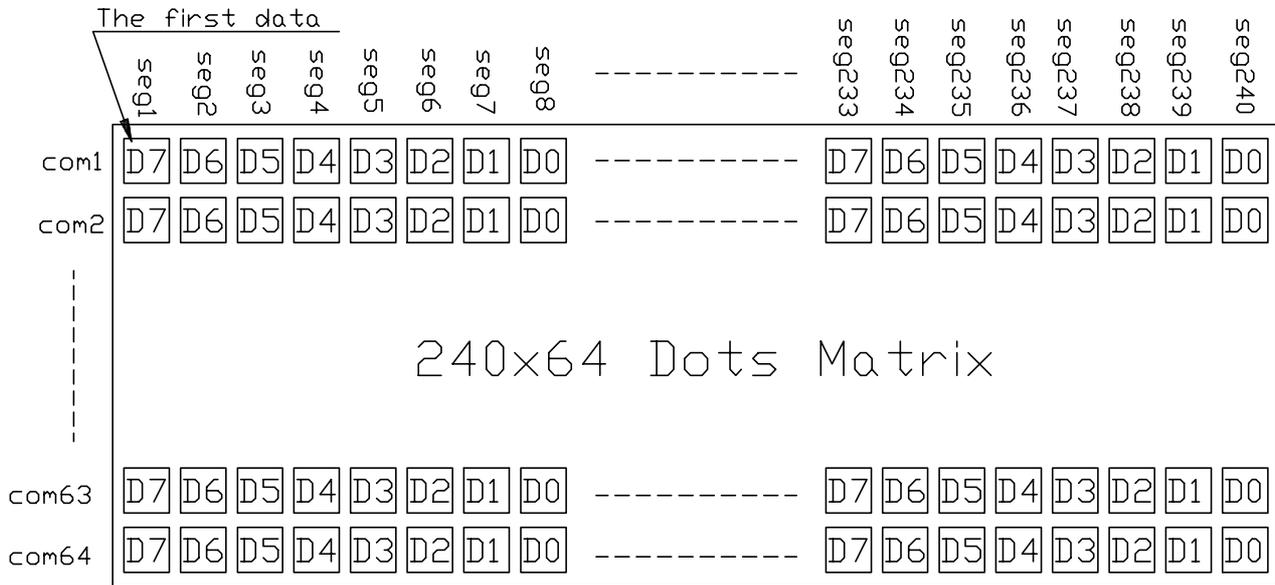
RELIABILITY TEST

No	Item	Conditions		Note
1	High Temp. Operation	50℃	240 HR	
2	High Temp. Storage	70℃	240 HR	
3	Low Temp. Operation	0℃	240 HR	
4	Low Temp. Storage	-20℃	240 HR	
5	High Temp./Humid Storage	40℃ 90%RH	240 HR	
6	Thermal Shock	0℃ ,30min 50℃ ,30min	10 cycles	
7	Vibration Test (IEC-68-2-6)	Frequency : 10~55 Hz Duration : 20 times, 6 min/time Amplitude : 0.75 mm	-	
8	Shock (IEC 68-2-27)	Duration : 11 mS Acceleration : 100g	-	X, Y, Z direction

APPEARANCE CHECK

CONDIITON OF APPEARANCE CHECK:
  (1)Specimen shall be checked by eyes in distance of 30cm under 40w-fluorescence lamp.
  (2)Checking direction shall be in 45 degree from perpendicular line op specimen surface.

Active Area
Viewing Area
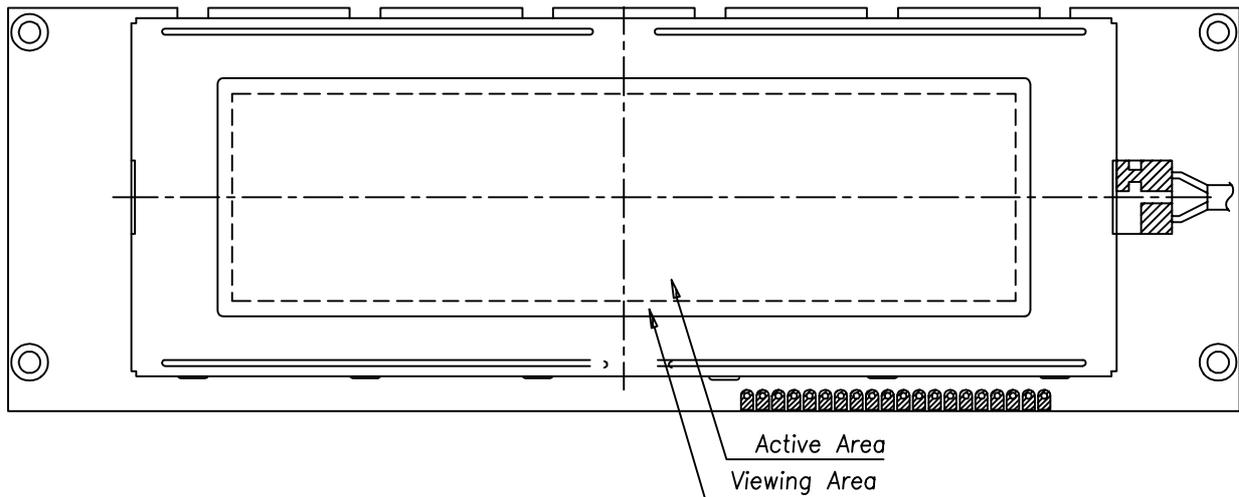
HANDLING PRECAUTIONS

(1)Treat polarizer very carefully since it is easy to be damaged.

(2)When cleaning the display surface, use soft cloth (e.g. gauss) with a solvent (recommended below) and wipe lightly.

◆ ethyl alcohol

◆ iso-prcolol

Do not wipe the display surface with dry or hard materials that will damage the polarizer surface.

Do not use the following solvents:

◆ water

◆ ketone

◆ aromatics

(3)Direct current causes electro-chemical reaction with remarkable degradation of the display quality.　Give careful consideration to prevent direct current at ON/OFF timing and during operation.

(4)Avoid strong shock and drop from the height.

(5)To prevent LCD panels from degradation, do not operate or store them exposed directly to sunshine or high temperature/humidity.

(6)Give careful consideration to avoid electrical static discharge with causes uneven contrast.

(7)Even a small condensation on the contact pads (terminals) causes electro-chemical reaction which makes missing row and column. Give careful attention to avoid condensation.　When assembling with zebra connector, clean the surface of the pads with alcohol and keep the air very clean.

LCD PRODUCT QUALITY STANDARD
DISPLAY APPEARANCE

No	Item	Criteria

1	inclusions (black spot, white spot, dust)	(1)round type
		  diameter mm(a*) no of defect*
		  a≦0.20 neglect
		  0.20＜a≦0.35 5max
		  0.35＜a none
		(2)linear type
		  length mm(l) width mm(W) no. of defect
		   na W≦0.03 neglect
		   l≦3 0.03＜W≦0.08 6
		   3＜l 0.08＜W none

2	scratch	1.scratch on protective film is permitted.
		2.scratch on polarizer shall be as follow:
		  (1)round type
		    diameter mm(a*) no of defect
		       a≦0.15 neglect
		    0.15＜a≦0.20 2 max
		    0.20＜a none
		  (2)linear type
		be judged bye 1.-(2) linear type

3	dent	diameter ＜ 1.5mm
4	bubble	not exceeding 0.5mm average diameter is acceptable between glass and polarizing film
5	pin hole	(a+b)/2 ≦ 0.15mm maximum number: ignored 0.15＜(a+b)/2≦0.20mm maximum number:10
6	dot defect	(a+b)/2≦0.20mm maximum number: ignored 0.20＜(a+b)/2≦0.30mm maximum number:5 x=width
7	contrast irregularity(spot)	diameter spec no of defect a≦0.50mm neglect 0.50＜a≦0.75 5 0.75＜a≦1.00 3 1.00＜a none
8	dot width	design width ±15%
9	color tone and uniformity	obvious uneven color is not permitted

REVISION HISTORY

Revision Content	Page	Date