TOSHIBA Field Effect Transistor Silicon N Channel MOS Type (DTMOS )

# **TK13A65U**

### **Switching Regulator Applications**

Unit: mm

Low drain-source ON resistance: RDS (ON) = 0.32 (typ.)

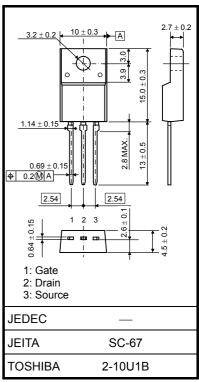
• High forward transfer admittance:  $|Y_{fS}| = 8.0 \text{ S (typ.)}$ 

• Low leakage current:  $I_{DSS} = 100 \mu A (V_{DS} = 650 V)$ 

• Enhancement-mode:  $V_{th} = 3.0 \text{ to } 5.0 \text{ V (VDS} = 10 \text{ V, ID} = 1 \text{ mA)}$ 

#### **Absolute Maximum Ratings (Ta = 25°C)**

Characteristics		Symbol	Rating	Unit
Drain-source voltage		$V_{DSS}$	650	V
Gate-source voltage		$V_{GSS}$	±30	V
Drain current	DC (Note 1)	ΙD	13	
	Pulse (t = 1 ms) (Note 1)	I <sub>DP</sub>	26	Α
Drain power dissipati	on (Tc = 25°C)	P <sub>D</sub>	40	W
Single pulse avalanche energy (Note 2)		E <sub>AS</sub>	86	mJ
Avalanche current		I <sub>AR</sub>	13	Α
Repetitive avalanche energy (Note 3)		E <sub>AR</sub>	4.0	mJ
Channel temperature		T <sub>ch</sub>	150	°C
Storage temperature range		T <sub>stg</sub>	-55 to 150	°C



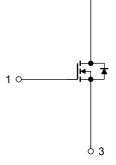
Weight: 1.7 g (typ.)

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

#### **Thermal Characteristics**

Characteristics	Symbol	Max	Unit
Thermal resistance, channel to case	R <sub>th (ch-c)</sub>	3.125	°C/W
Thermal resistance, channel to ambient	R <sub>th (ch-a)</sub>	62.5	°C/W

**Internal Connection** 



Note 1: Please use devices on conditions that the channel temperature is below 150°C.

Note 2:  $V_{DD}$  = 90 V,  $T_{ch}$  = 25 °C (initial), L = 0.9 mH,  $R_G$  = 25  $\Omega$ ,  $I_{AR}$  = 13 A

Note 3: Repetitive rating: pulse width limited by maximum channel temperature

This transistor is an electrostatic sensitive device. Please handle with caution.

# **Electrical Characteristics (Ta = 25°C)**

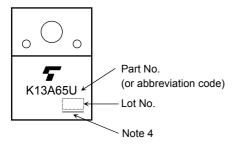
Charac	teristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage current		I <sub>GSS</sub>	$V_{GS} = \pm 30 \text{ V}, V_{DS} = 0 \text{ V}$	_	_	±1	μА
Drain cut-off current		I <sub>DSS</sub>	V <sub>DS</sub> = 650 V, V <sub>GS</sub> = 0 V	_	_	100	μА
Drain-source break	down voltage	V (BR) DSS	$I_D = 10 \text{ mA}, V_{GS} = 0 \text{ V}$	650	_	_	V
Gate threshold volt	age	V <sub>th</sub>	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 1 mA	3.0		5.0	V
Drain-source ON re	esistance	R <sub>DS</sub> (ON)	V <sub>GS</sub> = 10 V, I <sub>D</sub> = 6.5 A		0.32	0.38	Ω
Forward transfer a	dmittance	Y <sub>fs</sub>	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 6.5 A	2.0	8.0		S
Input capacitance		C <sub>iss</sub>			950		
Reverse transfer capacitance		C <sub>rss</sub>	V <sub>DS</sub> = 10 V, V <sub>GS</sub> = 0 V, f = 1 MHz		47		pF
Output capacitance		C <sub>oss</sub>			2300		
Switching time	Rise time	t <sub>r</sub>	10 V I <sub>D</sub> = 6.5 A V <sub>OUT</sub> V <sub>GS</sub>	_	30	_	
	Turn-ON time	t <sub>on</sub>	$\begin{array}{c c} & & & \\ \hline 50 \ \Omega & & & \\ \hline & & \\ \hline & & \\ \hline & & & \\ \hline & & $	_	65	_	ns
	Fall time	t <sub>f</sub>			8	_	
	Turn-OFF time	t <sub>off</sub>			80	_	
Total gate charge		Qg		_	17	_	
Gate-source charge		Qgs	$V_{DD} \approx 400 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 13 \text{ A}$	_	10	_	nC
Gate-drain charge		Q <sub>gd</sub>		_	7	_	

# **Source-Drain Ratings and Characteristics (Ta = 25°C)**

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Continuous drain reverse current (Note 1)	I <sub>DR</sub>	_	_	_	13	Α
Pulse drain reverse current (Note 1)	I <sub>DRP</sub>	_	_	_	26	Α
Forward voltage (diode)	V <sub>DSF</sub>	I <sub>DR</sub> = 13 A, V <sub>GS</sub> = 0 V	_	_	-1.7	V
Reverse recovery time	t <sub>rr</sub>	I <sub>DR</sub> = 13 A, V <sub>GS</sub> = 0 V,	_	430	_	ns
Reverse recovery charge	Q <sub>rr</sub>	dI <sub>DR</sub> /dt = 100 A/μs	_	7.0	_	μС

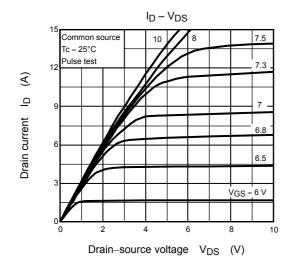
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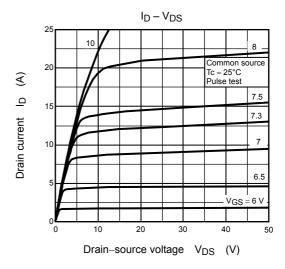
# Marking

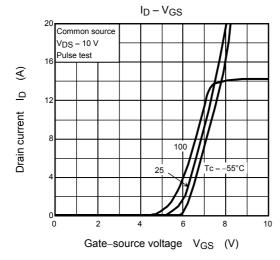


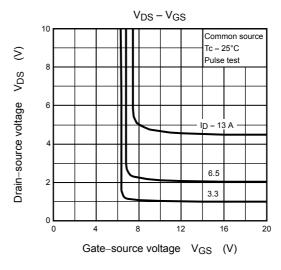
Note 4 : A line under a Lot No. identifies the indication of product Labels [[G]]/RoHS COMPATIBLE or [[G]]/RoHS [[Pb]]

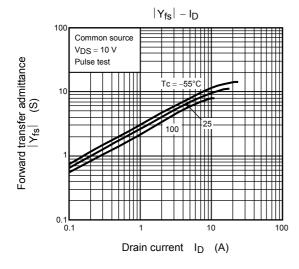
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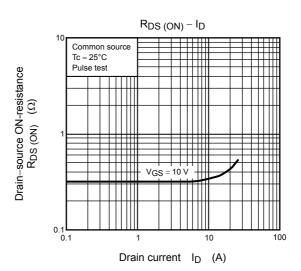


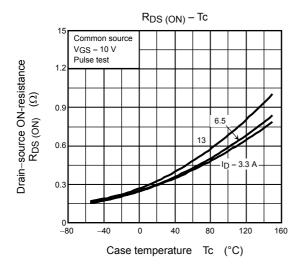


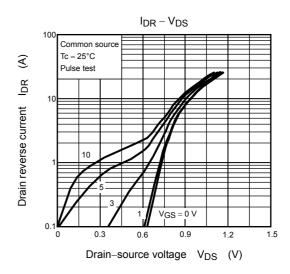


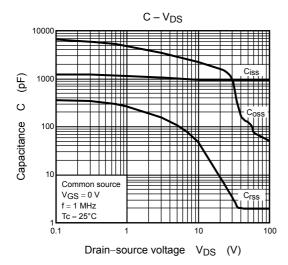


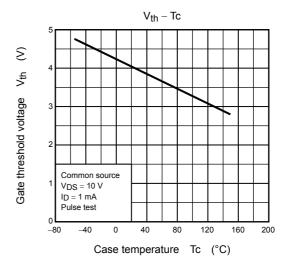


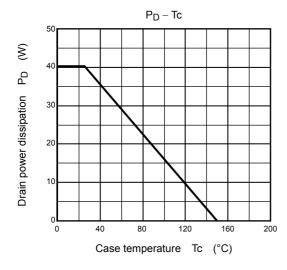


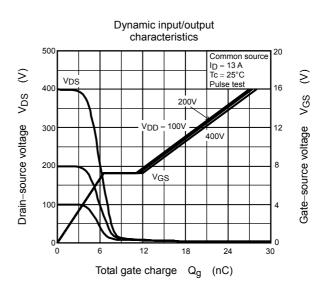




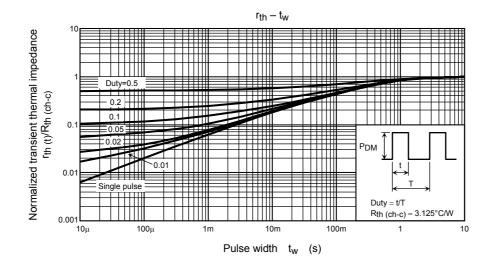


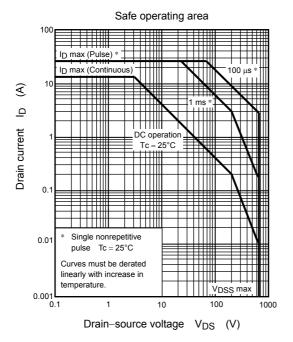


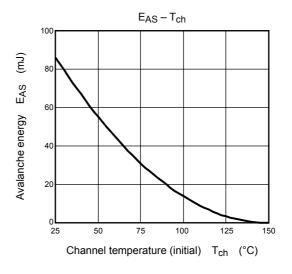


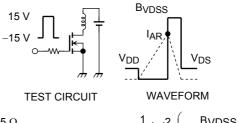


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$$\begin{aligned} R_G &= 25~\Omega \\ V_{DD} &= 90~V,~L = 0.9~mH \end{aligned} \qquad E_{AS} &= \frac{1}{2} \cdot L \cdot I^2 \cdot \left( \frac{BVDSS}{BVDSS - V_{DD}} \right) \end{aligned}$$

5 2009-03-24

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