

SPECIFICATION

FOR LCD MODULE

Version 0.1

CUSTOMER :

CUSTOMER NO . :

PRODUCT TYPE : Graphics LCM

MODULEL NO. : TM320240ACCWVGWSA

CUSTOMER APPROVED	
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REVISION RECORD

Version	Revision Items	Name	Date
0.0	First release	Xiefei	2006.7.18
0.1	Add power supply circuit	TP	2006.9.30



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1. LCD Module Part Numbering System

TM	320240	A	C	C	W	V	G	W	S	A
----	--------	---	---	---	---	---	---	---	---	---

1 2 3 4 5 6 7 8 9 10

NO.		Explanation					
1)	TIAN	TIANMA module indicating					
2	Modu	le type: 320 columns × 240 rows, 6 DIGITS					
3	TIAN	MA module series (A,B,C,D)					
4	LCD 1	type					
4)	C	Positive, FSTN					
(5)	Backl	Backlight type					
0	С	C Transflective, LED					
6	Temperature range						
	W Wide temperature						
7	Viewing Angle						
		V Wide viewing direction					
8	Techn	Technology					
	G	G COG					
9	The co	olor of backlight					
	W	White					
10	Modu	le type					
10	S	Standard					
	Funct	ion choice					
	A	Without any function					



2 Basic specification

Item	Contents					
I CD 4	□TN	□STN	■FSTN			
LCD type	■positive	□negative				
LCD Duty	□64	□128	2 40	□16		
LCD Bias	□1/9	□1/12	■ 1/16	□1/5		
Polarizer	□reflective	■ transflective	□transmissive			
LCD background color	□grey	□yellow/green	□blue-black	white		
Segment color	□white	■blue-black				
Backlighting	■LED	□EL	□CFL			
LED type	■edge	□area				
Backlighting color	white	□yellow/green	□blue			
View direction	□6:00	□12:00	□9:00	■ Wide View		
Operating temperature	□0°C~50°C	■ -20°C~70°C				
Storage temperature	□-20°C~60°C	■-30°C~80°C				
Controller						
Frame	□SPCC(black)	☐Zinc plated	□stainless steel	■Without		
Technology	□SMT	□СОВ	■COG			
Power supply	□single +3.3V	■single +5.0V	□dual	□triplex		
Data Transfer	■8 Bit Parallel	☐4 Bit Parallel	□Serial			

2.1 General

The TM320240ACCWVGWSA, Graphics LCM unit consists of 320 columns(segment)×240 rows(common) dot-matrix LCD panel, LCD driver on a single PCB without controller LSI.

2.2 Features

- ◆ Wide viewing direction. Wide Operating temperature.
- Requirements on environmental protection: RoHS.
- ◆ The contrast can be control by add a VR outside. Refer to page 8-"circuit".

Notes:

- Color tone can slightly change with temperature and driving voltage.
- Color tone will be changed by backlight.



3 Mechanical data

Parameter	Standard Value	Unit
Display type	Dot-matrix module	
Number of dots (W×H)	320×240	
View area (W×H)	79.8 × 60.6	mm
Active Area (W×H)	76.785 × 57.585	mm
Dot Size (W×H)	0.225 × 0.225	mm
Dot Pitch (W×H)	0.24 × 0.24	mm
Module size(W \times H \times D)	91.5 × 75.0 × 6.6	mm
Module total weight (approx)	45	g
Module outline dimensions	Refer to page 6-"Outline drawing"	

4 Absolute maximum ratings

(Without LED backlighting ,Ta=25°C)

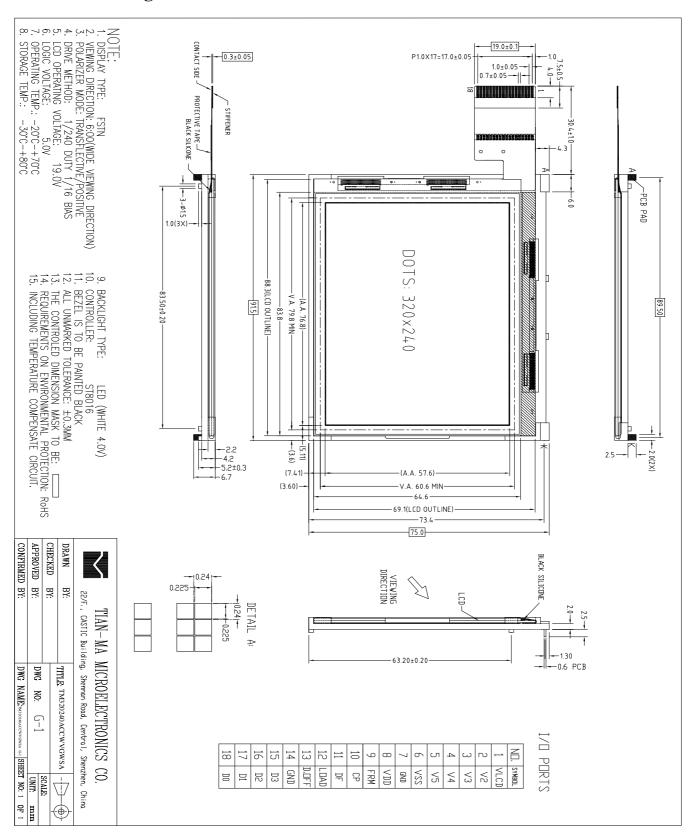
Parameter	Symbol	Min	Max	Unit	Remark
Logic circuit supply voltage	V_{DD}	-0.3	7.0	V	
LCD driving voltage	V_{LCD}	-0.3	19.0	V	
Operating temperature range	Тор	-20	+70	$^{\circ}$ C	No
Storage temperature range	Tst	-30	+80	$^{\circ}$ C	Condensation

Notes:

- LCD operating voltage V_{OP}=V_{LCD} −V_{SS}.
- If the module is above these absolute maximum ratings. It may become permanently damaged. Using the module within the following electrical characteristic conditions are also exceeded, the module will malfunction and cause poor reliability, and its service life will reduce.
- $V_{LCD} > V_{SS}$ must be maintained.



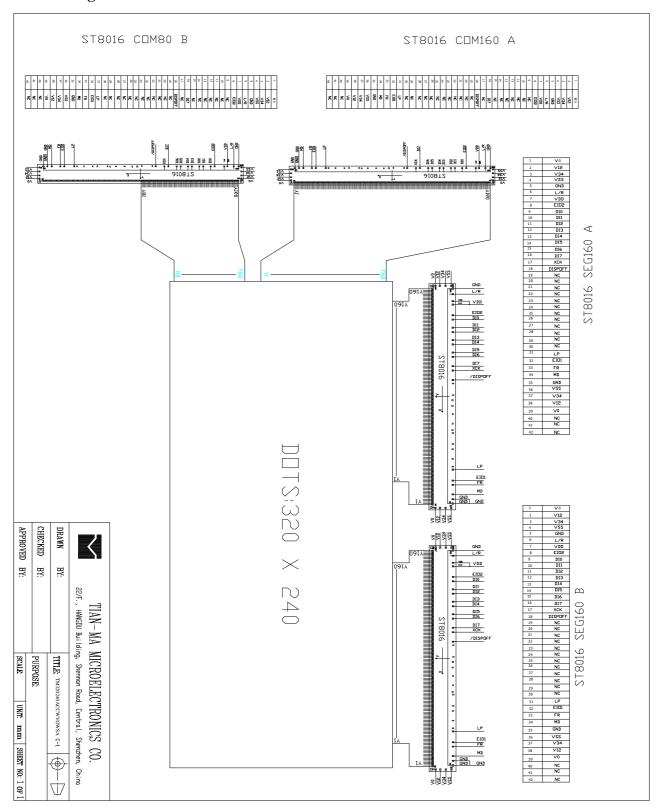
5. Outline Drawing





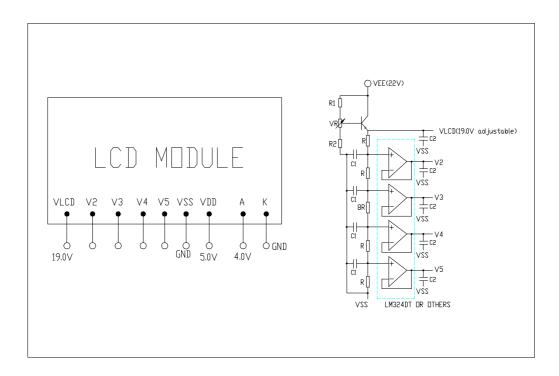
6. Circuit

6.1 Block Diagram





6.2 Recommend power supply circuit Only for reference.



Note:

• If you want to control the module contrast outside, please add the VR and its circuit.



7 Pin connections

Pin No.	Symbol	Level	Description
1	VLCD		
2	V2	-	Bias power supply pins for LCD drive voltage.
3	V3	-	Ensure that the voltages are set such that:
4	V4	-	VLCD > V2 > V3 > V4 > V5 > VSS
5	V5	-	
6	VSS	0V	Ground pin
7	GND	0 V	Ground pin
8	VDD	5V	Logic system power supply pin
9	FRM	H/L	Input/output pins for chip selection
10	СР	H/L	Clock input pin for taking display data
11	DF	H/L	AC signal input pin for LCD drive waveform
12	LOAD	H/L	Latch pulse input pin for display data
13	D.OFF	H/L	Control input pin for output of non-select level
14	GND	0V	Ground pin
15	D3	H/L	Data bit3
16	D2	H/L	Data bit2
17	D1	H/L	Data bit1
18	D0	H/L	Data bit0



8. Timing characteristics

8.1 COMMAND

The module TM320240ACCWVGWSA do not include the controller LSI, so there do not offer the code of command definition, it depend on the controller you choose. Please refer the SPEC. of controller you use.

8.2 Interface Timing characteristics

Note: Please refer to IC:	ST8016	data sheet for more	details.

AC Characteristics

(Segment Mode 2) (Vss = 0 V, Vpp = $+5.0\pm0.5$ V, Vp = +15.0 to +30.0 V, TopR = -25 to +85 °C)

PARAMETER	SYMBOL	CONDITIONS	MIN.	TYP.	MAX.	UNIT	NOTE
Shift clock period	twck	te,tr = 10ns	66			ns	1
Shift clock "H" pulse width	twckH		23			ns	
Shift clock "L" pulse width	twckL		23			ns	
Data setup time	tos		15			ns	
Data hold time	tон		23			ns	
Latch pulse "H" pulse width	twlph		30			ns	
Shift clock rise to latch pulse rise time	tlo		0			ns	
Shift clock fall to latch pulse fall time	tsı		50			ns	
Latch pulse rise to shift clock rise time	tıs		30			ns	
Latch pulse fall to shift clock fall time	tьн		30			ns	
Latch pulse fall to shift clock rise time	t∟sw		50			ns	
Enable setup time	ts		15			ns	
Input signal rise time	tr⊲				50	ns	2
Input signal fall time	t⊧				50	ns	2
DISPOFF removal time	tsp		100			ns	
DISPOFF "L" pulse width	twoL		1.2			μs	
Output delay time (1)	t⊳	CL = 15 pF			41	ns	
Output delay time (2)	tpD1, t pD2	CL = 15 pF			1.2	μs	
Output delay time (3)	t PD3	CL = 15 pF			1.2	μs	

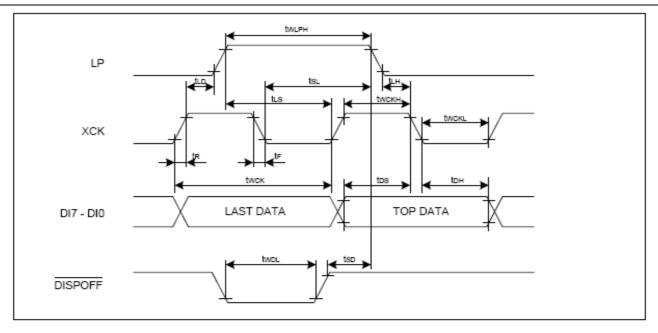
NOTES:

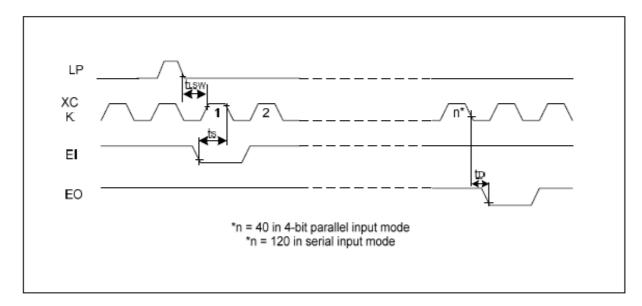
- 1. Takes the cascade connection into consideration.
- 2. (twck twckh twckl)/2 is maximum in the case of high speed operation.

(Common Mode) (Vss = 0 V, Vpp = +2.5 to +5.5 V, V0 = +15.0 to +30.0 V, TopR = -25 to +85° C)

PARAMETER	SYMBOL	CONDITIONS	MIN.	TYP.	MAX.	UNIT
Shift clock period	twlp	t _R ,t _F = 20ns	250			ns
Shift clock "H" pulse width	twlph	$VDD = +5.0 \pm 0.5V$	15			ns
Stillt clock 11 paise width	WLPH	VDD = +2.5 + 4.5V	30			ns
Data setup time	tsu		30			ns
Data hold time	tн		50			ns
Input signal rise time	tr				50	ns
Input signal fall time	t⊧				50	ns
DISPOFF removal time	tsp		100			ns
DISPOFF "L" pulse width	twoL		1.2			μs
Output delay time (1)	toL	CL = 15 pF			200	ns
Output delay time (2)	tpp1, t pp2	CL = 15 pF			1.2	μs
Output delay time (3)	t pp3	CL = 15 pF			1.2	μs







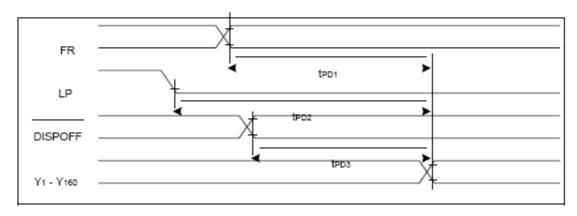
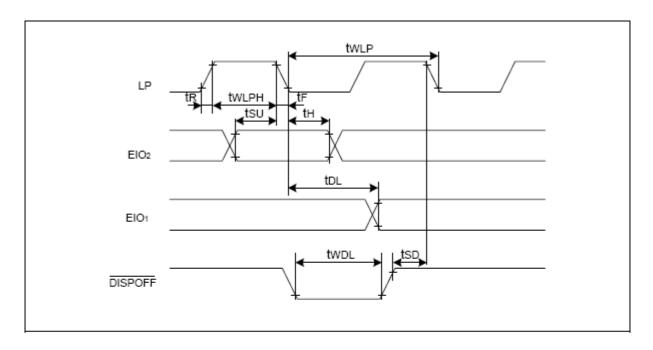


Figure 1. Timing Chart of Segment Mode





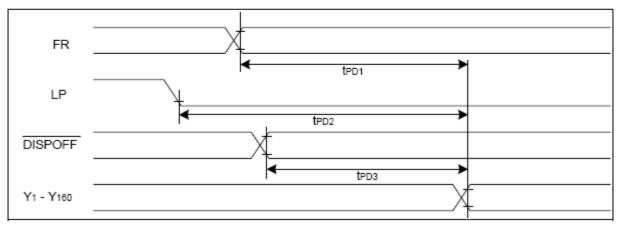


Figure 2. Timing Chart of Common Mode

8.3 character generator code map

Please refer the SPEC of controller.

9 Electrical characteristics

 $V_{SS}=0V$, $Ta=25^{\circ}C$

Item	Symbol	Condition	MIN	ТҮР	MAX	UNIT
Logic circuit supply voltage	V_{DD}		4.5	5.0	5.5	V



			-20°C	-	TBD	-	
			0℃	19.1	19.3	19.5	
LCD operation voltage		V_{op}	25℃	18.8	19.0	19.2	
			40℃	18.5	18.7	18.9	
			70°C	-	TBD		
Input voltage for logic	"H"level	V_{IH}		$0.8V_{DD}$			
circuit	"L"level	$ m V_{IL}$				$0.2V_{DD}$	
Output voltage for logic	"H"level	V _{OH}	V 50V	V _{DD} -0.4			
circuit	"L"level	V _{OL}	V_{DD} =5.0V			0.4	
Logic power supply curren	nt	I_{CC}					
(Without backlighting and		(F _{OSC}			30	35	mA
Display character)		=270KHz)					
Used driver IC			S	Γ8016 of Si	tronix		

10 LED backlight characteristics

Ta=25°C

Item	Symbol	Condition	MIN.	TYP.	MAX.	Unit
Forward voltage	$V_{\rm f}$	$I_f = 75 \text{mA}$		4.0	4.2	V
Forward current	I_{f}	$V_f = 4.0V$		75	100	mA
Reverse voltage	V _r				10	V
Reverse Current	I_r			45	60	uA
Luminous intensity*	Вр		250			cd/m ²
Luminous Uniformity*	△Bp	T 75 A	75			%
Color coordinate*	X	$I_f = 75 \text{mA}$	0.283		0.330	
	Y		0.276		0.339	

Note:

- Measured at the bare LED backlight unit.
- If the backlight is above these maximum ratings for long time, the service life of the LED backlight will reduce or it will cause poor reliability.

11 Optical Characteristics

11.1 Optical Characteristics

Ta=25°C

Item	Symbol	Condition	Min.	Тур.	Max.	Unit
	•			0.1		

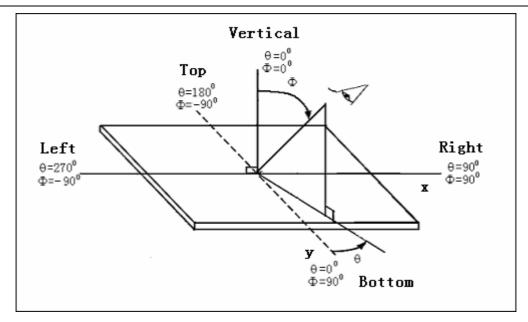


Transm	ission	Т			10%	16%		
Viewine	Viewing Angle		C=>2	$\theta_{\rm y} = 0^{\circ} \qquad -45 -60$			Dag	
Viewing			Cr≥2	$\theta_{\rm x}$ =0°		-30 30		Deg
	A 1	θ_{x}	C > 5	θ _y =0°				D.
Viewing	Angle	θ_{y}	Cr≥5	$\theta_x = 0^{\circ}$				Deg
Contrast	t Ratio	Cr	$\theta_x = 0^{\circ}$ $\theta_y = 0^{\circ}$			10.0		
							250	
	Turn on	Ton	Г	Tamb= +22°C				
	Turn on	Ton		0℃				
Response				-10℃ -20℃				ms
Time							200	1115
	TD CC T	TD CC		$\theta_y = 0^\circ$				
	Turn off	Toff	A wav	A wave, f=70Hz				

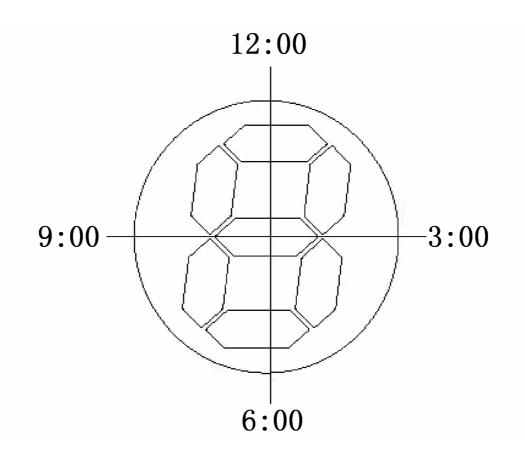
11.2 Definition of Optical Characteristics

11.2.1 Definition of Viewing Angle



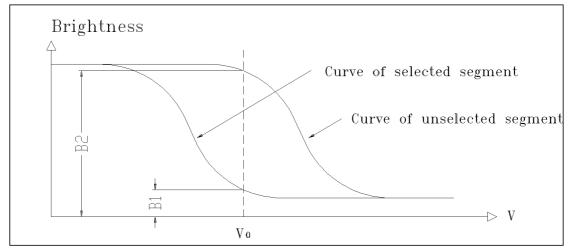


11.2.2 Indication of Viewing Angle





11.2.2 Definition of Contrast Ratio



Contrast Ratio = B2/B1 = $\frac{\text{unselected state brightness}}{\text{selected state brightness}}$

Measuring Conditions: 1) Ambient Temperature: 25℃

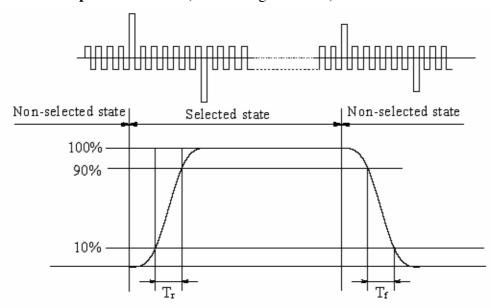
2) Frame frequency: 70Hz

3) Operating voltage: Vop=19V

4) Appling waveform: 1/240 duty 1/16 bias

5) View angle (θ , Φ): (0° , 0°)

11.2.3 Definition of Response time Test (LCD using DMS501)



Turn on time: $t_{on} = t_r$ Turn off time: $t_{off} = t_f$

Measuring Condition: 1) Operating Voltage: Vop=19V
2) Frame frequency: 70Hz

3) Appling waveform: 1/240duty 1/16bias

4) View angle (θ , Φ): (0° , 0°)



12 Reliability

12.1 Content of Reliability Test

Ta=25°C

No.	Test Item	Content of Test	Test condition	Criterion
1	High Temperature Storage	Endurance test applying the high storage temperature for a long time	80°C 240H	
2	Low Temperature Storage	Endurance test applying the low storage temperature for a long time	-30°C 240H	
3	High Temperature Operation	Endurance test applying the electric stress (voltage and current) and the thermal stress to the element for a long time	70℃ 240H	Remark1
4	Low Temperature Operation	Endurance test applying the electric stress under low temperature for a long time	-20°C 240H	Remark2 Remake3
5	High Temperature /Humidity Storage	Endurance test applying the high temperature and high humidity storage for a long time	40℃ 90%RH 240H	Remake4
6	Temperature Cycle	Endurance test applying the low and high temperature cycle -30°C \rightarrow 25°C \rightarrow 80°C \rightarrow 25°C \rightarrow 30min 5min 5min 1 cycle	-30°C/80°C 10 cycles	
7	Vibration Test (package state)	Endurance test applying the vibration during transportation	10Hz~150Hz, 100m/s ² , 120min	
8	Shock Test (package state)	Endurance test applying the shock during transportation	Half- sine wave, 300m/s ² ,18ms	Remark1 Remark5 Remark6
9	Atmospheric Pressure Test	Endurance test applying the atmospheric pressure during transportation by air	25kPa 16H Restore 2H	

Notes:

- 1. Each test item applies for a test sample only once, The test sample can not be used again in any other test item.
- 2. The test sample is inspected after 2 hours or more storing at room temperature and room humidity after each test item is finished.
- 3. The criteria refer to 12.2.



12.2 Inspection of criteria

Remark NO.	Content
1	Functional test is OK. Missing Segment, shorts, unclear segment, nondisplay, display abnormally, liquid crystal leak are unallowable.
2	After testing, cosmetic defects should not happen, no low temperature bubbles, seal loose and fall, frame rainbow, ACF bubble growing are unallowable in the appearance test.
3	Total current consumption should not be over 10% of initial value.
4	After tests being executed, Contrast must be larger than 70% of its initial value prior to the tests.
5	No glass crack, chipped glass, end seal loose frame crack and so on.
6	No structure loose and fall.

12.3 LCD module service life

Functions, performance, appearance, etc. shall be free from remarkable deterioration within 100,000 hours under ordinary operating and storage conditions room temperature ($25^{\circ}\text{C} \pm 10^{\circ}\text{C}$).

12.4 Definition of module service life

- Contrast becomes 30% of initial value.
- Current consumption becomes threes times higher than initial value.
- Remarkable alignment deterioration occurs in LCD cell layer.
- Unusual operation occurs in display functions



13 Quality level

Examination or Test	At T _a =25℃	Inspection				
Examination of Test	(Unless otherwise stated)	Min.	Max.	Unit	IL	AQL
External Visual Inspection	Under normal illumination and eyesight condition, the distance between eyes and LCD is 25cm.	See annex A			II	Major 1.0 Minor 2.5
Display Defects	Under normal illumination and eyesight condition, display on inspection.	S	ee annex B		II	Major 1.0 Minor 2.5

Note: Major defects: Open segment or common, Short, Serious damages, Leakage

Miner defects: Others

Sampling standard conforms to GB2828



14 Precautions for Use of LCD Modules

14.1 Handling Precautions

- 14.1.1 The display panel is made of glass. Do not subject it to a mechanical shock by dropping it from a high place, etc.
- 14.1.2 Liquid in LCD is hazardous substance, if the display panel is damaged and the liquid crystal substance inside it leaks out, be sure not to get any in your mouth, if the substance comes into contact with your skin or clothes, thoroughly and promptly wash it off using soap and water.
- 14.1.3 Do not apply excessive force to the display surface or the adjoining areas since this may cause the color tone to vary.
- 14.1.4 Don't touch, push or rub the exposed polarizer covering the display surface of the LCD module with anything harder than an HB pencil lead, the polarizer is soft and easily scratched, handle it carefully.
- 14.1.5 Don't put or attach anything on the display area to avoid leaving any marks on.
- 14.1.6 If the display surface is contaminated or becomes dusty, breathe on the surface and gently wipe it with a soft dry cloth. do not scrub hard to avoid damage the surface. If still not completely clear, moisten cloth with one of the following solvents:
 - Isopropyl alcohol
 - Ethyl alcohol

Solvents other than those mentioned above may damage the polarizer. Especially, do not use the following:

- Water
- Ketone
- Aromatic solvents
- 14.1.7 Do not attempt to disassemble the LCD Module.
- 14.1.8 If the logic circuit power is off, do not apply the input signals.
- 14.1.9 Avoid using the same display pattern long time (continous ON segment). Software must be prepared so that the pattern will be changed
- 14.1.10 To prevent destruction of the elements by static electricity, be careful to maintain an optimum work environment.
 - a. Be sure to ground the body and electric appliances when handling the LCD Modules. It is preferable to use conductive mat on table and wear cotton clothes or conductive processed fibre. Synthetic fibre is not recommended.
 - b. Tools required for assembly, such as soldering irons, must be properly ground.
 - c. To reduce the amount of static electricity generated, do not conduct assembly and other work under dry conditions.
 - d. The LCD Module is coated with a film to protect the display surface. Be careful and slow when peeling off this protective film since static electricity may be generated. It is recommended to use ionic fan or machine when operating. It is recommended to remove the protection foil slowly (> 3 sec.).
 - e. It is preferable to wear gloves etc, to avoid damaging the LCD. Please do not touch electrodes with bare hands or avoid any other contamination.

14.2 Storage precautions

- 14.2.1 When storing the LCD modules, avoid exposure to direct sunlight or to the light of fluorescent lamps.
- 14.2.2 The LCD modules should be stored under the storage temperature range. If the LCD modules will be stored for a long time, the recommend condition is:

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Temperature: $5^{\circ}\text{C} \sim 40^{\circ}\text{C}$

Relatively humidity: ≤80%

14.2.3 The LCD modules should be stored in a clean environment or room, free from acid, alkali and harmful gas.

14.2.4 Store the module in anti-static electricity container and without any physical load.

14.3 Transportation precautions

The LCD modules should be no falling and violent shocking during transportation, and also should avoid excessive press, water, damp and sunshine.

14.4 Soldering

- 14.4.1 Use the high quality solders, only solder the I/O terminals.
- 14.4.2 No higher than 280°C and time less than 3-4 second during soldering.
- 14.4.3 Rewiring: no more than 3 times.
- 14.4.4 when you remove connector or cable soldered to I/O terminals, please confirm that solder is fully melted. If you remove by force, electrodes at I/O terminals may be damaged (or stripped off). It is recommended to use solder suction machine.



Appendix A

Inspection items and criteria for appearance defects

Items	Contents	Criteria						
Protective Glue			No clear defects					
Cover Tape		Covering	Covering all of the chip and no clear crimple					
Leakage			Not	permitted				
Rainbow		Ac	cording to	the limit specimen				
	Wrong polarizer attachment		Not	permitted				
Polarizer	Polarizer Bubble between		ed	Max. 3 defect	s allowed			
	polarizer and glass	ф<0.3m	m 0.3mm≤φ ⁵		≤0.5mm			
	Scratches of polarizer	Ac	cording to	the limit specimen				
		Not counted	Max. 3 spots allowed					
Black spot (in viewing area)	l d	X<0.20mm	0.20mm≤X≤0.5mm					
		X=(a+b)/2			Man 2			
Black line (in	+	Not counted	Max.	3 lines allowed	Max. 3 spots (lines) allowed			
viewing area)	0	a<0.02mm		nm≤a≤0.05mm b≤2.0mm				
Progressive cracks		Not permitted						



Appendix A (continued)

Item	Contents	Criteria					
	Cracks on pads	a		b	с		
		≤3mm	\leq	W/5	≪T/2	Max. 2 Cracks allowed	
		≤2mm	\leq	W/5	T/2 <c<t< td=""><td></td><td></td></c<t<>		
	Cracks on contact side	a			b		
	/	≤3mm			≪T/2		
		≤2mm		T/2 <b<t< td=""><td></td><td></td></b<t<>			
Glass		C shall be not reach the seal area			Max. 2 cracks	Max. 5 cracks	
Cracks	Cracks on non-contact side	a			b	allowed	allowed
		≤3mm			≤T/2		
		≤2mm			T/2 <b<t< td=""><td></td><td></td></b<t<>		
	\(\frac{1}{2}\)	C≤0.5mm					
	→ SW -		d≤	€SW/3			
	Corner cracks	e<2.0mm ²		2	Max. 3 cracks		
	f-P		e<2.0mm ² f<2.0mm ²			allowed	



Appendix B

Inspection items and criteria for display defects

Items		Contents	Critera						
Open segment or o	open comr	non	Not permitted						
Short			Not permitted						
Wrong viewing angle			Not permitted						
Contrast radio une	ven		According to the l	imit specimen					
Crosstalk			According to the l	imit specimen					
		a	Not counted	Max.3 dots allowed					
		X<0.1mm	0.1mm≤X≤0.2mm						
Pin holes and cracks in segment (DOT)			Max.3						
		- - D	Not counted	Max.2 dots allowed	allowed				
	1	A<0.1mm	0.1mm≤A≤0.2mm D<0.25mm						
			Not counted	Max.3 spots allowed					
Black spot (in			X<0.1mm	0.1mm≤X≤0.2mm					
viewing area)	a	X=(a+b)/2		Max.3 spots (lines)					
			Not counted	Max.3 lines allowed	allowed				
Black line (in viewing area)	b b		a<0.02mm	0.02mm≤a≤0.05mm b≤0.5mm					



Appendix B (continued)

Items	Content	Critera		
Transfor- mation of segment	- O	Not counted	Max. 2 defects allowed	
		x<0.1mm	0.1mm≤x≤0.2mm	
		x=(a+b)/2		Max.3 defects allowe
	D-++-a	Not counted	Max. 1 defects allowed	d
		a<0.1mm	0.1mm≤a≤0.2mm D>0	
		Max.2 defects allowed $0.8W \le a \le 1.2W$ a = measured value of width $W = \text{nominal value of width}$		