International TOR Rectifier

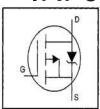
- HEXFET® Power MOSFET
- Dynamic dv/dt Rating
- Repetitive Avalanche RatedP-Channel
- 175°C Operating Temperature
- Fast Switching
- · Ease of Paralleling
- Simple Drive Requirements
- Lead-Free

Description

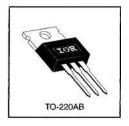
Third Generation HEXFETs from International Rectifier provide the designer with the best combination of fast switching, ruggedized device design, low on-resistance and cost-effectiveness.

The TO-220 package is universally preferred for all commercial-industrial applications at power dissipation levels to approximately 50 watts. The low thermal resistance and low package cost of the TO-220 contribute to its wide acceptance throughout the industry.

IRF9Z24PbF



 $V_{DSS} = -60V$ $R_{DS(on)} = 0.28\Omega$ $I_{D} = -11A$



Absolute Maximum Ratings

DOMESTIC TO SEASON NOT HELD OF THE BIT	Parameter	Max.	Units	
Ip @ Tc = 25°C	25°C Continuous Drain Current, VGS @ -10 V -11			
ID @ T _C = 100°C	Continuous Drain Current, V _{GS} @ -10 V -7.7		A	
IDM	Pulsed Drain Current ①	-44		
P _D @ T _C = 25°C	Power Dissipation	60	W	
	Linear Derating Factor	0.40	W/°C	
V _{GS}	Gate-to-Source Voltage	±20	V	
Eas	Single Pulse Avalanche Energy ②	240	mJ	
IAR	Avalanche Current ①	-11	A	
EAR	Repetitive Avalanche Energy ①	6.0	mJ	
dv/dt	Peak Diode Recovery dv/dt ③	-4.5	V/ns	
Tj Tstg	Operating Junction and Storage Temperature Range	-55 to +175	°C	
041 	Soldering Temperature, for 10 seconds	300 (1.6mm from case)		
	Mounting Torque, 6-32 or M3 screw	10 lbf•in (1.1 N•m)		

Thermal Resistance

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1:	Parameter	Min.	Тур.	Max.	Units
Reuc	Junction-to-Case			2.5	1
Recs	Case-to-Sink, Flat, Greased Surface	_	0.50	_	°C/M
Roja	Junction-to-Ambient	T -	_	62	1

Electrical Characteristics @ T_J = 25°C (unless otherwise specified)

	Parameter	Min.	Typ.	Max.	Units	Test Conditions	
V _{(BR)DSS}	Drain-to-Source Breakdown Voltage	-60		8	٧	V _{GS} =0V, I _D =-250μA	
ΔV _{(BR)DSS} /ΔT _J	Breakdown Voltage Temp. Coefficient		-0.056	8_0	V/°C	Reference to 25°C, I _D =-1mA	
R _{DS(on)}	Static Drain-to-Source On-Resistance	-	(-)	0.28	Ω	V _{GS} =-10V, I _D =-6.6A ④	
V _{GS(th)}	Gate Threshold Voltage	-2.0	Se rve s	-4.0	٧	V _{DS} =V _{GS} , I _D =-250μA	
gís	Forward Transconductance	1.4		, 	S	V _{DS} =-25V, I _D =-6.6A ④	
E	Drain-to-Source Leakage Current	_	N-	-100		V _{DS} =-60V, V _{GS} =0V	
Ipss	Drain-to-Source Leakage Current	_	12 	-500	μΑ	V _{DS} =-48V, V _{GS} =0V, T _J =150°C	
I _{GSS}	Gate-to-Source Forward Leakage	to-Source Forward Leakage — — -100	nA	V _{GS} =-20V			
IGSS	Gate-to-Source Reverse Leakage	10-0	_	100	nA.	V _{GS} =20V	
Qg	Total Gate Charge			19		I _D =-11A	
Q_{gs}	Gate-to-Source Charge		-	5.4	nC	V _{DS} =-48V	
Qgd	Gate-to-Drain ("Miller") Charge		_	11		V _{GS} =-10V See Fig. 6 and 13 @	
td(on)	Turn-On Delay Time	S	13	-		V _{DD} =-30V	
tr	Rise Time		68	_	ns	I _D =-11A	
t _{d(off)}	Turn-Off Delay Time	-	15	_	115	R _G =18Ω	
tr	Fall Time	5-4	29	_		R _D =2.5Ω See Figure 10 ⊕	
L _D	Internal Drain Inductance	3=	4.5	_	nН	Between lead, 6 mm (0.25in.)	
Ls	Internal Source Inductance		7.5	_	пп	from package and center of die contact	
Ciss	Input Capacitance	_	570	-		V _{GS} =0V	
Coss	Output Capacitance	_	360	_	pF	V _{DS} =-25V	
Crss	Reverse Transfer Capacitance	1	65			f=1.0MHz See Figure 5	

Source-Drain Ratings and Characteristics

	Parameter	Min.	Typ.	Max.	Units	Test Conditions
ls	Continuous Source Current (Body Diode)		_	-11		MOSFET symbol showing the
I _{SM}	Pulsed Source Current (Body Diode) ①	-	_	-44	Α	integral reverse p-n junction diode.
VsD	Diode Forward Voltage	-	_	-6.3	٧	TJ=25°C, IS=-11A, VGS=0V @
trr	Reverse Recovery Time	_	100	200	ns	T _J =25°C, I _F =-11A
Qrr	Reverse Recovery Charge		0.32	0.64	μC	di/dt=100A/μs ④
ton	Forward Turn-On Time	Intrinsic turn-on time is neglegible (turn-on is dominated by Ls+LD)				

Notes:

- Repetitive rating; pulse width limited by max. junction temperature (See Figure 11)
- ③ Isp≤-11A, di/dt≤140A/ μ s, V_{DD}≤V(BR)DSS, T_J≤175°C
- ② V_{DD} =-25V, starting T_J =25°C, L=2.3mH R_G =25 Ω , Ias=-11A (See Figure 12)
- ④ Pulse width ≤ 300 µs; duty cycle ≤2%.

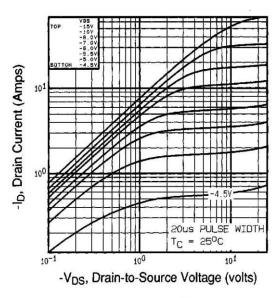


Fig 1. Typical Output Characteristics, $T_C=25^{\circ}C$

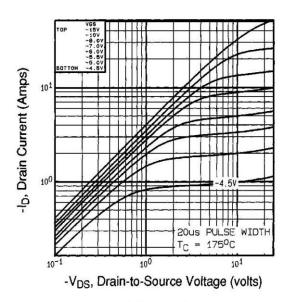


Fig 2. Typical Output Characteristics, T_C=175°C

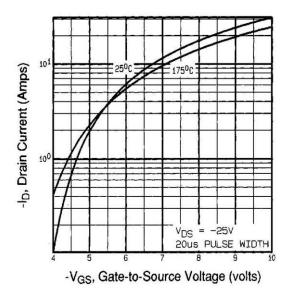


Fig 3. Typical Transfer Characteristics

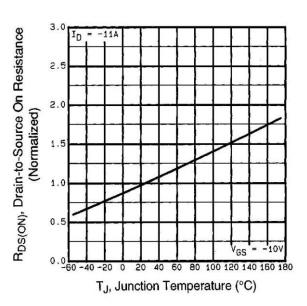


Fig 4. Normalized On-Resistance Vs. Temperature

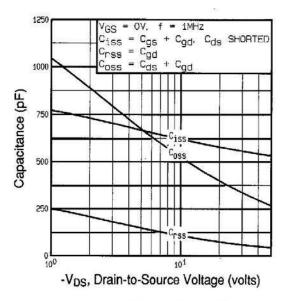


Fig 5. Typical Capacitance Vs. Drain-to-Source Voltage

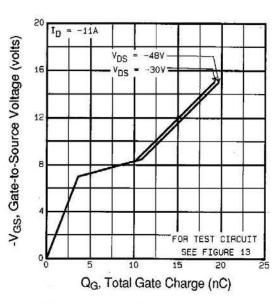


Fig 6. Typical Gate Charge Vs. Gate-to-Source Voltage

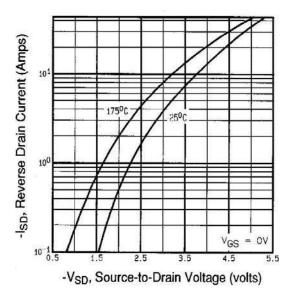


Fig 7. Typical Source-Drain Diode Forward Voltage

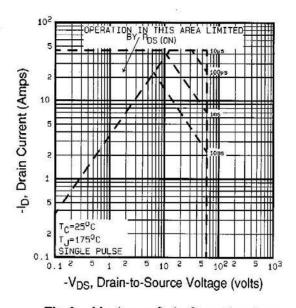


Fig 8. Maximum Safe Operating Area

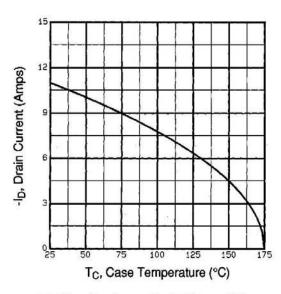


Fig 9. Maximum Drain Current Vs. Case Temperature

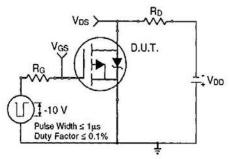


Fig 10a. Switching Time Test Circuit

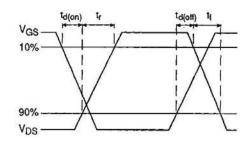


Fig 10b. Switching Time Waveforms

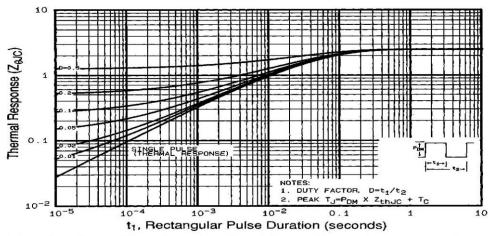


Fig 11. Maximum Effective Transient Thermal Impedance, Junction-to-Case

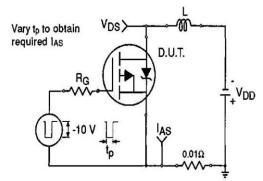


Fig 12a. Unclamped Inductive Test Circuit

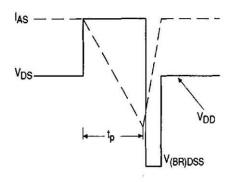


Fig 12b. Unclamped Inductive Waveforms

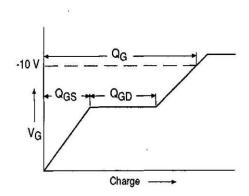


Fig 13a. Basic Gate Charge Waveform

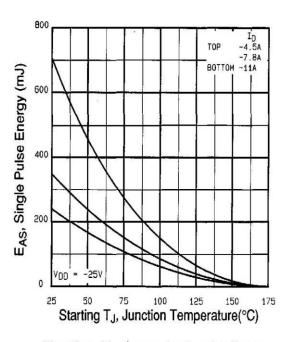


Fig 12c. Maximum Avalanche Energy Vs. Drain Current

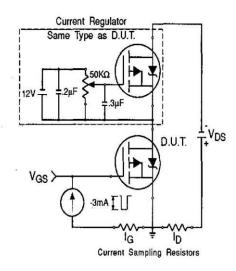
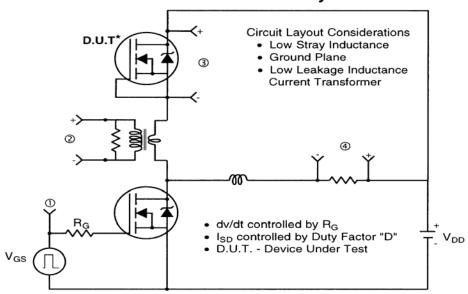
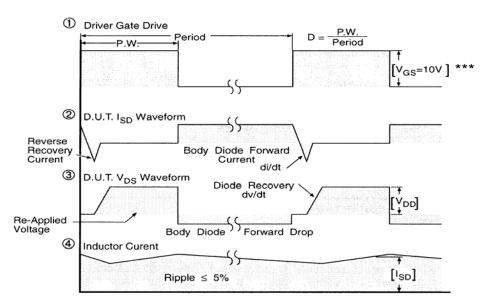


Fig 13b. Gate Charge Test Circuit

Peak Diode Recovery dv/dt Test Circuit



Reverse Polarity of D.U.T for P-Channel



*** V_{GS} = 5.0V for Logic Level and 3V Drive Devices

Fig 14. For P-Channel HEXFETS

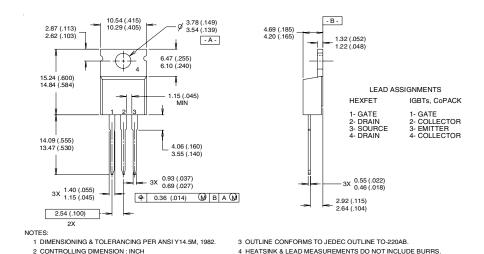
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International

Rectifier

TO-220AB Package Outline

Dimensions are shown in millimeters (inches)



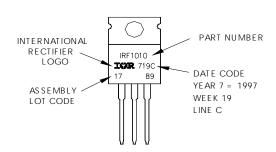
TO-220AB Part Marking Information

EXAMPLE: THIS IS AN IRF1010

LOT CODE 1789

ASSEMBLED ON WW 19, 1997 IN THE ASSEMBLY LINE "C"

Note: "P" in assembly line position indicates "Lead-Free"



Data and specifications subject to change without notice.



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06/04

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