



# N-Channel 20-V (D-S) MOSFET

PRODUCT SUMMARY			
V <sub>DS</sub> (V)	R <sub>DS(on)</sub> (Ω)	I <sub>D</sub> (A) <sup>a</sup>	Q <sub>g</sub> (Typ.)
20	0.006 at V <sub>GS</sub> = 10 V	20 <sup>e</sup>	27.5 nC
	0.007 at V <sub>GS</sub> = 4.5 V	20 <sup>e</sup>	

### FEATURES

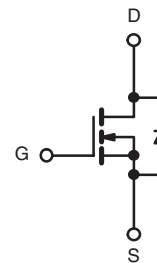
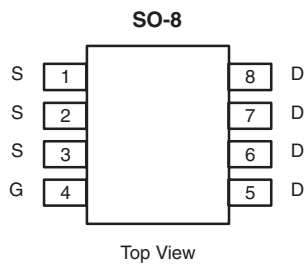
- TrenchFET<sup>®</sup> Power MOSFET
- 100 % R<sub>g</sub> and UIS Tested



RoHS  
COMPLIANT

### APPLICATIONS

- Low-Side MOSFET for Synchronous Buck
  - Game Machine
  - PC



N-Channel MOSFET

Ordering Information: Si4114DY-T1-E3 (Lead (Pb)-free)

ABSOLUTE MAXIMUM RATINGS T <sub>A</sub> = 25 °C, unless otherwise noted				
Parameter	Symbol	Limit	Unit	
Drain-Source Voltage	V <sub>DS</sub>	20	V	
Gate-Source Voltage	V <sub>GS</sub>	± 16		
Continuous Drain Current (T <sub>J</sub> = 150 °C)	I <sub>D</sub>	T <sub>C</sub> = 25 °C	A	20 <sup>e</sup>
		T <sub>C</sub> = 70 °C		18.2
		T <sub>A</sub> = 25 °C		15.2 <sup>b, c</sup>
		T <sub>A</sub> = 70 °C		12.1 <sup>b, c</sup>
Pulsed Drain Current	I <sub>DM</sub>	50		
Continuous Source-Drain Diode Current	I <sub>S</sub>	T <sub>C</sub> = 25 °C	mJ	5.1
		T <sub>A</sub> = 25 °C		2.2 <sup>b, c</sup>
Single Pulse Avalanche Current	I <sub>AS</sub>	30		
Avalanche Energy	E <sub>AS</sub>	45	mJ	
Maximum Power Dissipation	P <sub>D</sub>	T <sub>C</sub> = 25 °C	W	5.7
		T <sub>C</sub> = 70 °C		3.6
		T <sub>A</sub> = 25 °C		2.5 <sup>b, c</sup>
		T <sub>A</sub> = 70 °C		1.6 <sup>b, c</sup>
Operating Junction and Storage Temperature Range	T <sub>J</sub> , T <sub>stg</sub>	- 55 to 150	°C	

THERMAL RESISTANCE RATINGS					
Parameter	Symbol	Typical	Maximum	Unit	
Maximum Junction-to-Ambient <sup>b, d</sup>	R <sub>thJA</sub>	39	50	°C/W	
Maximum Junction-to-Foot (Drain)	R <sub>thJF</sub>	18	22		

Notes:

- Based on T<sub>C</sub> = 25 °C.
- Surface Mounted on 1" x 1" FR4 board.
- t = 10 s.
- Maximum under Steady State conditions is 85 °C/W.
- Package limited.



SPECIFICATIONS $T_J = 25\text{ }^\circ\text{C}$ , unless otherwise noted						
Parameter	Symbol	Test Conditions	Min.	Typ.	Max.	Unit
<b>Static</b>						
Drain-Source Breakdown Voltage	$V_{DS}$	$V_{GS} = 0\text{ V}, I_D = 250\text{ }\mu\text{A}$	20			V
$V_{DS}$ Temperature Coefficient	$\Delta V_{DS}/T_J$	$I_D = 250\text{ }\mu\text{A}$		19		mV/ $^\circ\text{C}$
$V_{GS(th)}$ Temperature Coefficient	$\Delta V_{GS(th)}/T_J$			- 5.3		
Gate-Source Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_D = 250\text{ }\mu\text{A}$	1.0		2.1	V
Gate-Source Leakage	$I_{GSS}$	$V_{DS} = 0\text{ V}, V_{GS} = \pm 16\text{ V}$			$\pm 100$	nA
Zero Gate Voltage Drain Current	$I_{DSS}$	$V_{DS} = 20\text{ V}, V_{GS} = 0\text{ V}$			1	$\mu\text{A}$
		$V_{DS} = 20\text{ V}, V_{GS} = 0\text{ V}, T_J = 55\text{ }^\circ\text{C}$			10	
On-State Drain Current <sup>a</sup>	$I_{D(on)}$	$V_{DS} \geq 5\text{ V}, V_{GS} = 10\text{ V}$	30			A
Drain-Source On-State Resistance <sup>a</sup>	$R_{DS(on)}$	$V_{GS} = 10\text{ V}, I_D = 10\text{ A}$		0.0049	0.006	$\Omega$
		$V_{GS} = 4.5\text{ V}, I_D = 7\text{ A}$		0.0056	0.007	
Forward Transconductance <sup>a</sup>	$g_{fs}$	$V_{DS} = 10\text{ V}, I_D = 10\text{ A}$		55		S
<b>Dynamic<sup>b</sup></b>						
Input Capacitance	$C_{iss}$	$V_{DS} = 10\text{ V}, V_{GS} = 0\text{ V}, f = 1\text{ MHz}$		3700		pF
Output Capacitance	$C_{oss}$			745		
Reverse Transfer Capacitance	$C_{rss}$			315		
Total Gate Charge	$Q_g$	$V_{DS} = 10\text{ V}, V_{GS} = 10\text{ V}, I_D = 10\text{ A}$		62	95	nC
		$V_{DS} = 10\text{ V}, V_{GS} = 4.5\text{ V}, I_D = 10\text{ A}$		27.5	42	
Gate-Source Charge	$Q_{gs}$			8.0		
Gate-Drain Charge	$Q_{gd}$		6.0			
Gate Resistance	$R_g$	$f = 1\text{ MHz}$	0.15	0.7	1.4	$\Omega$
Turn-On Delay Time	$t_{d(on)}$	$V_{DD} = 10\text{ V}, R_L = 2\text{ }\Omega$ $I_D \cong 5\text{ A}, V_{GEN} = 4.5\text{ V}, R_g = 1\text{ }\Omega$		30	55	ns
Rise Time	$t_r$			13	25	
Turn-Off Delay Time	$t_{d(off)}$			60	100	
Fall Time	$t_f$			30	55	
Turn-On Delay Time	$t_{d(on)}$	$V_{DD} = 10\text{ V}, R_L = 2\text{ }\Omega$ $I_D \cong 5\text{ A}, V_{GEN} = 10\text{ V}, R_g = 1\text{ }\Omega$		13	25	
Rise Time	$t_r$			9	18	
Turn-Off Delay Time	$t_{d(off)}$			38	65	
Fall Time	$t_f$			8	16	
<b>Drain-Source Body Diode Characteristics</b>						
Continuous Source-Drain Diode Current	$I_S$	$T_C = 25\text{ }^\circ\text{C}$			5.1	A
Pulse Diode Forward Current <sup>a</sup>	$I_{SM}$				50	
Body Diode Voltage	$V_{SD}$	$I_S = 2\text{ A}$		0.71	1.1	V
Body Diode Reverse Recovery Time	$t_{rr}$	$I_F = 10\text{ A}, di/dt = 100\text{ A}/\mu\text{s}, T_J = 25\text{ }^\circ\text{C}$		26	50	ns
Body Diode Reverse Recovery Charge	$Q_{rr}$			16	30	nC
Reverse Recovery Fall Time	$t_a$			13		ns
Reverse Recovery Rise Time	$t_b$			13		

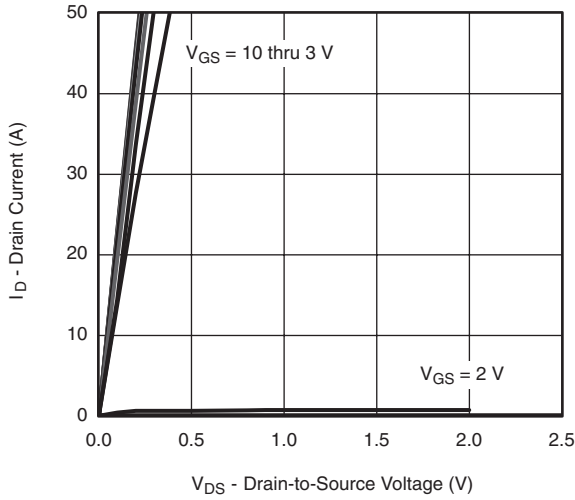
## Notes

- a. Pulse test; pulse width  $\leq 300\text{ }\mu\text{s}$ , duty cycle  $\leq 2\%$   
b. Guaranteed by design, not subject to production testing.

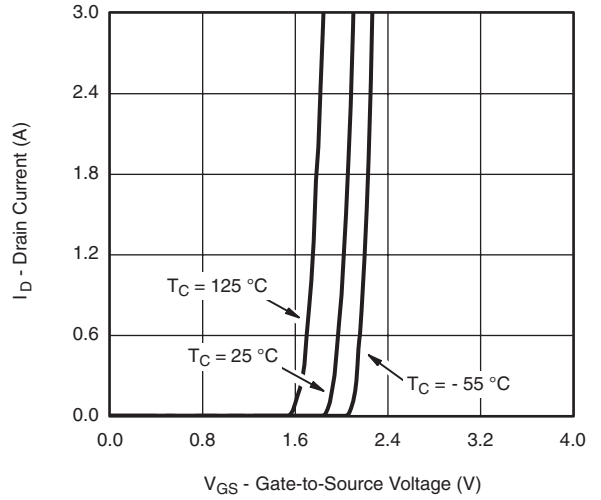
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.



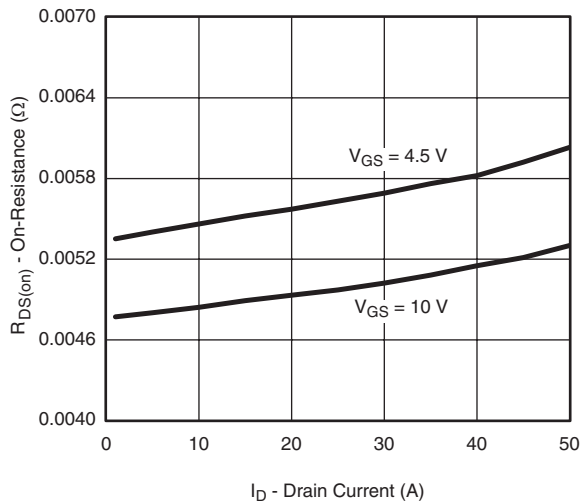
**TYPICAL CHARACTERISTICS** 25 °C, unless otherwise noted



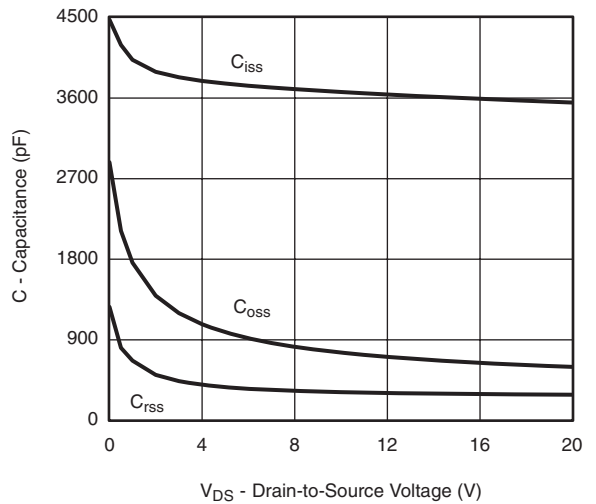
**Output Characteristics**



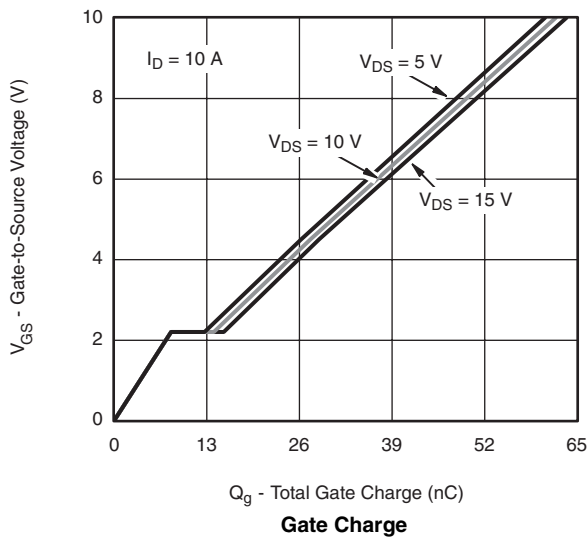
**Transfer Characteristics**



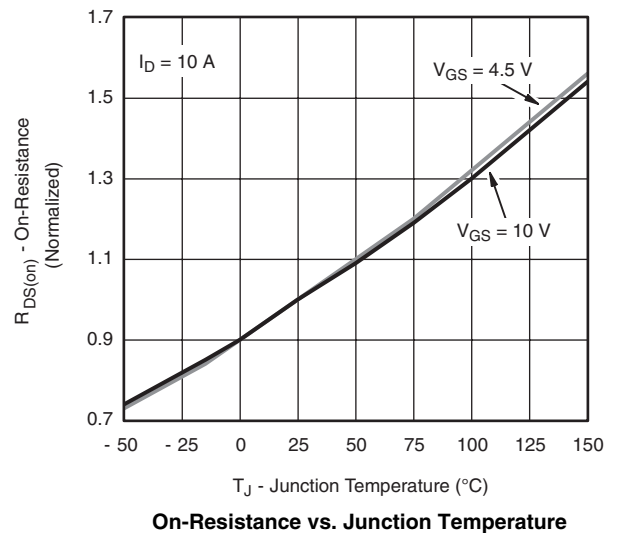
**On-Resistance vs. Drain Current and Gate Voltage**



**Capacitance**



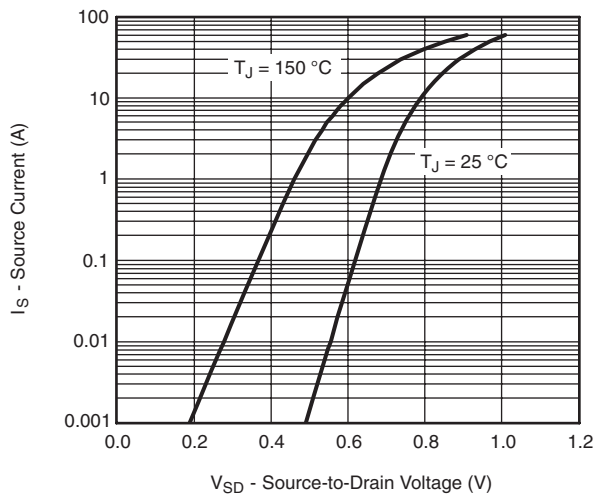
**Gate Charge**



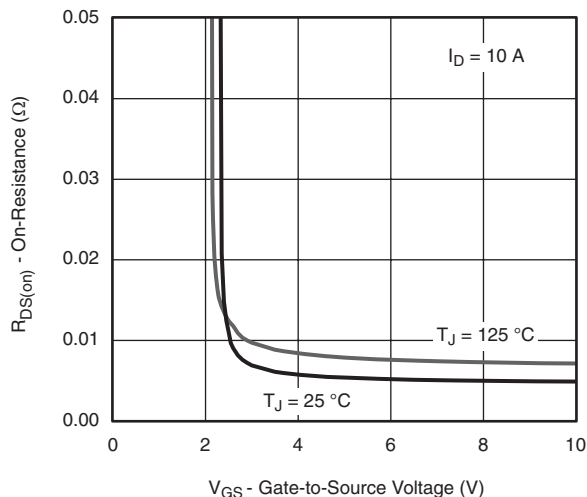
**On-Resistance vs. Junction Temperature**



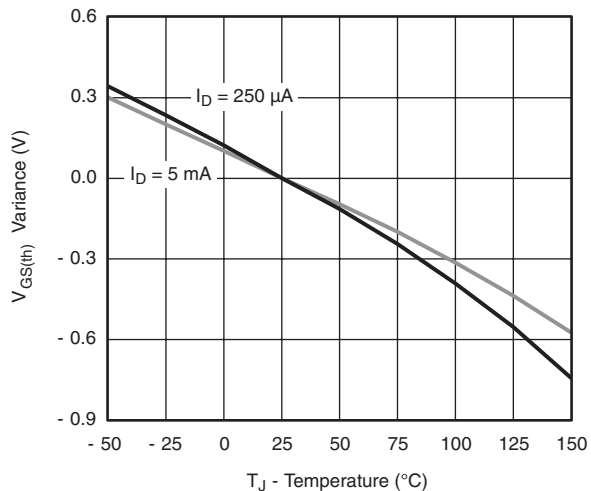
**TYPICAL CHARACTERISTICS** 25 °C, unless otherwise noted



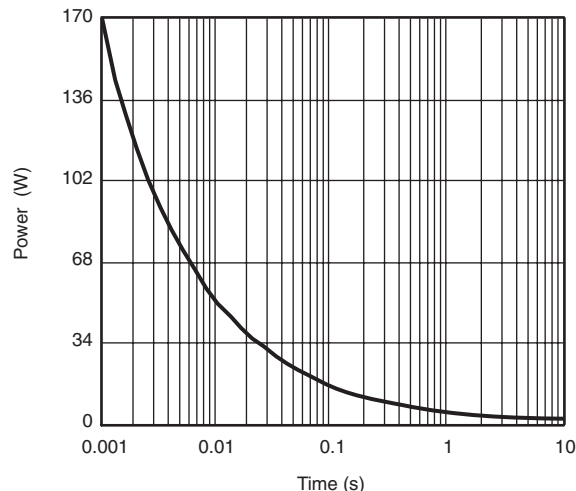
Source-Drain Diode Forward Voltage



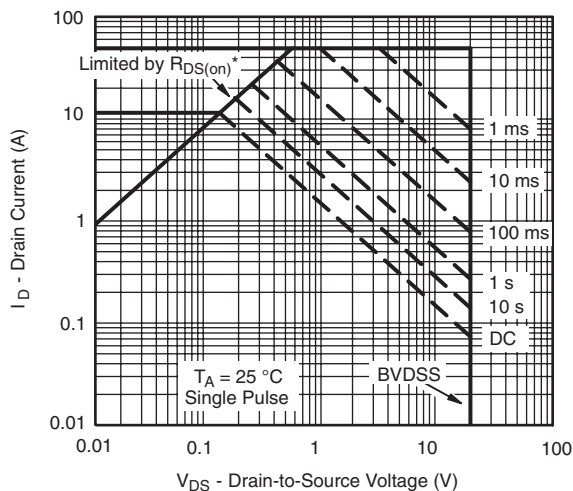
On-Resistance vs. Gate-to-Source Voltage



Threshold Voltage



Single Pulse Power, Junction-to-Ambient

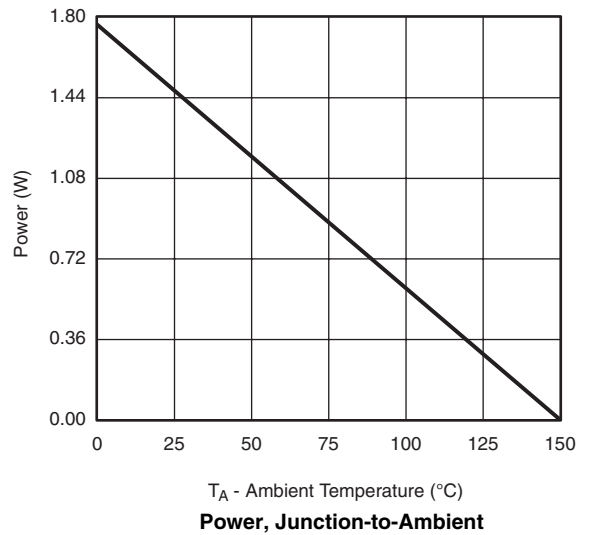
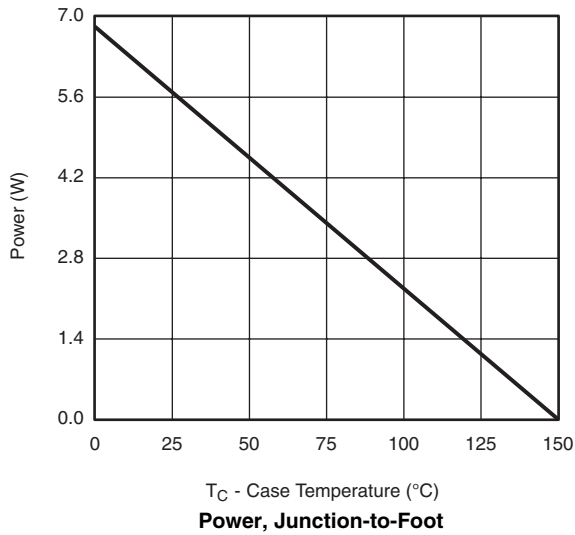
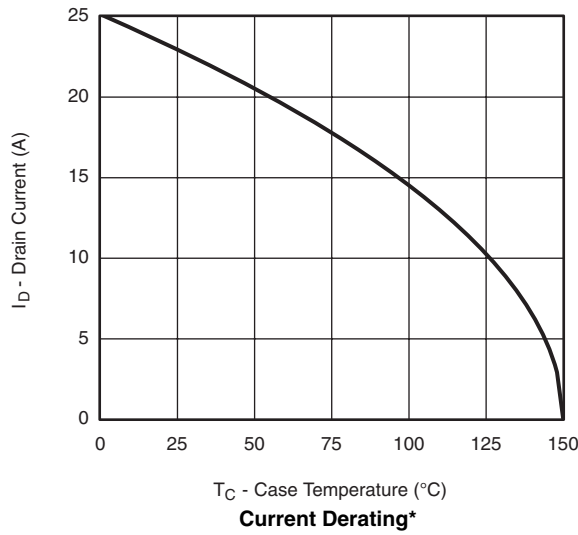


\*  $V_{GS} >$  minimum  $V_{GS}$  at which  $R_{DS(on)}$  is specified

Safe Operating Area, Junction-to-Ambient



**TYPICAL CHARACTERISTICS** 25 °C, unless otherwise noted



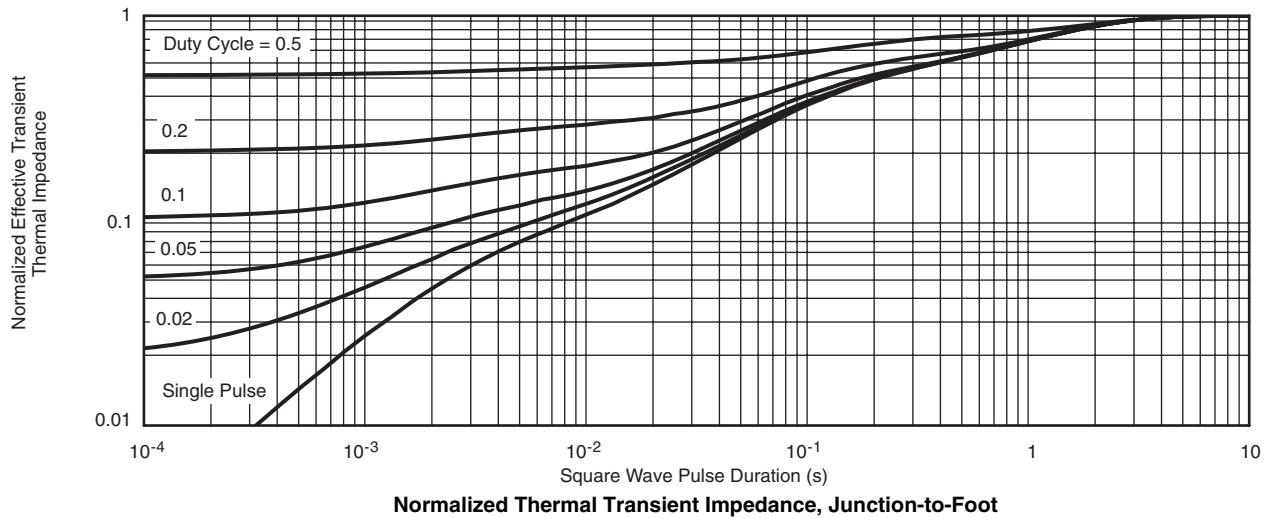
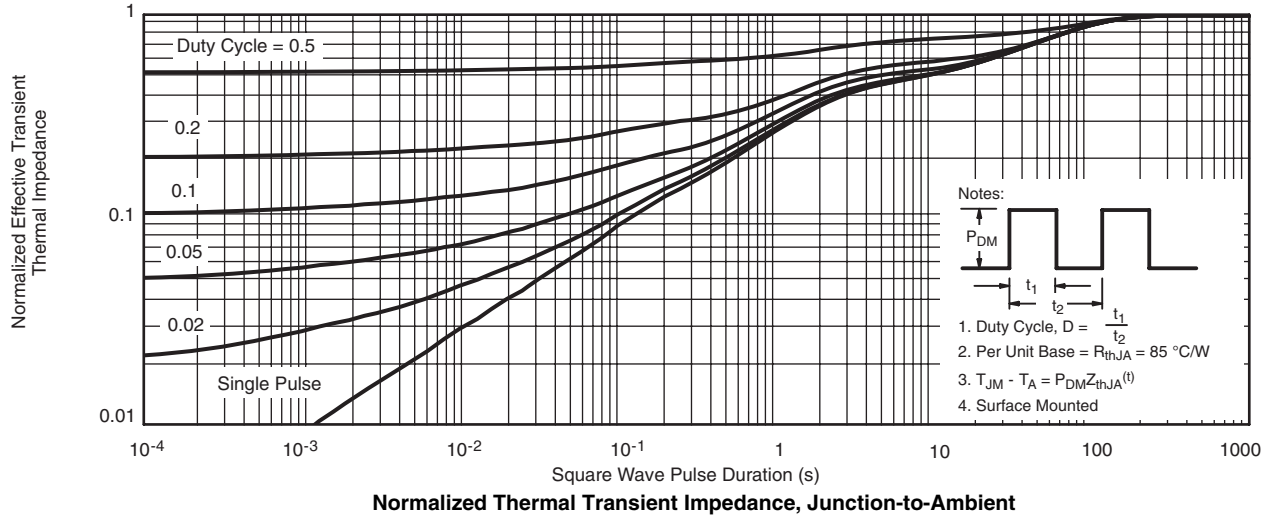
\* The power dissipation  $P_D$  is based on  $T_{J(max)} = 175$  °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit.

# Si4114DY

Vishay Siliconix



**TYPICAL CHARACTERISTICS** 25 °C, unless otherwise noted



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