



High Density Mounting Type Photocoupler

LTV-817 Series

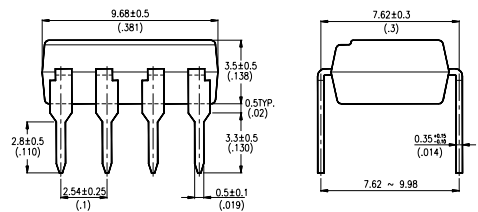
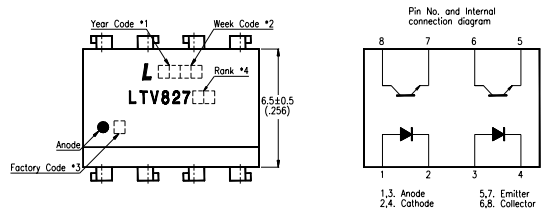
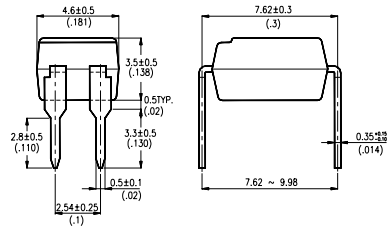
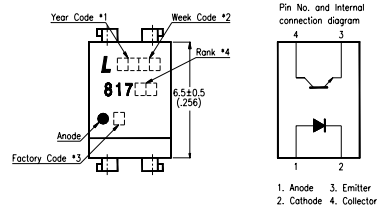
Features

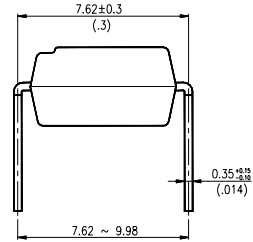
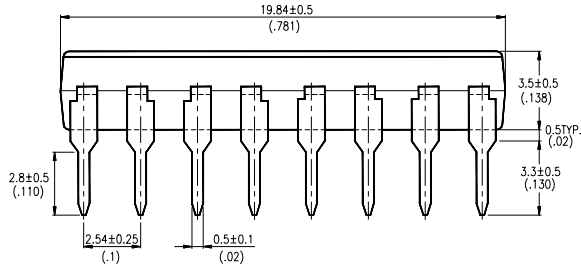
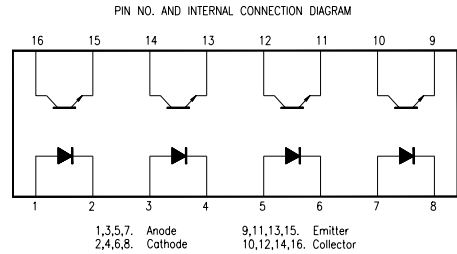
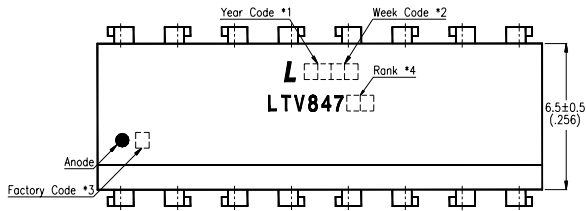
- Current transfer ratio
(CTR : MIN. 50% at $I_F=5mA$, $V_{CE}=5V$)
- High input-output isolation voltage:
(V_{ISO} : 5,000V_{rms})
- Compact dual-in-line package
LTV-817 : 1-channel type
LTV-827 : 2-channel type
LTV-847 : 4-channel type
- UL approved (No. E113898)
- TUV approved (No. R9653630)
- CSA approved (No. CA91533-1)
- FIMKO approved (No. 202634)
- NEMKO approved (No. P98101945)
- DEMKO approved (No. 307857)
- SEMKO approved (No. 9832157/01-03)
- VDE approved (No. 094722)
- Options available :
 - Leads with 0.4"(10.16mm)spacing (M Type)
 - Leads bends for surface mounting(S Type)
 - Tape and Reel of Type I for SMD(Add"-TA"Suffix)
 - Tape and Reel of Type II for SMD(Add"-TA1"Suffix)
 - VDE 0884 approvals (Add"-V"Suffix)

Applications

1. Computer terminals.
2. System appliances, measuring instruments.
3. Registers, copiers, automatic vending machines.
4. Electric home appliances such as fan heaters, etc.
5. Signal transmission between circuits of different potentials and impedances.

Package Dimensions





Note:

1. Year date code.
2. 2-digit work week.
3. Factory code shall be marked (Z : Taiwan, Y : Thailand).
4. Rank shall be or shall not be marked.
5. All dimensions are in millimeters (inches).
6. Tolerance is $\pm 0.25\text{mm}$ (.010") unless otherwise noted.
7. Specifications are subject to change without notice.

Ordering Information

Part Number	Package	Safety Standard Approval	Application part number
LTV-817 LTV-817M LTV-817S LTV-817S-TA LTV-817S-TA1	4-pin DIP 4-pin (leads with 0.4" spacing) 4-pin (lead bends for surface mount) 4-pin (tape and reel packaging of type I) 4-pin (tape and reel packaging of type II)	<ul style="list-style-type: none"> • UL approved • TUV approved • CSA approved • FIMKO approved • NEMKO approved • SEMKO approved • DEMKO approved 	LTV-817
LTV-827 LTV-827M LTV-827S LTV-827S-TA LTV-827S-TA1	8-pin DIP 8-pin (leads with 0.4" spacing) 8-pin (lead bends for surface mount) 8-pin (tape and reel packaging of type I) 8-pin (tape and reel packaging of type II)		LTV-827
LTV-847 LTV-847M LTV-847S LTV-847S-TA LTV-847S-TA1	16-pin DIP 16-pin (leads with 0.4" spacing) 16-pin (lead bends for surface mount) 16-pin (tape and reel packaging of type I) 16-pin (tape and reel packaging of type II)		LTV-847
LTV817-V LTV817M-V LTV817S-V LTV817STA-V LTV817STA1-V	4-pin DIP 4-pin (leads with 0.4" spacing) 4-pin (lead bends for surface mount) 4-pin (tape and reel packaging of type I) 4-pin (tape and reel packaging of type II)	<ul style="list-style-type: none"> • VDE approved 	LTV-817
LTV827-V LTV827M-V LTV827S-V LTV827STA-V LTV827STA1-V	8-pin DIP 8-pin (leads with 0.4" spacing) 8-pin (lead bends for surface mount) 8-pin (tape and reel packaging of type I) 8-pin (tape and reel packaging of type II)		LTV-827
LTV847-V LTV847M-V LTV847S-V LTV847STA-V LTV847STA1-V	16-pin DIP 16-pin (leads with 0.4" spacing) 16-pin (lead bends for surface mount) 16-pin (tape and reel packaging of type I) 16-pin (tape and reel packaging of type II)		LTV-847

PHOTOCOUPLER

Absolute Maximum Ratings

(Ta=25°C)

Parameter		Symbol	Rating	Unit
Input	Forward Current	I _F	50	mA
	Reverse Voltage	V _R	6	V
	Power Dissipation	P	70	mW
Output	Collector-Emitter Voltage	V _{CEO}	35	V
	Emitter-Collector Voltage	V _{ECO}	6	V
	Collector Current	I _C	50	mA
	Collector Power Dissipation	P _C	150	mW
Total Power Dissipation		P _{tot}	200	mW
Operating Temperature		T _{opr}	-30~+100	°C
Storage Temperature		T _{stg}	-55~+125	°C
*1.Isolation Voltage		V _{iso}	5	KV _{rms}
*2.Soldering Temperature		T _{sol}	260	°C

*1. AC for 1 minute, R.H. = 40 ~ 60%

• Isolation voltage shall be measured using the following method.

(1) Short between anode and cathode on the primary side and between collector, emitter and base on the secondary side.

(2) The isolation voltage tester with zero-cross circuit shall be used.

(3) The waveform of applied voltage shall be a sine wave.

*2. For 10 seconds.

Electrical/Optical Characteristics

(Ta=25°C)

Parameter		Symbol	Min.	Typ.	Max.	Unit	Conditions
Input	Forward Voltage	V _F	—	1.2	1.4	V	I _F =20mA
	Reverse Current	I _R	—	—	10	μA	V _R =4V
	Terminal Capacitance	C _t	—	30	250	pF	V=0, f=1KHz
Output	Collector Dark Current	I _{CEO}	—	—	100	nA	V _{CE} =20V
	Collector-Emitter Breakdown Voltage	BV _{CEO}	35	—	—	V	I _C =0.1mA
	Emitter-Collector Breakdown Voltage	BV _{ECO}	6	—	—	V	I _E =10 μA
Transfer Characteristics	*Current Transfer Ratio	CTR	50	—	600	%	I _F =5mA, V _{CE} =5V R _{BE} =∞
	Collector Current	I _C	2.5	—	30	mA	
	Collector-emitter Saturation Voltage	V _{CE(sat)}	—	0.1	0.2	V	I _F =20mA, I _C =1mA
	Isolation Resistance	R _{iso}	5 × 10 ¹⁰	10 ¹¹	—	Ω	DC500V, 40~60% R.H.
	Floating Capacitance	C _f	—	0.6	1.0	pF	V=0, f=1MHz
	Cut-off Frequency	f _c	—	80	—	KHz	V _{CE} =5V, I _C =2mA R _L =100 Ω, -3dB
	Response Time (Rise)	t _r	—	4	18	μs	V _{CE} =2V, I _C =2mA
Response Time (Fall)	t _f	—	3	18	μs	R _L =100 Ω	

$$*CTR = \frac{I_C}{I_F} \times 100\%$$

■ Supplement

Rank Table of Current Transfer Ratio CTR

Model No.	Rank Mark	CTR(%)
LTV-817	L	50~100
LTV-817	A	80~160
LTV-817	B	130~260
LTV-817	C	200~400
LTV-817	D	300~600
LTV-817	L or A or B or C or D	50~600
Conditions	I _F =5mA V _{CE} =5V Ta=25°C	

Typical Electrical/Optical Characteristic Curves (25°C Ambient Temperature Unless Otherwise Noted)

Fig.1 Forward Current vs. Ambient Temperature

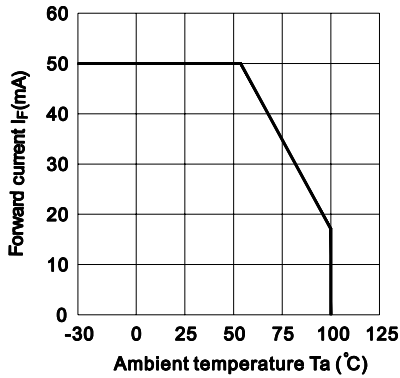


Fig.2 Collector Power Dissipation vs. Ambient Temperature

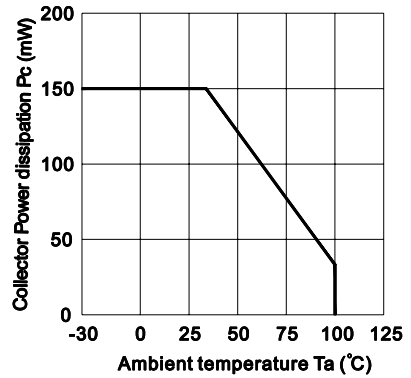


Fig.3 Collector-emitter Saturation Voltage vs. Forward Current

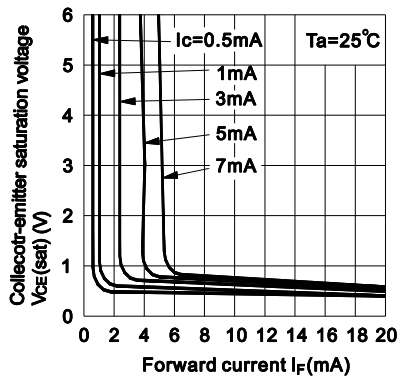


Fig.4 Forward Current vs. Forward Voltage

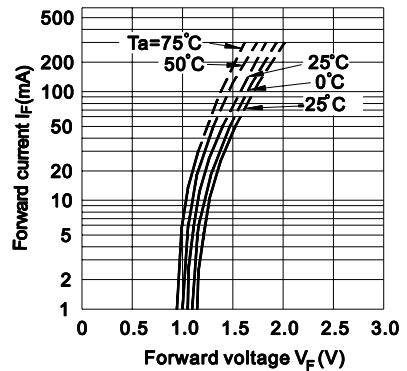


Fig.5 Current Transfer Ratio vs. Forward Current

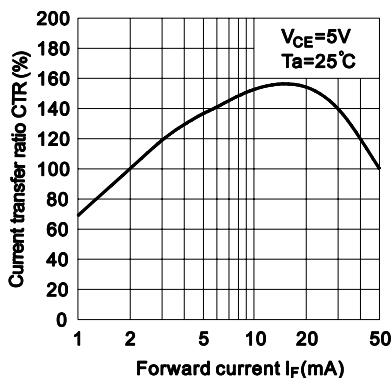


Fig.6 Collector Current vs. Collector-emitter Voltage

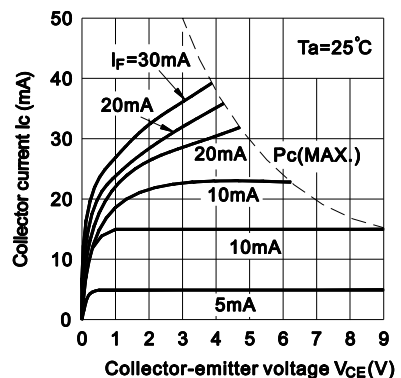


Fig.7 Relative Current Transfer Ratio vs. Ambient Temperature

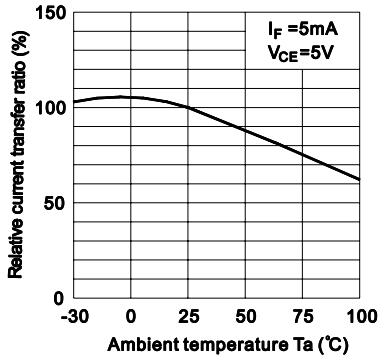


Fig.8 Collector-emitter Saturation Voltage vs. Ambient Temperature

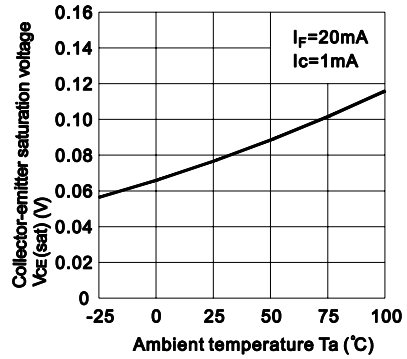


Fig.9 Collector Dark Current vs. Ambient Temperature

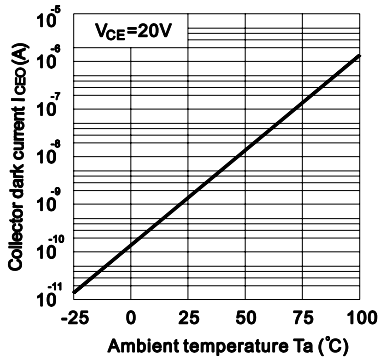


Fig.10 Response Time vs. Load Resistance

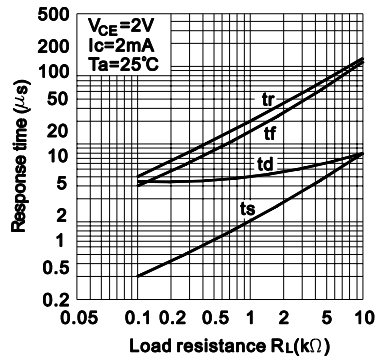
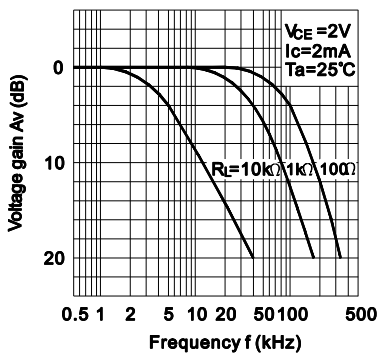
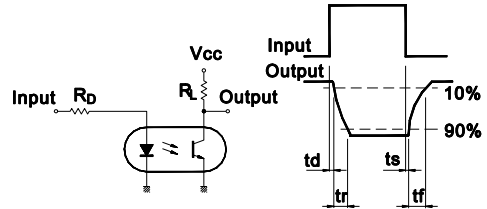


Fig.11 Frequency Response



Test Circuit for Response Time



Test Circuit for Frequency Response

