

BAT54CTT1

Preferred Device

Dual Series Schottky Barrier Diodes

These Schottky barrier diodes are designed for high speed switching applications, circuit protection, and voltage clamping. Extremely low forward voltage reduces conduction loss. Miniature surface mount package is excellent for hand held and portable applications where space is limited.

Features

- Extremely Fast Switching Speed
- Low Forward Voltage – 0.35 Volts (Typ) @ $I_F = 10$ mAdc
- Pb-Free Package is Available

MAXIMUM RATINGS ($T_J = 125^\circ\text{C}$ unless otherwise noted)

Rating	Symbol	Value	Unit
Reverse Voltage	V_R	30	V
Forward Power Dissipation @ $T_A = 25^\circ\text{C}$ Derate above 25°C	P_F	225 1.8	mW mW/ $^\circ\text{C}$
Thermal Resistance, Junction-to-Ambient (Note 1)	$R_{\theta JA}$	555	$^\circ\text{C}/\text{W}$
Forward Current (DC)	I_F	200 Max	mA
Junction Temperature	T_J	-55 to 125	$^\circ\text{C}$
Storage Temperature Range	T_{stg}	-55 to +150	$^\circ\text{C}$

Maximum ratings are those values beyond which device damage can occur. Maximum ratings applied to the device are individual stress limit values (not normal operating conditions) and are not valid simultaneously. If these limits are exceeded, device functional operation is not implied, damage may occur and reliability may be affected.

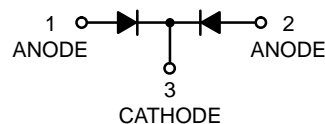
1. FR-4 board with minimum mounting pad.



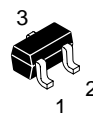
ON Semiconductor®

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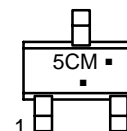
30 VOLT DUAL COMMON CATHODE SCHOTTKY BARRIER DIODES



MARKING DIAGRAM



SC-75
CASE 463
STYLE 3



5C = Device Code
M = Date Code*
■ = Pb-Free Package

(Note: Microdot may be in either location)

*Date Code orientation may vary depending upon manufacturing location.

ORDERING INFORMATION

Device	Package	Shipping†
BAT54CTT1	SC-75	3000 / Tape & Reel
BAT54CTT1G	SC-75 (Pb-Free)	3000 / Tape & Reel

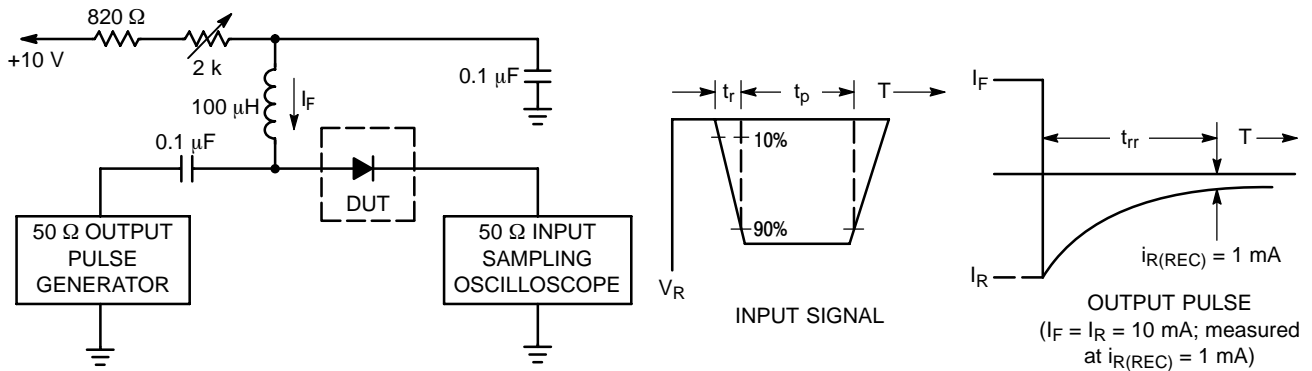
†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

Preferred devices are recommended choices for future use and best overall value.

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ELECTRICAL CHARACTERISTICS ($T_A = 25^\circ\text{C}$ unless otherwise noted) (EACH DIODE)

Characteristic	Symbol	Min	Typ	Max	Unit
Reverse Breakdown Voltage ($I_R = 10 \mu\text{A}$)	$V_{(BR)R}$	30	-	-	V
Total Capacitance ($V_R = 1.0 \text{ V}$, $f = 1.0 \text{ MHz}$)	C_T	-	7.6	10	pF
Reverse Leakage ($V_R = 25 \text{ V}$)	I_R	-	0.5	2.0	μA_{dc}
Forward Voltage ($I_F = 0.1 \text{ mA}_{dc}$)	V_F	-	0.22	0.24	V _{dc}
Forward Voltage ($I_F = 30 \text{ mA}_{dc}$)	V_F	-	0.41	0.5	V _{dc}
Forward Voltage ($I_F = 100 \text{ mA}_{dc}$)	V_F	-	0.52	0.8	V _{dc}
Reverse Recovery Time ($I_F = I_R = 10 \text{ mA}_{dc}$, $I_{R(REC)} = 1.0 \text{ mA}_{dc}$, Figure 1)	t_{rr}	-	-	5.0	ns
Forward Voltage ($I_F = 1.0 \text{ mA}_{dc}$)	V_F	-	0.29	0.32	V _{dc}
Forward Voltage ($I_F = 10 \text{ mA}_{dc}$)	V_F	-	0.35	0.40	V _{dc}
Forward Current (DC)	I_F	-	-	200	mA _{dc}
Repetitive Peak Forward Current	I_{FRM}	-	-	300	mA _{dc}
Non-Repetitive Peak Forward Current ($t < 1.0 \text{ s}$)	I_{FSM}	-	-	600	mA _{dc}



- Notes: 1. A 2.0 k Ω variable resistor adjusted for a Forward Current (I_F) of 10 mA.
 2. Input pulse is adjusted so $I_{R(\text{peak})}$ is equal to 10 mA.
 3. $t_p \gg t_{rr}$

Figure 1. Recovery Time Equivalent Test Circuit

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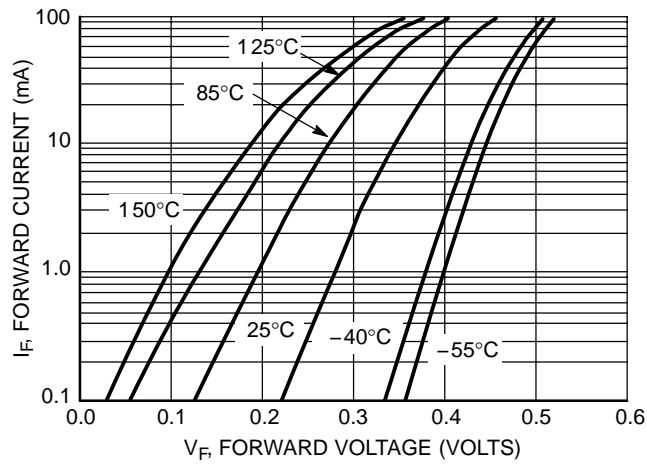


Figure 2. Forward Voltage

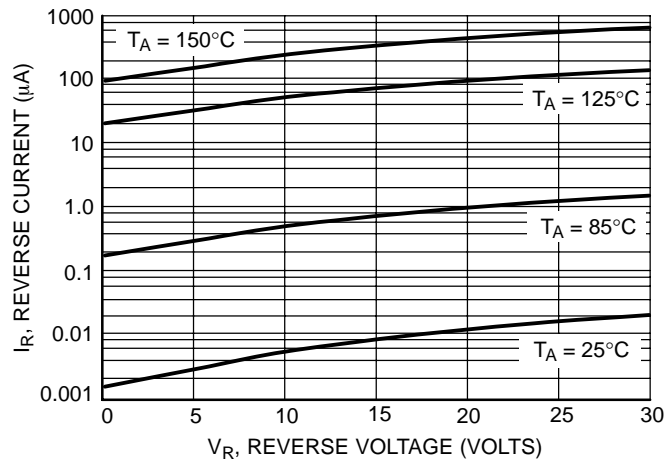


Figure 3. Leakage Current

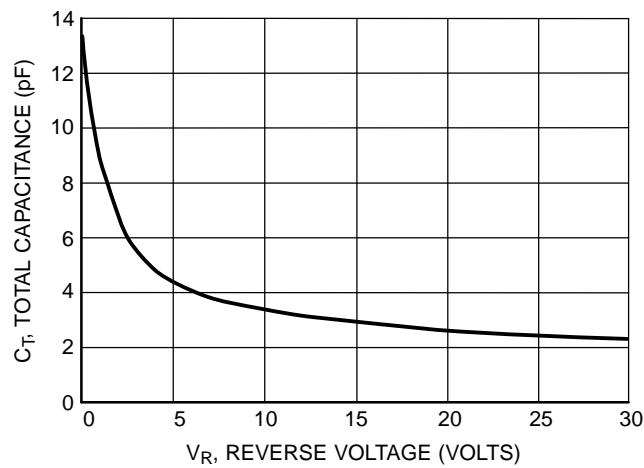
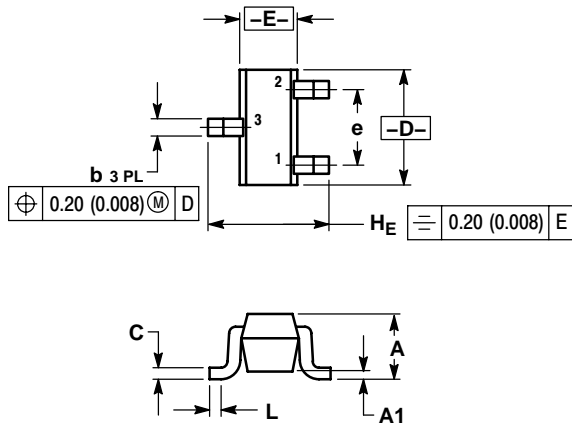


Figure 4. Total Capacitance

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PACKAGE DIMENSIONS

SC-75 / SOT-416
 PLASTIC PACKAGE
 CASE 463-01
 ISSUE F

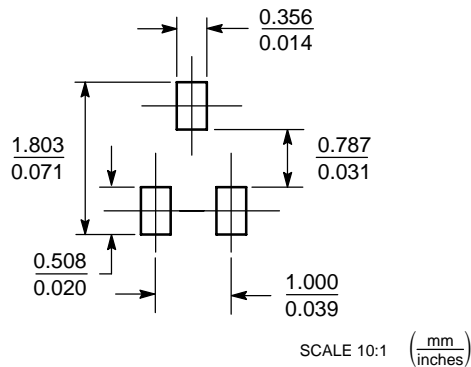


- NOTES:
 1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
 2. CONTROLLING DIMENSION: MILLIMETER.

DIM	MILLIMETERS			INCHES		
	MIN	NOM	MAX	MIN	NOM	MAX
A	0.70	0.80	0.90	0.027	0.031	0.035
A1	0.00	0.05	0.10	0.000	0.002	0.004
b	0.15	0.20	0.30	0.006	0.008	0.012
C	0.10	0.15	0.25	0.004	0.006	0.010
D	1.55	1.60	1.65	0.059	0.063	0.067
E	0.70	0.80	0.90	0.027	0.031	0.035
e	1.00 BSC			0.04 BSC		
L	0.10	0.15	0.20	0.004	0.006	0.008
HE	1.50	1.60	1.70	0.061	0.063	0.065

STYLE 3:
 PIN 1. ANODE
 PIN 2. ANODE
 PIN 3. CATHODE

SOLDERING FOOTPRINT*



*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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