

Features

- Low On-Resistance
 - 25mΩ @ $V_{GS} = 4.5V$
 - 29mΩ @ $V_{GS} = 2.5V$
 - 36mΩ @ $V_{GS} = 1.8V$
- Low Input Capacitance
- Fast Switching Speed
- Low Input/Output Leakage
- **Lead Free By Design/RoHS Compliant (Note 1)**
- **ESD Protected Up To 2kV**
- **"Green" Device (Note 2)**
- **Qualified to AEC-Q101 Standards for High Reliability**

Mechanical Data

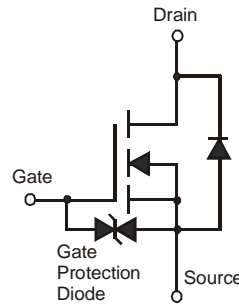
- Case: SOT-23
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020D
- Terminals: Finish — Matte Tin annealed over Copper leadframe. Solderable per MIL-STD-202, Method 208
- Terminals Connections: See Diagram Below
- Marking Information: See Page 4
- Ordering Information: See Page 4
- Weight: 0.008 grams (approximate)



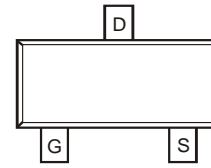
ESD PROTECTED TO 2kV



TOP VIEW



Internal Schematic



TOP VIEW

Maximum Ratings @ $T_A = 25^\circ C$ unless otherwise specified

Characteristic			Symbol	Value	Units
Drain-Source Voltage			V_{DSS}	20	V
Gate-Source Voltage			V_{GSS}	± 8	V
Continuous Drain Current (Note 3)	Steady State	$T_A = 25^\circ C$	I_D	6.5	A
		$T_A = 70^\circ C$		5.2	
Pulsed Drain Current			I_{DM}	30	A

Thermal Characteristics

Characteristic	Symbol	Value	Unit
Power Dissipation (Note 3)	P_D	0.81	W
Thermal Resistance, Junction to Ambient @ $T_A = 25^\circ C$	$R_{\theta JA}$	157	$^\circ C/W$
Operating and Storage Temperature Range	T_J, T_{STG}	-55 to +150	$^\circ C$

- Notes:
1. No purposefully added lead.
 2. Diodes Inc.'s "Green" policy can be found on our website at http://www.diodes.com/products/lead_free/index.php.
 3. Device mounted on FR-4 PCB with minimum recommended pad layout.

Electrical Characteristics @ $T_A = 25^\circ\text{C}$ unless otherwise specified

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 4)						
Drain-Source Breakdown Voltage	BV_{DSS}	20	—	—	V	$V_{GS} = 0V, I_D = 250\mu A$
Zero Gate Voltage Drain Current	I_{DSS}	—	—	1.0	μA	$T_J = 25^\circ\text{C}, V_{DS} = 20V, V_{GS} = 0V$
Gate-Source Leakage	I_{GSS}	—	—	± 10	μA	$V_{GS} = \pm 8V, V_{DS} = 0V$
ON CHARACTERISTICS (Note 4)						
Gate Threshold Voltage	$V_{GS(th)}$	0.5	—	0.9	V	$V_{DS} = V_{GS}, I_D = 250\mu A$
Static Drain-Source On-Resistance	$R_{DS(on)}$	—	21	25	m Ω	$V_{GS} = 4.5V, I_D = 6.5A$
			23	29		$V_{GS} = 2.5V, I_D = 5.5A$
			28	36		$V_{GS} = 1.8V, I_D = 3.5A$
Forward Transfer Admittance	$ Y_{fs} $	—	8	—	S	$V_{DS} = 10V, I_D = 5A$
DYNAMIC CHARACTERISTICS						
Input Capacitance	C_{iss}	—	151	—	pF	$V_{DS} = 10V, V_{GS} = 0V$ $f = 1.0\text{MHz}$
Output Capacitance	C_{oss}	—	91	—	pF	
Reverse Transfer Capacitance	C_{rss}	—	32	—	pF	
Total Gate Charge	Q_g	—	8.5	—	nC	$V_{GS} = 4.5V, V_{DS} = 10V, I_D = 6.5A$
Gate-Source Charge	Q_{gs}	—	1.6	—	nC	
Gate-Drain Charge	Q_{gd}	—	2.8	—	nC	
Turn-On Delay Time	$t_{D(on)}$	—	54	—	ns	$V_{DD} = 10V, V_{GS} = 4.5V,$ $R_L = 10\Omega, R_G = 6\Omega, I_D = 1A$
Turn-On Rise Time	t_r	—	66	—	ns	
Turn-Off Delay Time	$t_{D(off)}$	—	613	—	ns	
Turn-Off Fall Time	t_f	—	205	—	ns	

Notes: 4. Short duration pulse test used to minimize self-heating effect.

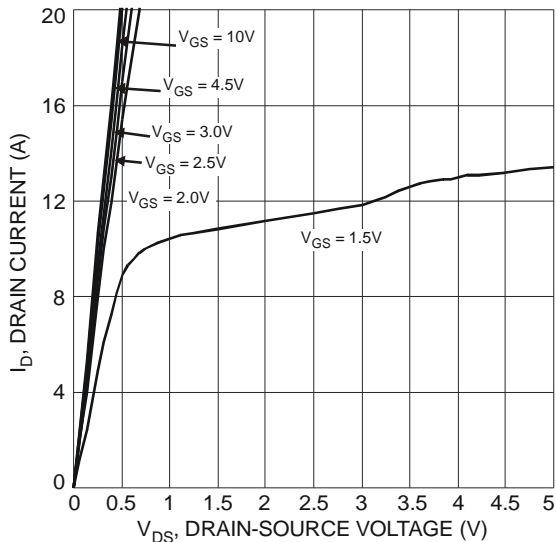


Fig. 1 Typical Output Characteristic

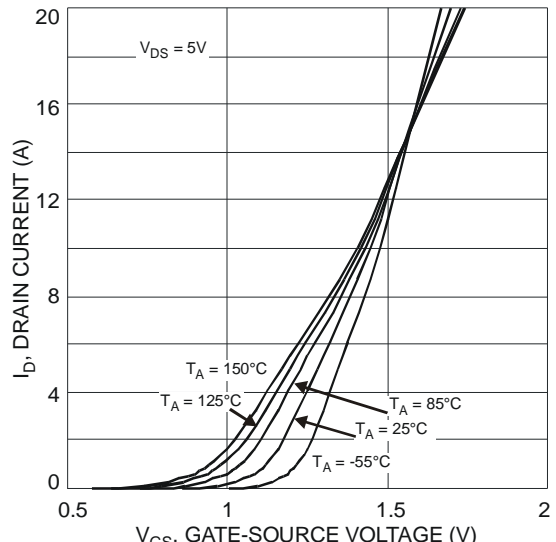


Fig. 2 Typical Transfer Characteristic

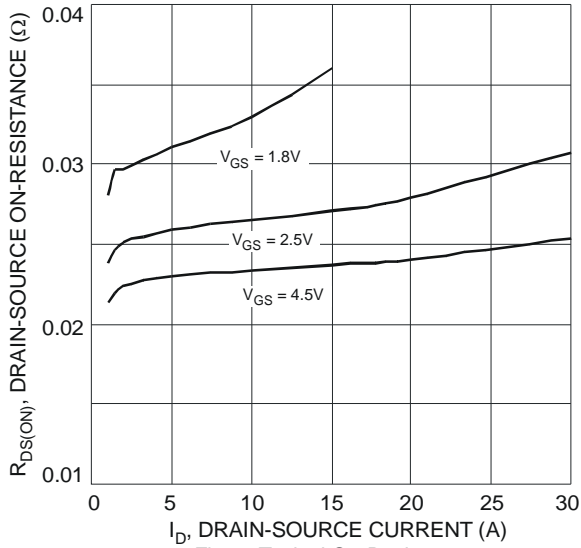


Fig. 3 Typical On-Resistance vs. Drain Current and Gate Voltage

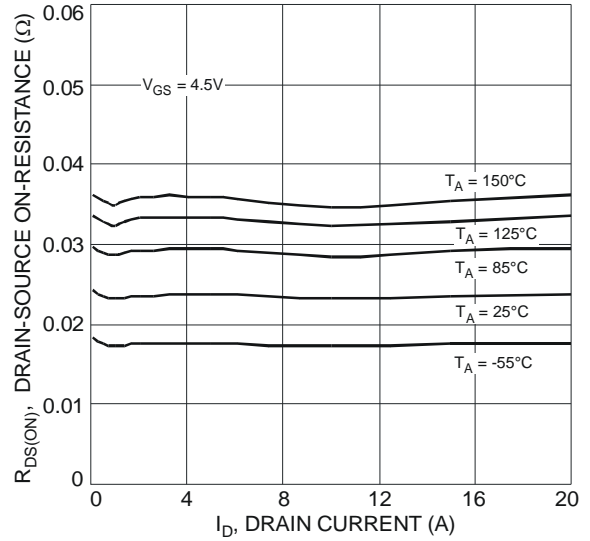


Fig. 4 Typical On-Resistance vs. Drain Current and Temperature

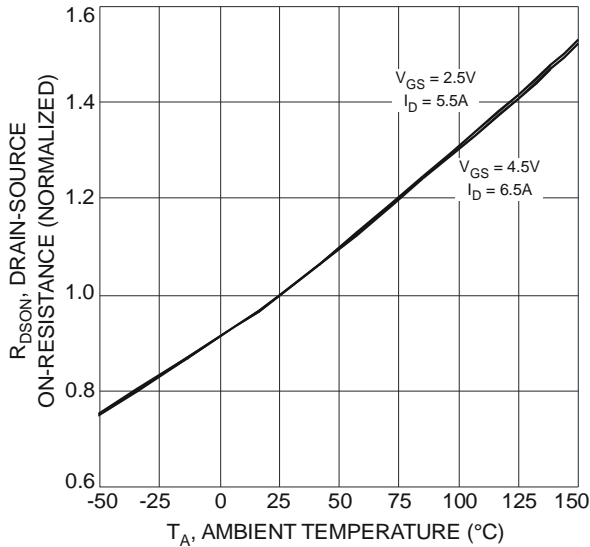


Fig. 5 On-Resistance Variation with Temperature

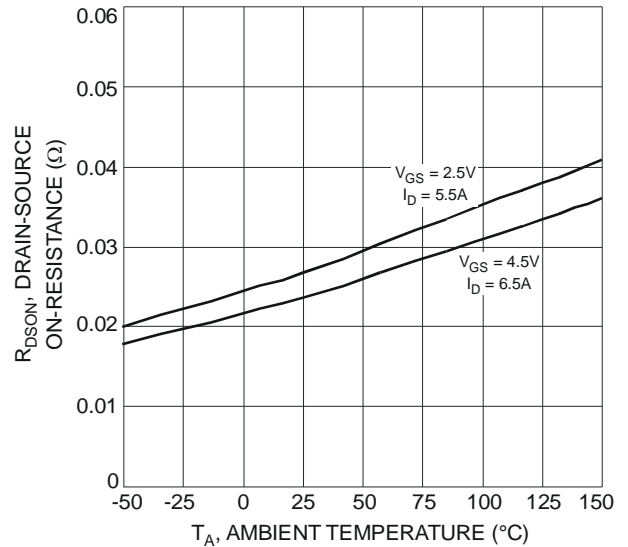


Fig. 6 On-Resistance Variation with Temperature

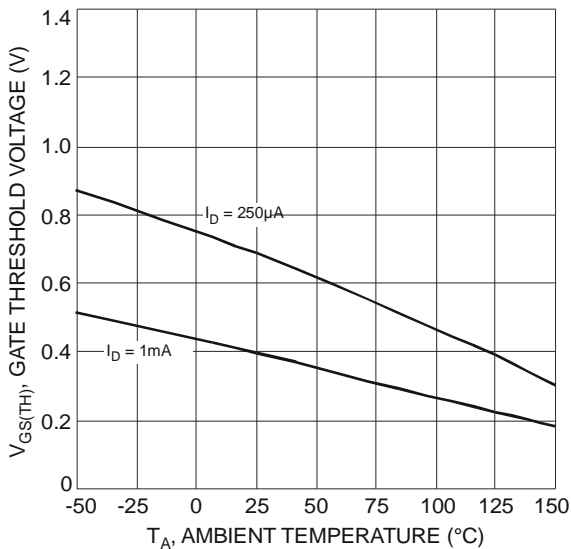


Fig. 7 Gate Threshold Variation vs. Ambient Temperature

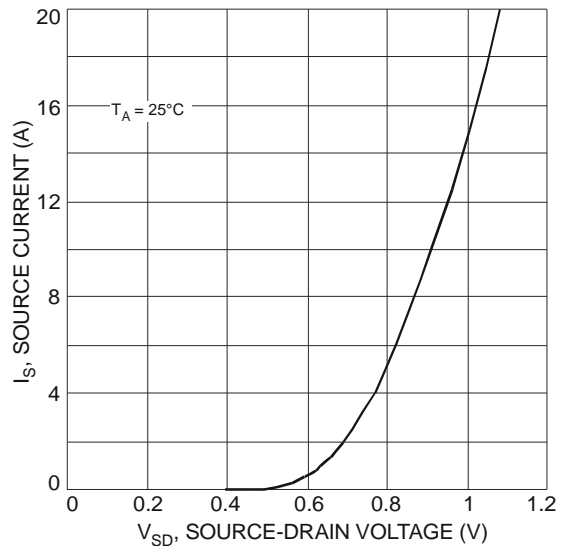


Fig. 8 Diode Forward Voltage vs. Current

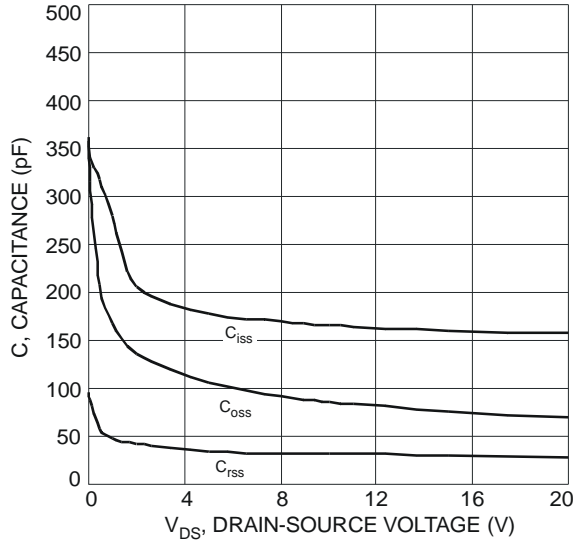


Fig. 9 Typical Total Capacitance

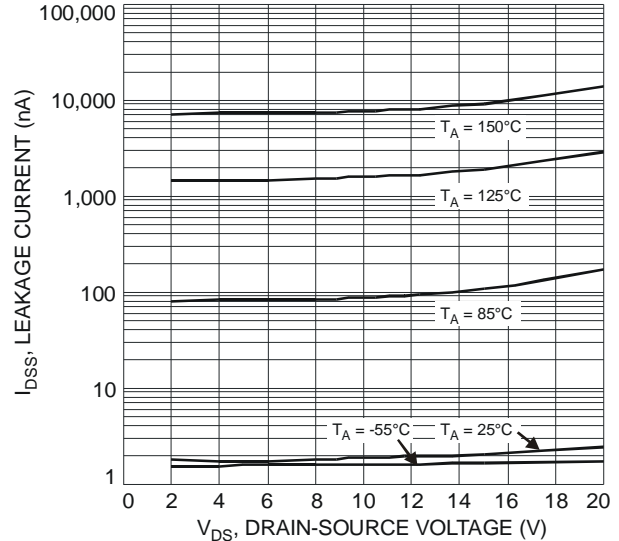


Fig. 10 Typical Leakage Current vs. Drain-Source Voltage

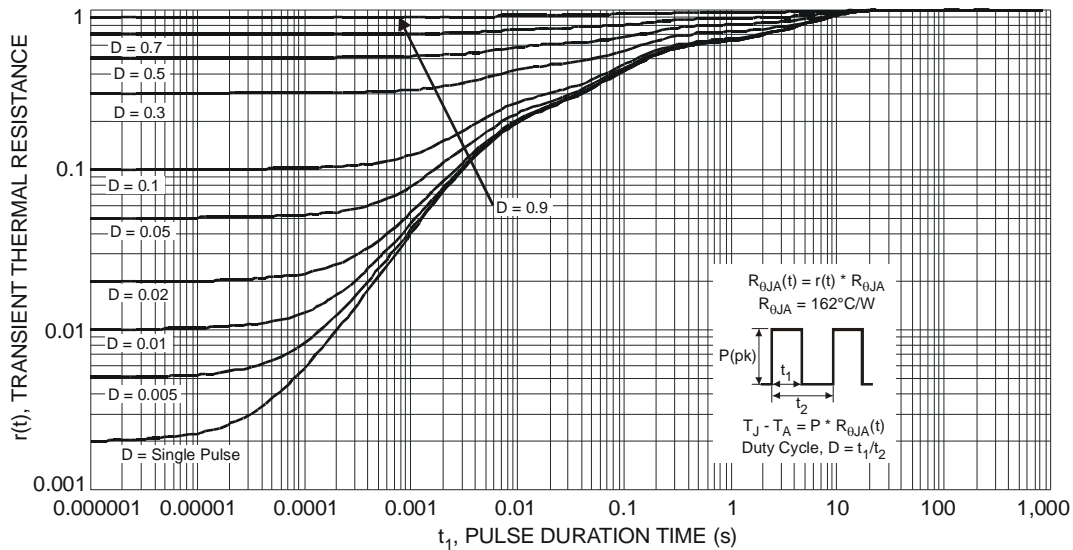


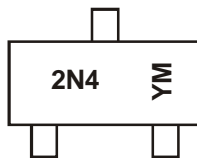
Fig. 11 Transient Thermal Response

Ordering Information (Note 5)

Part Number	Case	Packaging
DMG6968U-7	SOT-23	3000/Tape & Reel

Notes: 5. For packaging details, go to our website at <http://www.diodes.com/datasheets/ap02007.pdf>.

Marking Information

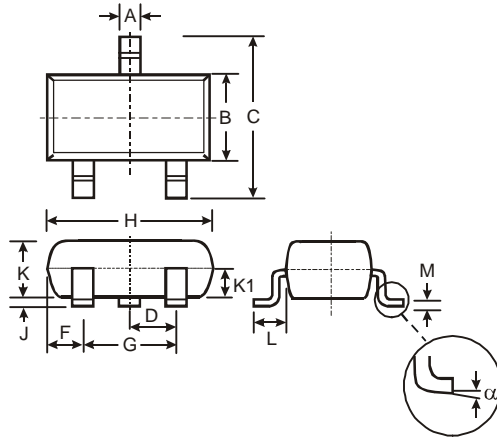


2N4 = Product Type Marking Code
 YM = Date Code Marking
 Y = Year (ex: W = 2009)
 M = Month (ex: 9 = September)

Date Code Key

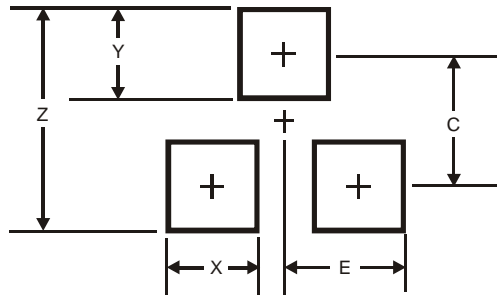
Year	2009	2010	2011	2012	2013	2014	2015					
Code	W	X	Y	Z	A	B	C					
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	O	N	D

Package Outline Dimensions



SOT-23			
Dim	Min	Max	Typ
A	0.37	0.51	0.40
B	1.20	1.40	1.30
C	2.30	2.50	2.40
D	0.89	1.03	0.915
F	0.45	0.60	0.535
G	1.78	2.05	1.83
H	2.80	3.00	2.90
J	0.013	0.10	0.05
K	0.903	1.10	1.00
K1	-	-	0.400
L	0.45	0.61	0.55
M	0.085	0.18	0.11
α	0°	8°	-
All Dimensions in mm			

Suggested Pad Layout



Dimensions	Value (in mm)
Z	2.9
X	0.8
Y	0.9
C	2.0
E	1.35

IMPORTANT NOTICE

Diodes Incorporated and its subsidiaries reserve the right to make modifications, enhancements, improvements, corrections or other changes without further notice to any product herein. Diodes Incorporated does not assume any liability arising out of the application or use of any product described herein; neither does it convey any license under its patent rights, nor the rights of others. The user of products in such applications shall assume all risks of such use and will agree to hold Diodes Incorporated and all the companies whose products are represented on our website, harmless against all damages.

LIFE SUPPORT

Diodes Incorporated products are not authorized for use as critical components in life support devices or systems without the expressed written approval of the President of Diodes Incorporated.