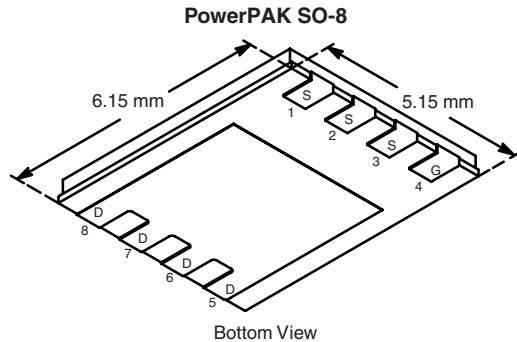


N-Channel 30-V (D-S) MOSFET

| PRODUCT SUMMARY | | | |
|---------------------|----------------------------------|---------------------------------|-----------------------|
| V _{DS} (V) | R _{DS(on)} (Ω) | I _D (A) ^a | Q _g (Typ.) |
| 30 | 0.0054 at V _{GS} = 10 V | 40 ^g | 21.5 nC |
| | 0.007 at V _{GS} = 4.5 V | 40 ^g | |

FEATURES

- Halogen-free available
- TrenchFET[®] Power MOSFET
- 100 % R_g Tested
- 100 % Avalanche Tested

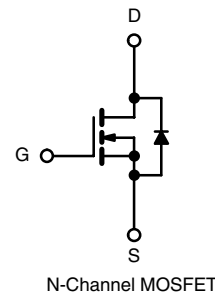


Bottom View

Ordering Information: Si7634BDP-T1-E3 (Lead (Pb)-free)
Si7634BDP-T1-GE3 (Lead (Pb)-free and Halogen-free)

APPLICATIONS

- Notebook PC Core
 - Low Side
 - High Side



| ABSOLUTE MAXIMUM RATINGS T _A = 25 °C, unless otherwise noted | | | |
|---|-----------------------------------|----------------------|------|
| Parameter | Symbol | Limit | Unit |
| Drain-Source Voltage | V _{DS} | 30 | V |
| Gate-Source Voltage | V _{GS} | ± 20 | |
| Continuous Drain Current (T _J = 150 °C) | T _C = 25 °C | 40 ^g | A |
| | T _C = 70 °C | 40 ^g | |
| | T _A = 25 °C | 22.5 ^{b, c} | |
| | T _A = 70 °C | 18.0 ^{b, c} | |
| Pulsed Drain Current | I _{DM} | 70 | |
| Continuous Source-Drain Diode Current | T _C = 25 °C | 40 ^g | |
| | T _A = 25 °C | 4.5 ^{b, c} | |
| Single Pulse Avalanche Current | I _{AS} | 30 | |
| Single Pulse Avalanche Energy | E _{AS} | 45 | mJ |
| Maximum Power Dissipation | T _C = 25 °C | 48 | W |
| | T _C = 70 °C | 31 | |
| | T _A = 25 °C | 5.0 ^{b, c} | |
| | T _A = 70 °C | 3.2 ^{b, c} | |
| Operating Junction and Storage Temperature Range | T _J , T _{stg} | - 55 to 150 | °C |
| Soldering Recommendations (Peak Temperature) ^{d, e} | | 260 | |

| THERMAL RESISTANCE RATINGS | | | | | |
|---|--------------|-------------------|---------|---------|------|
| Parameter | | Symbol | Typical | Maximum | Unit |
| Maximum Junction-to-Ambient ^{b, f} | t ≤ 10 s | R _{thJA} | 20 | 25 | °C/W |
| Maximum Junction-to-Case (Drain) | Steady State | R _{thJC} | 2.1 | 2.6 | |

Notes:

- Based on T_C = 25 °C.
- Surface Mounted on 1" x 1" FR4 board.
- t = 10 s.
- See Solder Profile (<http://www.vishay.com/ppg?73257>). The PowerPAK SO-8 is a leadless package. The end of the lead terminal is exposed copper (not plated) as a result of the singulation process in manufacturing. A solder fillet at the exposed copper tip cannot be guaranteed and is not required to ensure adequate bottom side solder interconnection.
- Rework Conditions: manual soldering with a soldering iron is not recommended for leadless components.
- Maximum under Steady State conditions is 65 °C/W.
- Package Limited.

| SPECIFICATIONS $T_J = 25\text{ }^\circ\text{C}$, unless otherwise noted | | | | | | |
|--|-------------------------|---|------|--------|-----------|---------------|
| Parameter | Symbol | Test Conditions | Min. | Typ. | Max. | Unit |
| Static | | | | | | |
| Drain-Source Breakdown Voltage | V_{DS} | $V_{GS} = 0\text{ V}, I_D = 250\text{ }\mu\text{A}$ | 30 | | | V |
| V_{DS} Temperature Coefficient | $\Delta V_{DS}/T_J$ | $I_D = 250\text{ }\mu\text{A}$ | | 33 | | mV/°C |
| $V_{GS(th)}$ Temperature Coefficient | $\Delta V_{GS(th)}/T_J$ | | | -6.4 | | |
| Gate-Source Threshold Voltage | $V_{GS(th)}$ | $V_{DS} = V_{GS}, I_D = 250\text{ }\mu\text{A}$ | 1.4 | | 2.6 | V |
| Gate-Source Leakage | I_{GSS} | $V_{DS} = 0\text{ V}, V_{GS} = \pm 20\text{ V}$ | | | ± 100 | nA |
| Zero Gate Voltage Drain Current | I_{DSS} | $V_{DS} = 30\text{ V}, V_{GS} = 0\text{ V}$ | | | 1 | μA |
| | | $V_{DS} = 30\text{ V}, V_{GS} = 0\text{ V}, T_J = 55\text{ }^\circ\text{C}$ | | | 10 | |
| On-State Drain Current ^a | $I_{D(on)}$ | $V_{DS} \geq 5\text{ V}, V_{GS} = 10\text{ V}$ | 30 | | | A |
| Drain-Source On-State Resistance ^a | $R_{DS(on)}$ | $V_{GS} = 10\text{ V}, I_D = 15\text{ A}$ | | 0.0045 | 0.0054 | Ω |
| | | $V_{GS} = 4.5\text{ V}, I_D = 10\text{ A}$ | | 0.0058 | 0.007 | |
| Forward Transconductance ^a | g_{fs} | $V_{DS} = 15\text{ V}, I_D = 15\text{ A}$ | | 78 | | S |
| Dynamic^b | | | | | | |
| Input Capacitance | C_{iss} | $V_{DS} = 15\text{ V}, V_{GS} = 0\text{ V}, f = 1\text{ MHz}$ | | 3150 | | pF |
| Output Capacitance | C_{oss} | | | 420 | | |
| Reverse Transfer Capacitance | C_{rss} | | | 166 | | |
| Total Gate Charge | Q_g | $V_{DS} = 15\text{ V}, V_{GS} = 10\text{ V}, I_D = 10\text{ A}$ | | 45.5 | 68 | nC |
| | | $V_{DS} = 15\text{ V}, V_{GS} = 4.5\text{ V}, I_D = 10\text{ A}$ | | 21.5 | 33 | |
| Gate-Source Charge | Q_{gs} | | 8 | | | |
| Gate-Drain Charge | Q_{gd} | | 6.2 | | | |
| Gate Resistance | R_g | $f = 1\text{ MHz}$ | | 0.8 | 1.6 | Ω |
| Turn-On Delay Time | $t_{d(on)}$ | $V_{DD} = 15\text{ V}, R_L = 1.5\text{ }\Omega$ $I_D \cong 10\text{ A}, V_{GEN} = 10\text{ V}, R_g = 1\text{ }\Omega$ | | 12 | 24 | ns |
| Rise Time | t_r | | | 10 | 20 | |
| Turn-Off Delay Time | $t_{d(off)}$ | | | 34 | 55 | |
| Fall Time | t_f | | | 8 | 16 | |
| Turn-On Delay Time | $t_{d(on)}$ | $V_{DD} = 15\text{ V}, R_L = 1.5\text{ }\Omega$ $I_D \cong 10\text{ A}, V_{GEN} = 4.5\text{ V}, R_g = 1\text{ }\Omega$ | | 30 | 50 | |
| Rise Time | t_r | | | 12 | 24 | |
| Turn-Off Delay Time | $t_{d(off)}$ | | | 34 | 55 | |
| Fall Time | t_f | | | 12 | 24 | |
| Drain-Source Body Diode Characteristics | | | | | | |
| Continuous Source-Drain Diode Current | I_S | $T_C = 25\text{ }^\circ\text{C}$ | | | 40 | A |
| Pulse Diode Forward Current ^a | I_{SM} | | | | 70 | |
| Body Diode Voltage | V_{SD} | $I_S = 3\text{ A}$ | | 0.75 | 1.1 | V |
| Body Diode Reverse Recovery Time | t_{rr} | $I_F = 10\text{ A}, di/dt = 100\text{ A}/\mu\text{s}, T_J = 25\text{ }^\circ\text{C}$ | | 30 | 60 | ns |
| Body Diode Reverse Recovery Charge | Q_{rr} | | | 35 | 70 | nC |
| Reverse Recovery Fall Time | t_a | | | 20 | | ns |
| Reverse Recovery Rise Time | t_b | | | 10 | | |

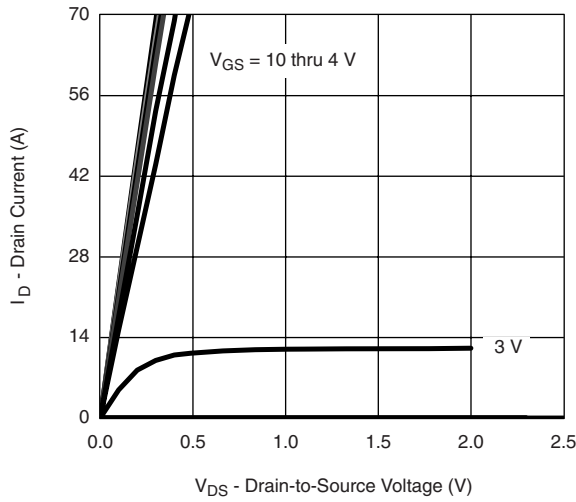
Notes:

- a. Pulse test; pulse width $\leq 300\text{ }\mu\text{s}$, duty cycle $\leq 2\%$.
b. Guaranteed by design, not subject to production testing.

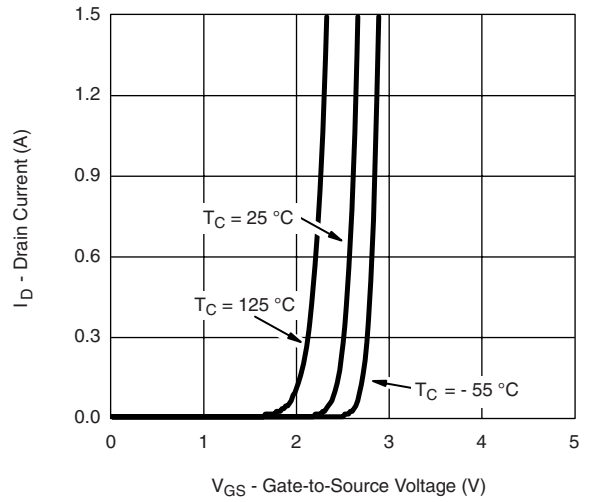
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.



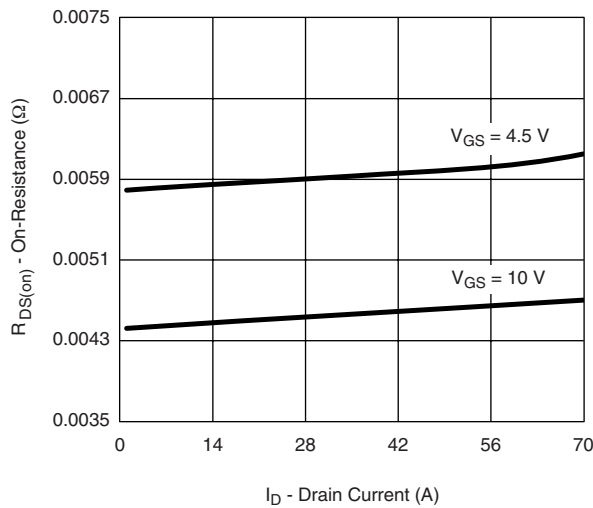
TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



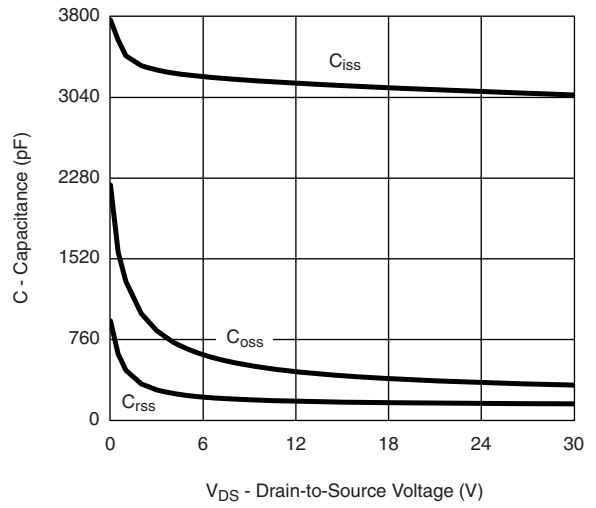
Output Characteristics



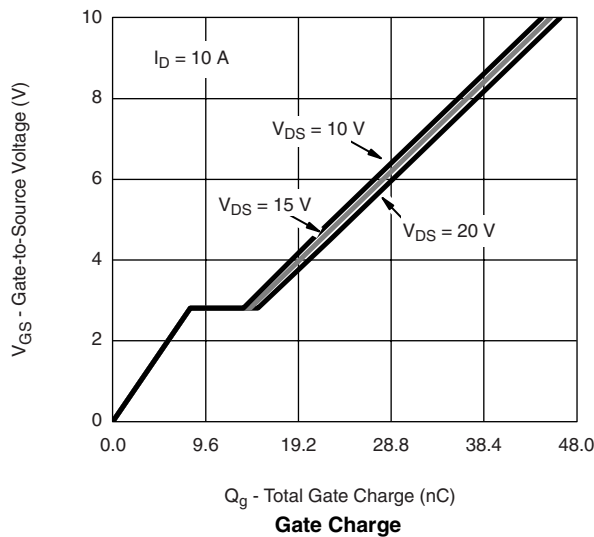
Transfer Characteristics



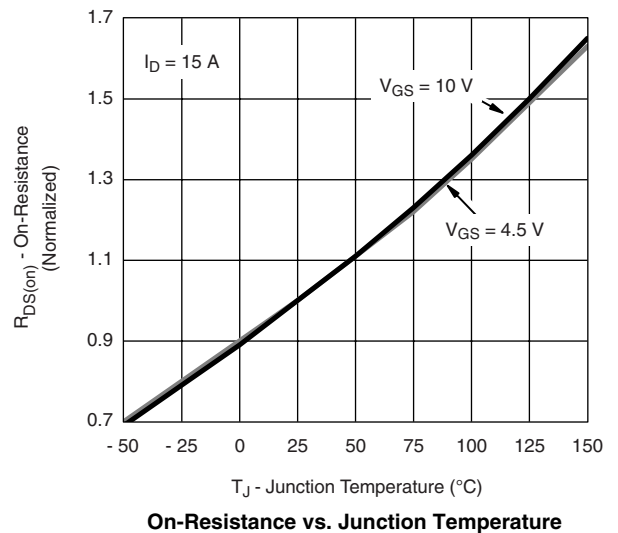
On-Resistance vs. Drain Current and Gate Voltage



Capacitance



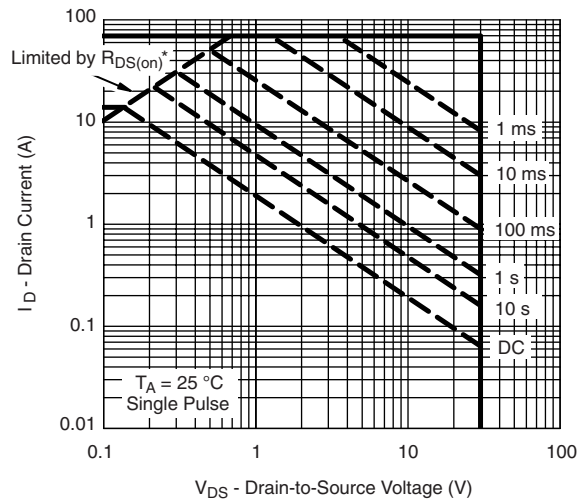
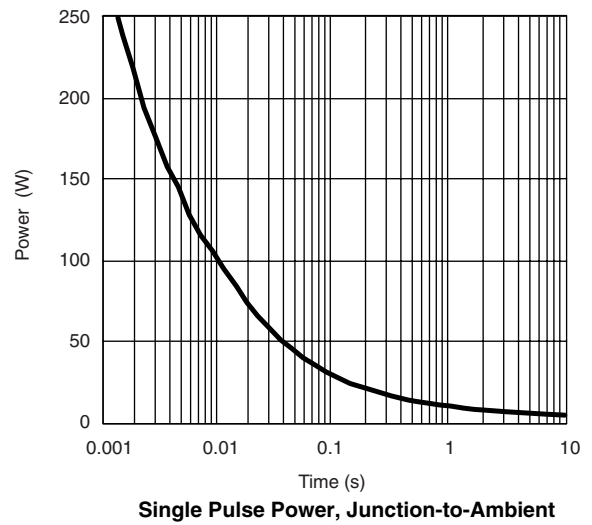
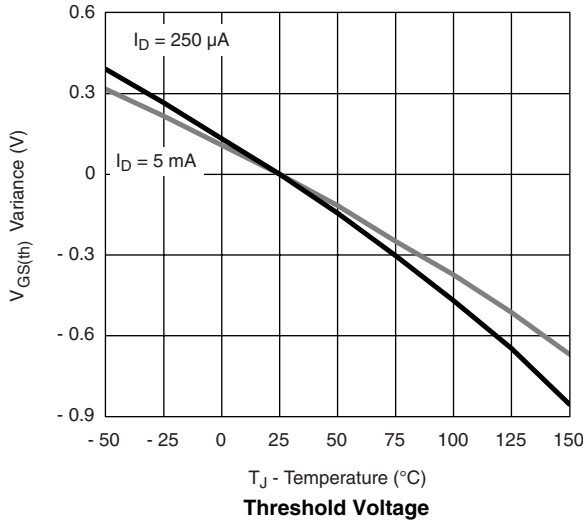
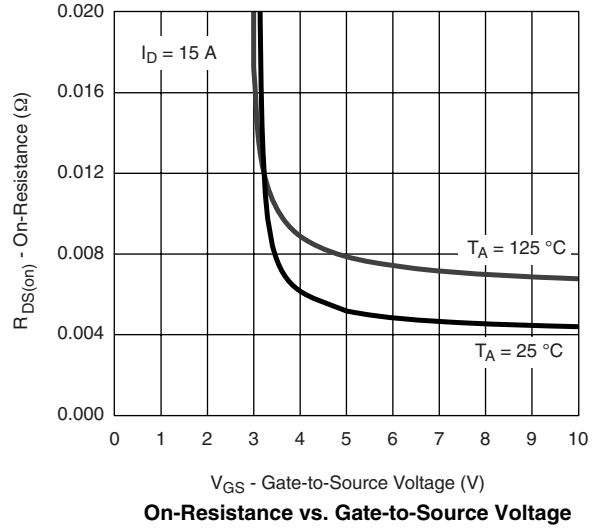
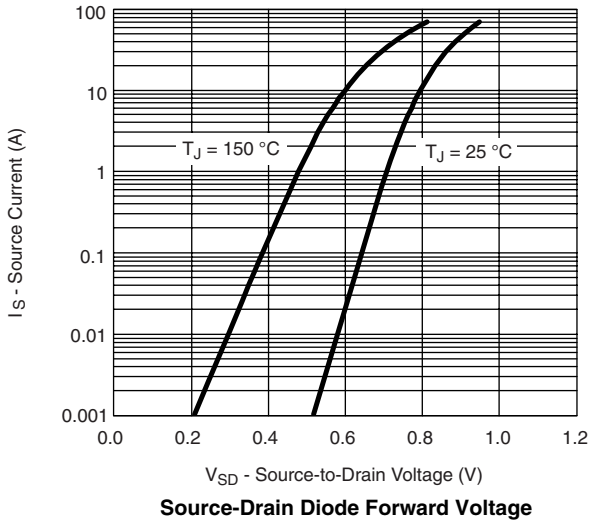
Gate Charge



On-Resistance vs. Junction Temperature



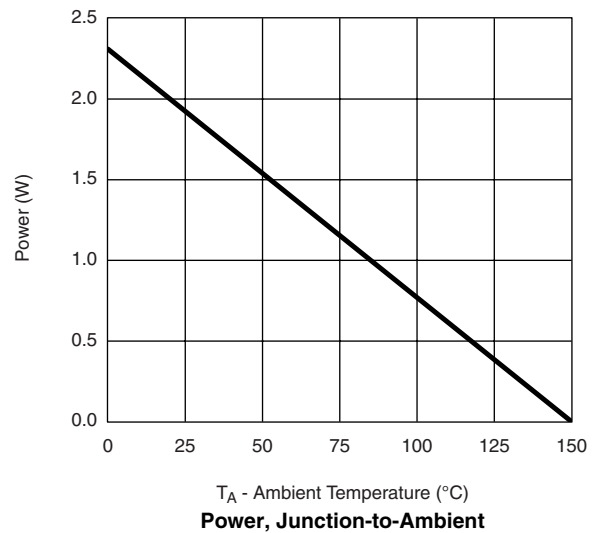
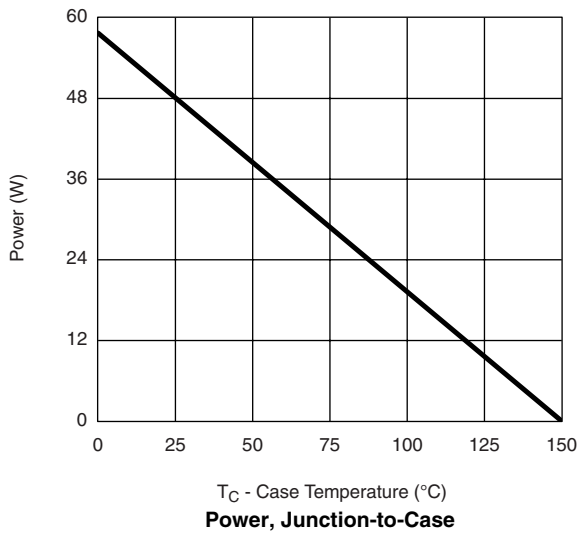
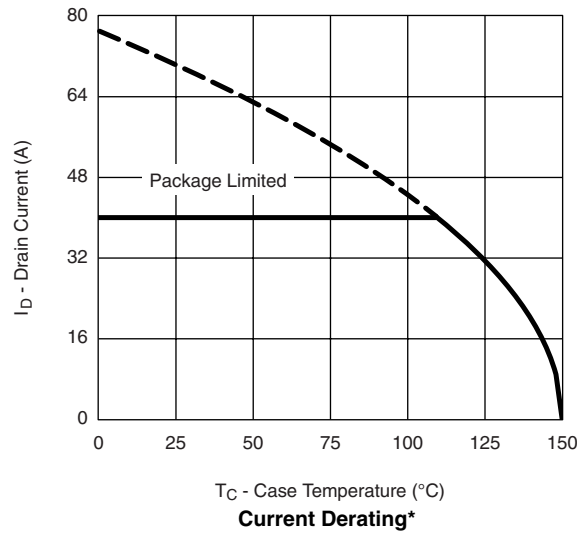
TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



* $V_{GS} >$ minimum V_{GS} at which $R_{DS(on)}$ is specified



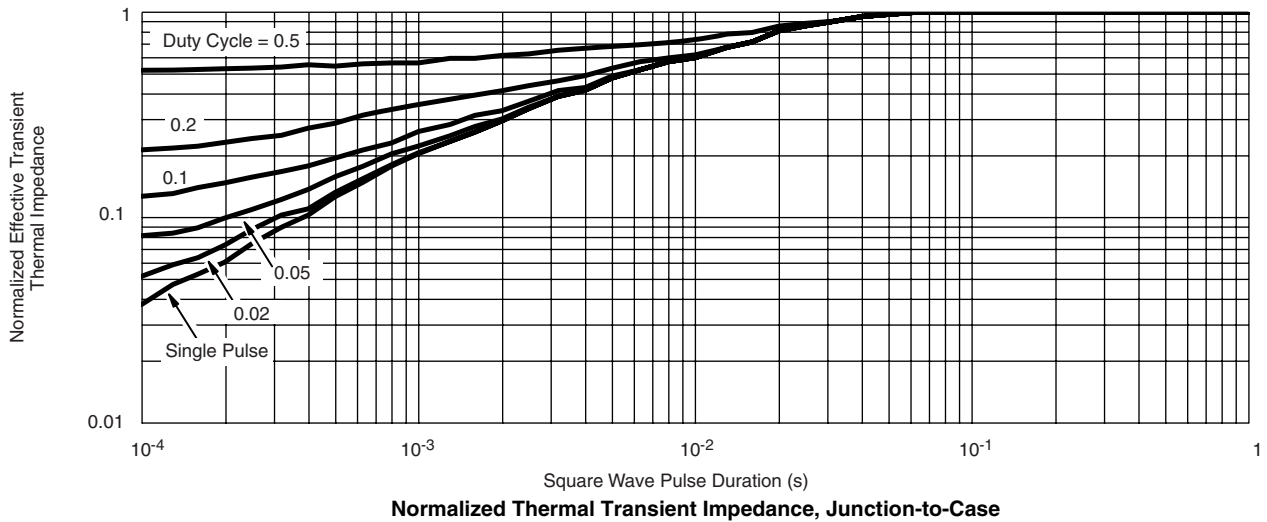
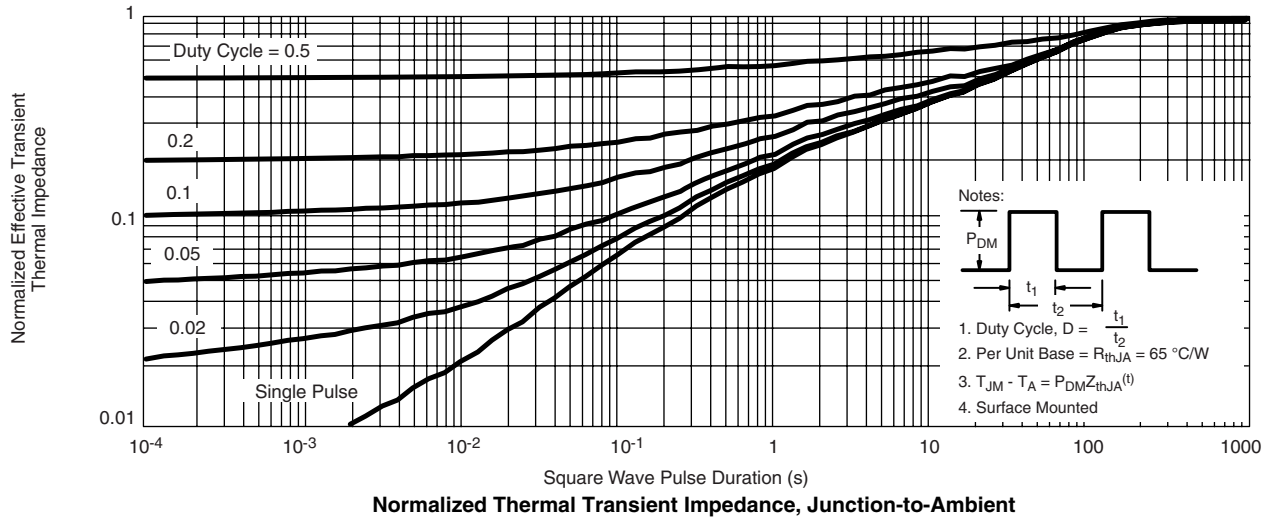
TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



* The power dissipation P_D is based on $T_{J(max)} = 175$ °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit.



TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package/tape drawings, part marking, and reliability data, see <http://www.vishay.com/ppg?74031>.



Notice

Specifications of the products displayed herein are subject to change without notice. Vishay Intertechnology, Inc., or anyone on its behalf, assumes no responsibility or liability for any errors or inaccuracies.

Information contained herein is intended to provide a product description only. No license, express or implied, by estoppel or otherwise, to any intellectual property rights is granted by this document. Except as provided in Vishay's terms and conditions of sale for such products, Vishay assumes no liability whatsoever, and disclaims any express or implied warranty, relating to sale and/or use of Vishay products including liability or warranties relating to fitness for a particular purpose, merchantability, or infringement of any patent, copyright, or other intellectual property right.

The products shown herein are not designed for use in medical, life-saving, or life-sustaining applications. Customers using or selling these products for use in such applications do so at their own risk and agree to fully indemnify Vishay for any damages resulting from such improper use or sale.