

## Dual P-Channel 20-V (D-S) MOSFET

PRODUCT SUMMARY		
$V_{DS}$ (V)	$r_{DS(on)}$ ( $\Omega$ )	$I_D$ (A)
- 20	0.037 at $V_{GS} = - 4.5$ V	- 7.4
	0.048 at $V_{GS} = - 2.5$ V	- 6.5
	0.066 at $V_{GS} = - 1.8$ V	- 55

### FEATURES

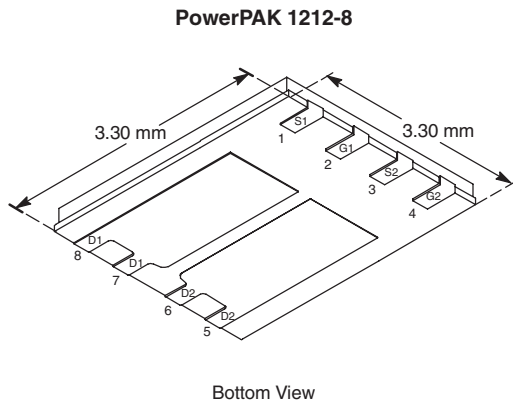
- TrenchFET<sup>®</sup> Power MOSFET
- New Low Thermal Resistance PowerPAK<sup>®</sup> Package



**RoHS**  
COMPLIANT

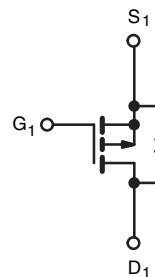
### APPLICATIONS

- Portable
  - PA Switch
  - Battery Switch
  - Load Switch

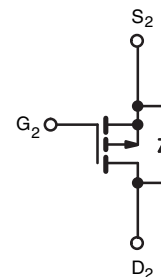


Bottom View

Ordering Information: Si7913DN-T1-E3 (Lead (Pb)-free)



P-Channel MOSFET



P-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS $T_A = 25$ °C, unless otherwise noted					
Parameter	Symbol	10 sec	Steady State	Unit	
Drain-Source Voltage	$V_{DS}$	- 20		V	
Gate-Source Voltage	$V_{GS}$	$\pm 8$			
Continuous Drain Current ( $T_J = 150$ °C) <sup>a</sup>	$I_D$	$T_A = 25$ °C	- 7.4	- 5.0	A
		$T_A = 85$ °C	- 5.3	- 3.6	
Pulsed Drain Current	$I_{DM}$	- 20		A	
Continuous Source Current (Diode Conduction) <sup>a</sup>	$I_S$	- 2.3	- 1.1		
Maximum Power Dissipation <sup>a</sup>	$P_D$	$T_A = 25$ °C	2.8	1.3	W
		$T_A = 85$ °C	1.5	0.85	
Operating Junction and Storage Temperature Range	$T_J, T_{stg}$	- 55 to 150		°C	
Soldering Recommendations (Peak Temperature) <sup>b, c</sup>		260			

THERMAL RESISTANCE RATINGS					
Parameter	Symbol	Typical	Maximum	Unit	
Maximum Junction-to-Ambient <sup>a</sup>	$R_{thJA}$	$t \leq 10$ sec	35	44	°C/W
		Steady State	75	94	
Maximum Junction-to-Case	$R_{thJC}$	4	5		

Notes:

a. Surface Mounted on 1" x 1" FR4 Board.

b. See Solder Profile (<http://www.vishay.com/ppg?73257>). The PowerPAK 1212-8 is a leadless package. The end of the lead terminal is exposed copper (not plated) as a result of the singulation process in manufacturing. A solder fillet at the exposed copper tip cannot be guaranteed and is not required to ensure adequate bottom side solder interconnection.

c. Rework Conditions: manual soldering with a soldering iron is not recommended for leadless components.

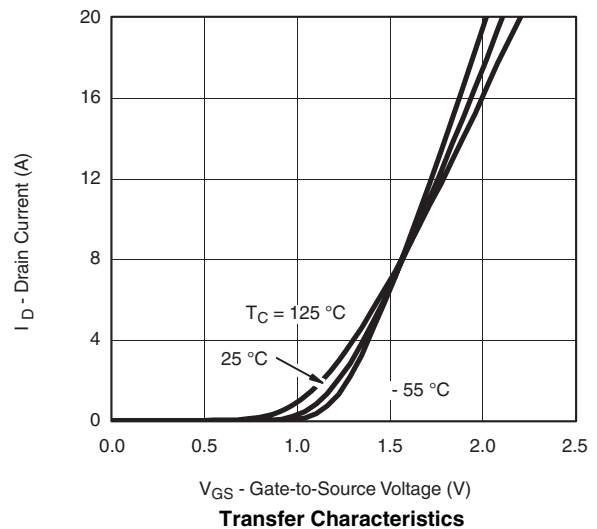
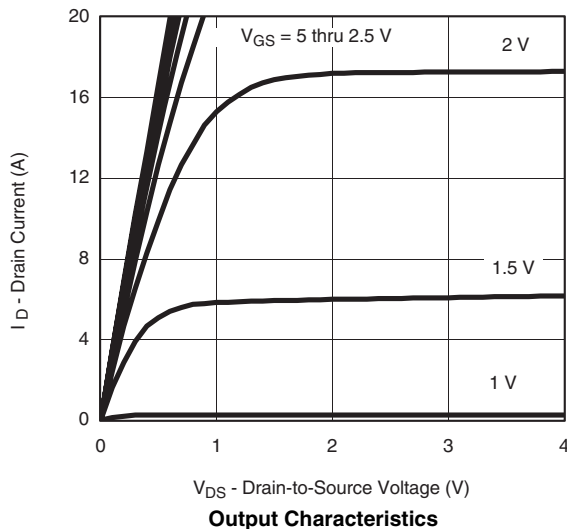
<b>SPECIFICATIONS</b> $T_J = 25\text{ }^\circ\text{C}$ , unless otherwise noted						
Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
<b>Static</b>						
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_D = -250\text{ }\mu\text{A}$	-0.40		-1.0	V
Gate-Body Leakage	$I_{GSS}$	$V_{DS} = 0\text{ V}, V_{GS} = \pm 8\text{ V}$			$\pm 100$	nA
Zero Gate Voltage Drain Current	$I_{DSS}$	$V_{DS} = -20\text{ V}, V_{GS} = 0\text{ V}$			-1	$\mu\text{A}$
		$V_{DS} = -20\text{ V}, V_{GS} = 0\text{ V}, T_J = 85\text{ }^\circ\text{C}$			-5	
On-State Drain Current <sup>a</sup>	$I_{D(on)}$	$V_{DS} \leq -5\text{ V}, V_{GS} = -4.5\text{ V}$	-20			A
Drain-Source On-State Resistance <sup>a</sup>	$r_{DS(on)}$	$V_{GS} = -4.5\text{ V}, I_D = -7.4\text{ A}$		0.029	0.037	$\Omega$
		$V_{GS} = -2.5\text{ V}, I_D = -6.5\text{ A}$		0.038	0.048	
		$V_{GS} = -1.8\text{ V}, I_D = -1.5\text{ A}$		0.051	0.066	
Forward Transconductance <sup>a</sup>	$g_{fs}$	$V_{DS} = -6\text{ V}, I_D = -7.4\text{ A}$		20		S
Diode Forward Voltage <sup>a</sup>	$V_{SD}$	$I_S = -2.3\text{ A}, V_{GS} = 0\text{ V}$		-0.74	-1.2	V
<b>Dynamic<sup>b</sup></b>						
Total Gate Charge	$Q_g$	$V_{DS} = -10\text{ V}, V_{GS} = -4.5\text{ V}, I_D = -7.4\text{ A}$		15.3	24	nC
Gate-Source Charge	$Q_{gs}$		2.0			
Gate-Drain Charge	$Q_{gd}$		3.9			
Gate Resistance	$R_g$	$f = 1\text{ MHz}$		7		$\Omega$
Turn-On Delay Time	$t_{d(on)}$	$V_{DD} = -10\text{ V}, R_L = 10\text{ }\Omega$ $I_D \cong -1\text{ A}, V_{GEN} = -4.5\text{ V}, R_g = 6\text{ }\Omega$		20	30	ns
Rise Time	$t_r$		70	110		
Turn-Off Delay Time	$t_{d(off)}$		72	110		
Fall Time	$t_f$		150	225		
Source-Drain Reverse Recovery Time	$t_{rr}$	$I_F = -2.3\text{ A}, di/dt = 100\text{ A}/\mu\text{s}$		25	50	

Notes:

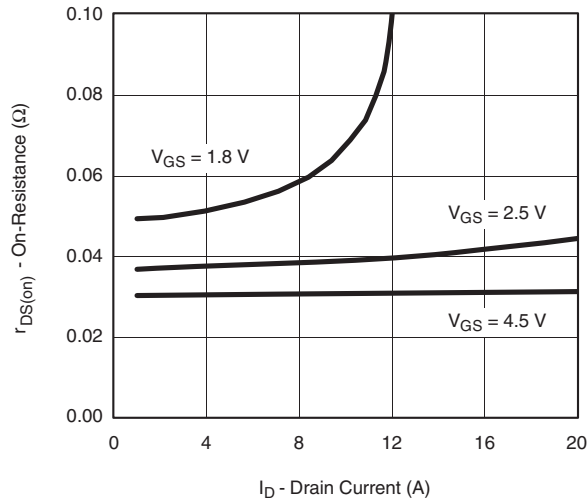
- a. Pulse test; pulse width  $\leq 300\text{ }\mu\text{s}$ , duty cycle  $\leq 2\%$ .
- b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

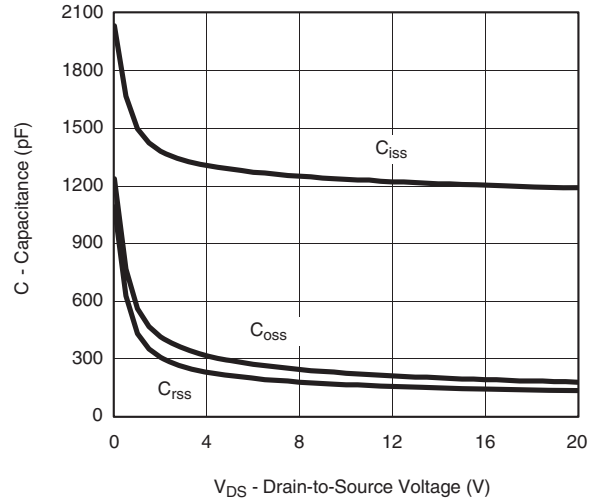
## TYPICAL CHARACTERISTICS 25 °C unless noted



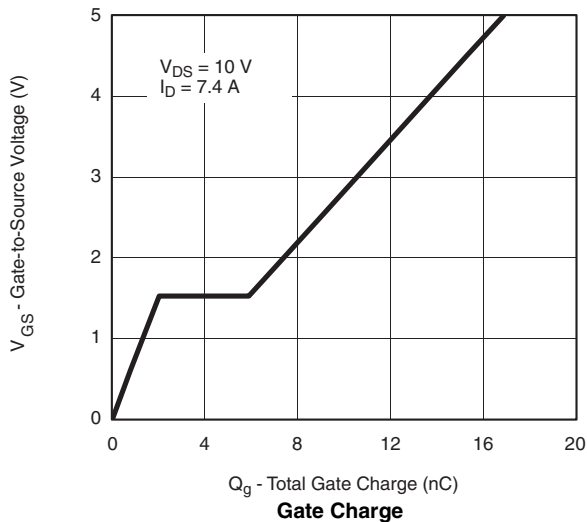
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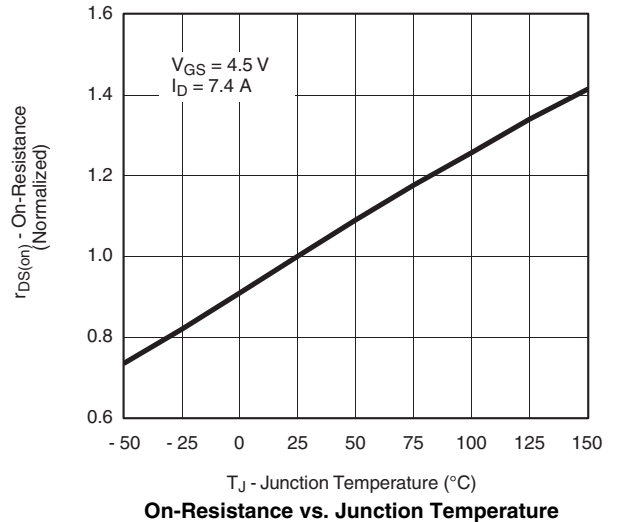
**On-Resistance vs. Drain Current**



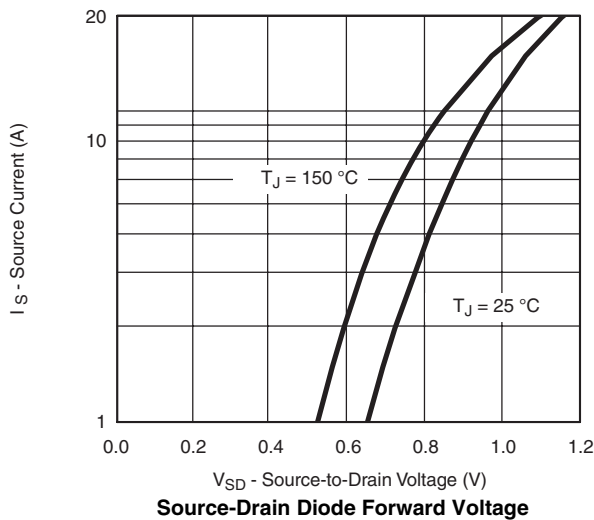
**Capacitance**



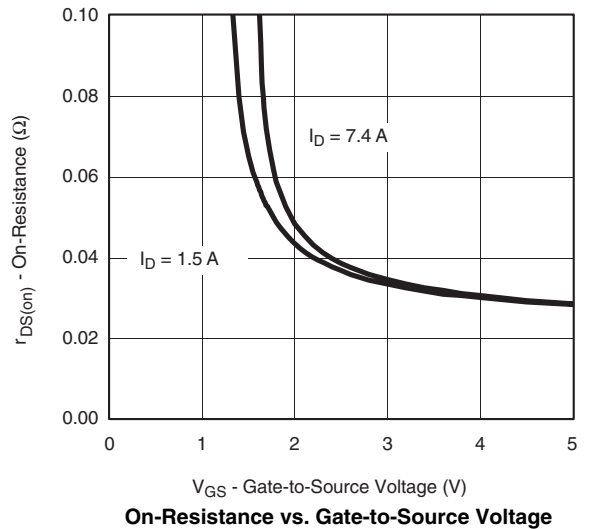
**Gate Charge**



**On-Resistance vs. Junction Temperature**

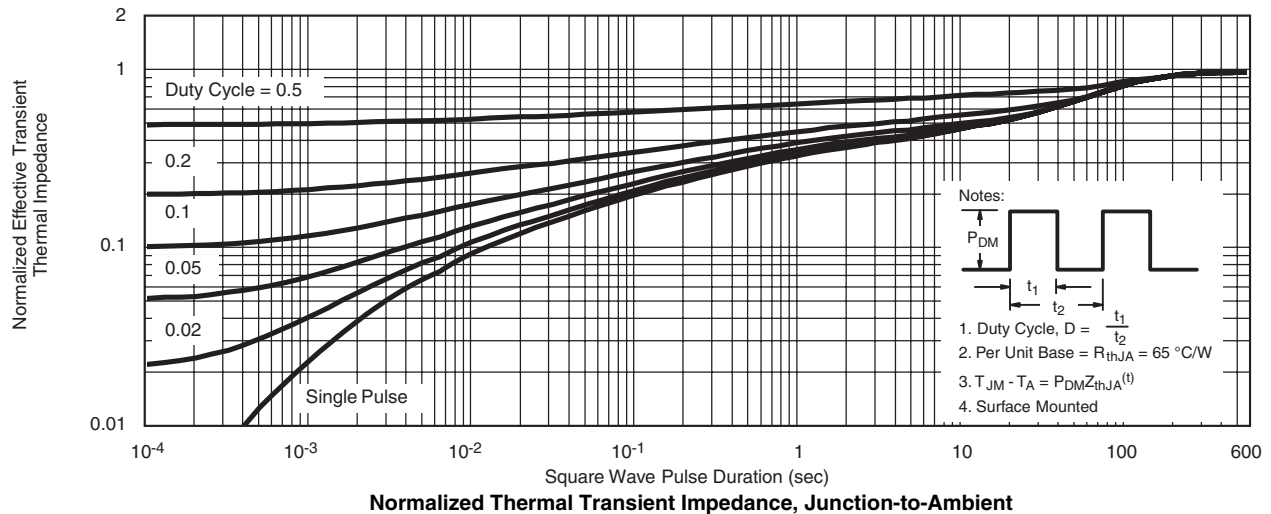
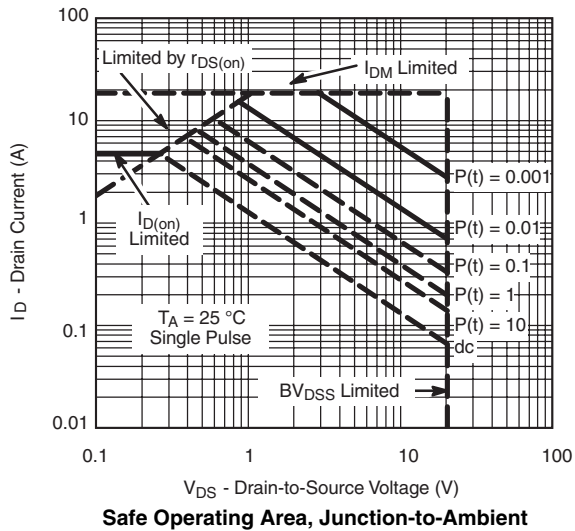
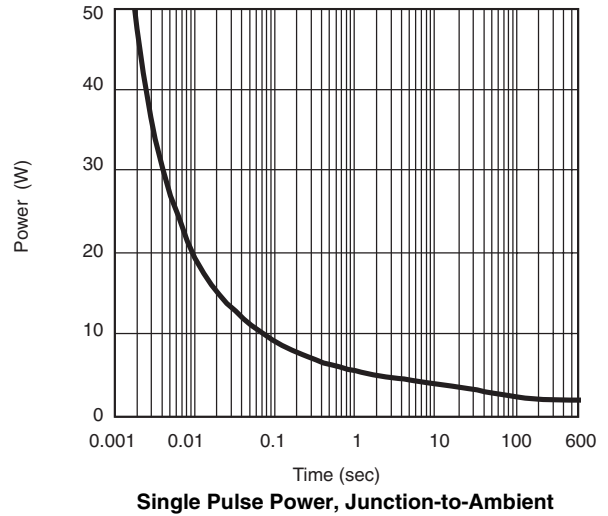
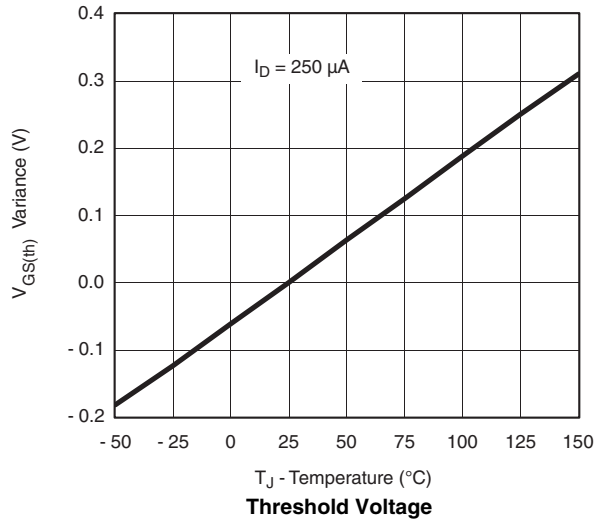


**Source-Drain Diode Forward Voltage**



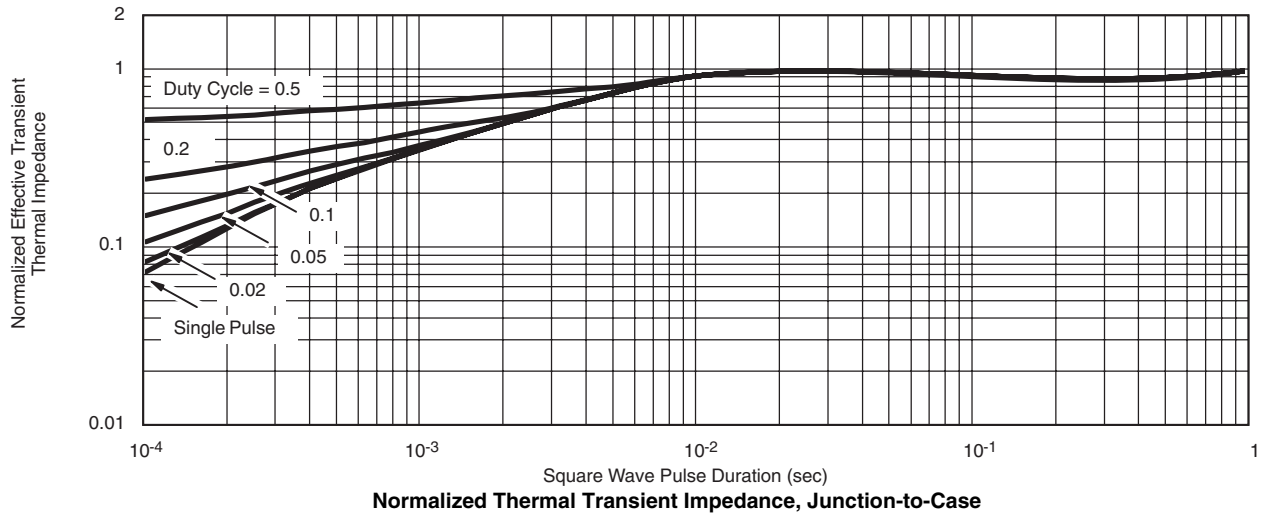
**On-Resistance vs. Gate-to-Source Voltage**

## TYPICAL CHARACTERISTICS 25 °C unless noted





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