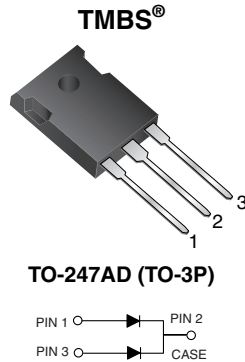


# Dual High-Voltage Trench MOS Barrier Schottky Rectifier

Ultra Low  $V_F = 0.372$  V at  $I_F = 5$  A



## FEATURES

- Trench MOS Schottky technology
- Low forward voltage drop, low power losses
- High efficiency operation
- Low thermal resistance
- Solder dip 260 °C, 40 s
- Component in accordance to RoHS 2002/95/EC and WEEE 2002/96/EC



RoHS  
COMPLIANT

## TYPICAL APPLICATIONS

For use in high frequency inverters, switching power supplies, freewheeling diodes, OR-ing diode, dc-to-dc converters and reverse battery protection.

## PRIMARY CHARACTERISTICS

$I_{F(AV)}$	2 x 20 A
$V_{RRM}$	100 V
$I_{FSM}$	300 A
$V_F$ at $I_F = 20$ A	0.60 V
$T_J$ max.	150 °C

## MECHANICAL DATA

**Case:** TO-247AD (TO-3P)

Epoxy meets UL 94V-0 flammability rating

**Terminals:** Matte tin plated leads, solderable per J-STD-002 and JESD22-B102

E3 suffix for commercial grade, meets JESD 201 class 1A whisker test

**Polarity:** As marked

**Mounting Torque:** 10 in-lbs maximum

## MAXIMUM RATINGS ( $T_A = 25$ °C unless otherwise noted)

PARAMETER	SYMBOL	V40100P	UNIT
Maximum repetitive peak reverse voltage	$V_{RRM}$	100	V
Maximum average forward rectified current (Fig. 1)	$I_{F(AV)}$	40 20	A
Peak forward surge current 8.3 ms single half sine-wave superimposed on rated load	$I_{FSM}$	300	A
Peak repetitive reverse current per diode at $t_p = 2$ $\mu$ s, 1 kHz	$I_{RRM}$	1.0	A
Voltage rate of change (rated $V_R$ )	dV/dt	10 000	V
Operating junction and storage temperature range	$T_J, T_{STG}$	- 40 to + 150	°C



ELECTRICAL CHARACTERISTICS (T <sub>A</sub> = 25 °C unless otherwise noted)						
PARAMETER	TEST CONDITIONS		SYMBOL	TYP.	MAX.	UNIT
Breakdown voltage	I <sub>R</sub> = 1.0 mA	T <sub>J</sub> = 25 °C	V <sub>BR</sub>	100	-	V
Instantaneous forward voltage per diode <sup>(1)</sup>	I <sub>F</sub> = 5 A I <sub>F</sub> = 10 A I <sub>F</sub> = 20 A	T <sub>J</sub> = 25 °C	V <sub>F</sub>	0.461 0.525 0.652	- - 0.73	V
	I <sub>F</sub> = 5 A I <sub>F</sub> = 10 A I <sub>F</sub> = 20 A	T <sub>J</sub> = 125 °C		0.372 0.443 0.595	- - 0.67	
Reverse current per diode <sup>(2)</sup>	V <sub>R</sub> = 70 V	T <sub>J</sub> = 25 °C T <sub>J</sub> = 125 °C	I <sub>R</sub>	11.5 8.0	500 15	μA mA
	V <sub>R</sub> = 100 V	T <sub>J</sub> = 25 °C T <sub>J</sub> = 125 °C		60.6 20.2	1000 45	μA mA

**Notes:**

- (1) Pulse test: 300 μs pulse width, 1 % duty cycle
- (2) Pulse test: Pulse width ≤ 40 ms

THERMAL CHARACTERISTICS (T <sub>A</sub> = 25 °C unless otherwise noted)			
PARAMETER	SYMBOL	V40100P	UNIT
Typical thermal resistance per diode	R <sub>θJC</sub>	1.5	°C/W

ORDERING INFORMATION (Example)				
PREFERRED P/N	UNIT WEIGHT (g)	PREFERRED PACKAGE CODE	BASE QUANTITY	DELIVERY MODE
V40100P-E3/45	6.056	45	30/tube	Tube

**RATINGS AND CHARACTERISTICS CURVES**

(T<sub>A</sub> = 25 °C unless otherwise noted)

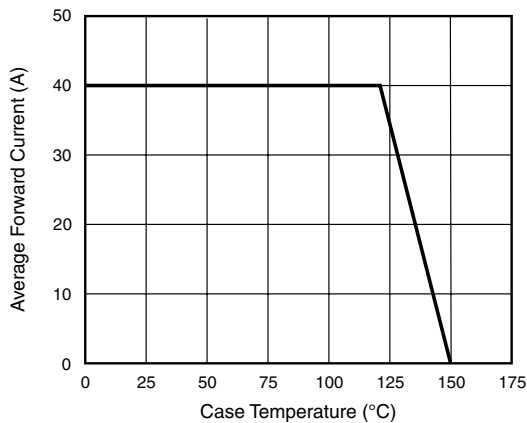


Figure 1. Forward Current Derating Curve

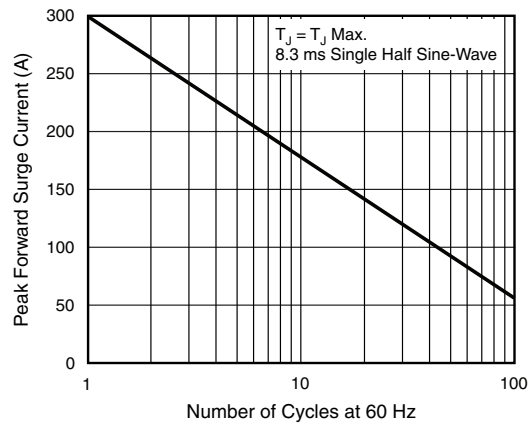


Figure 2. Maximum Non-Repetitive Peak Forward Surge Current Per Diode

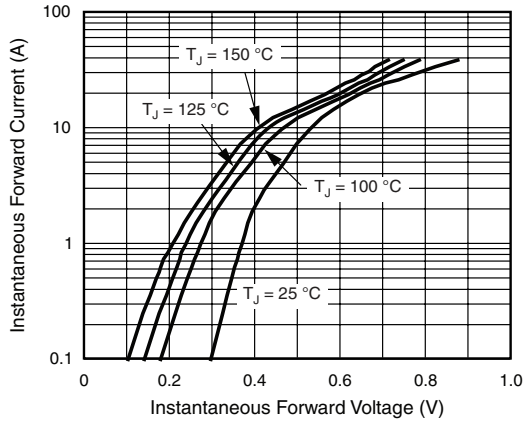


Figure 3. Typical Instantaneous Forward Characteristics Per Diode

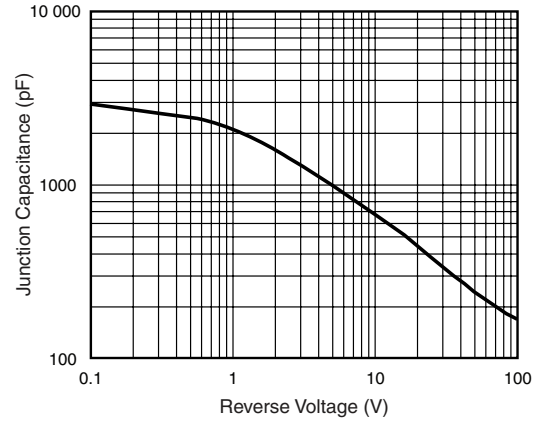


Figure 5. Typical Junction Capacitance Per Diode

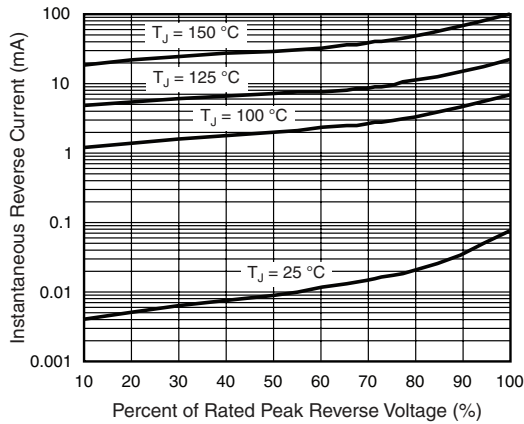


Figure 4. Typical Reverse Characteristics Per Diode

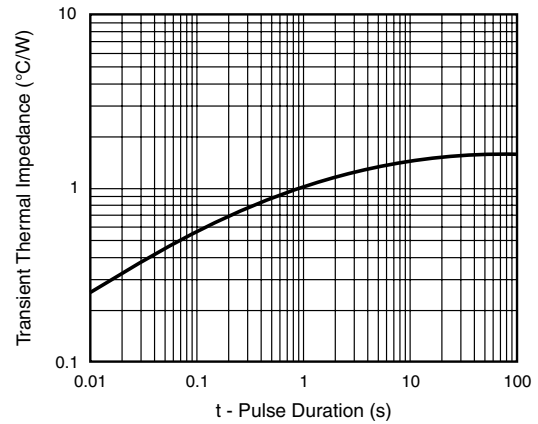
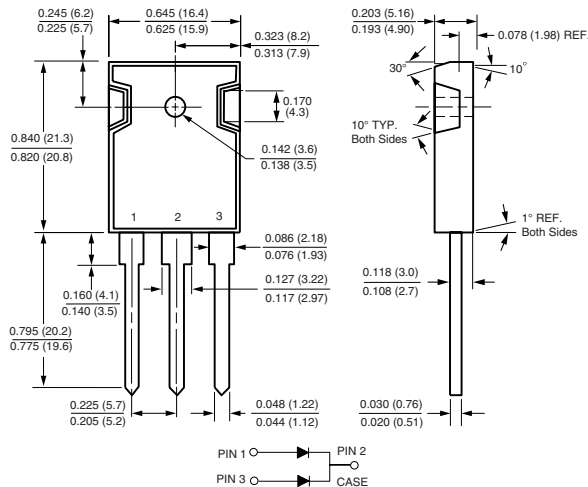


Figure 6. Typical Transient Thermal Impedance Per Diode

**PACKAGE OUTLINE DIMENSIONS** in inches (millimeters)

**TO-247AD (TO-3P)**





## Disclaimer

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