

PHOTOCOUPLER
PS9303L, PS9303L2

HIGH CMR, 1 Mbps TOTEM POLE OUTPUT TYPE
6-PIN SDIP PHOTOCOUPLER

-NEPOC Series-

DESCRIPTION

The PS9303L and PS9303L2 are optical coupled high-speed, totem pole output isolators containing a GaAlAs LED on the input side and a photodiode and a signal processing circuit on the output side on one chip.

The PS9303L and PS9303L2 are specified high CMR and pulse width distortion with operating temperature. It is suitable for IPM drive.

The PS9303L is lead bending type (Gull-wing) for surface mounting.

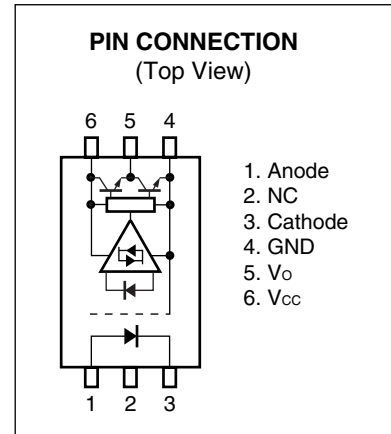
The PS9303L2 is lead bending type for long creepage distance (Gull-wing) for surface mount.

FEATURES

- High common mode transient immunity ($CM_H, CM_L = \pm 15 \text{ kV}/\mu\text{s}$ MIN.)
- Half size of 8-pin DIP
- Pulse width distortion ($|t_{PLH} - t_{PHL}| = 550 \text{ ns}$ MAX.)
- High-speed (1 Mbps)
- High isolation voltage ($BV = 5\,000 \text{ Vr.m.s.}$)
- Totem pole output (Active High Output Type)
- Pb-Free product

APPLICATIONS

- IPM Driver
- General purpose inverter



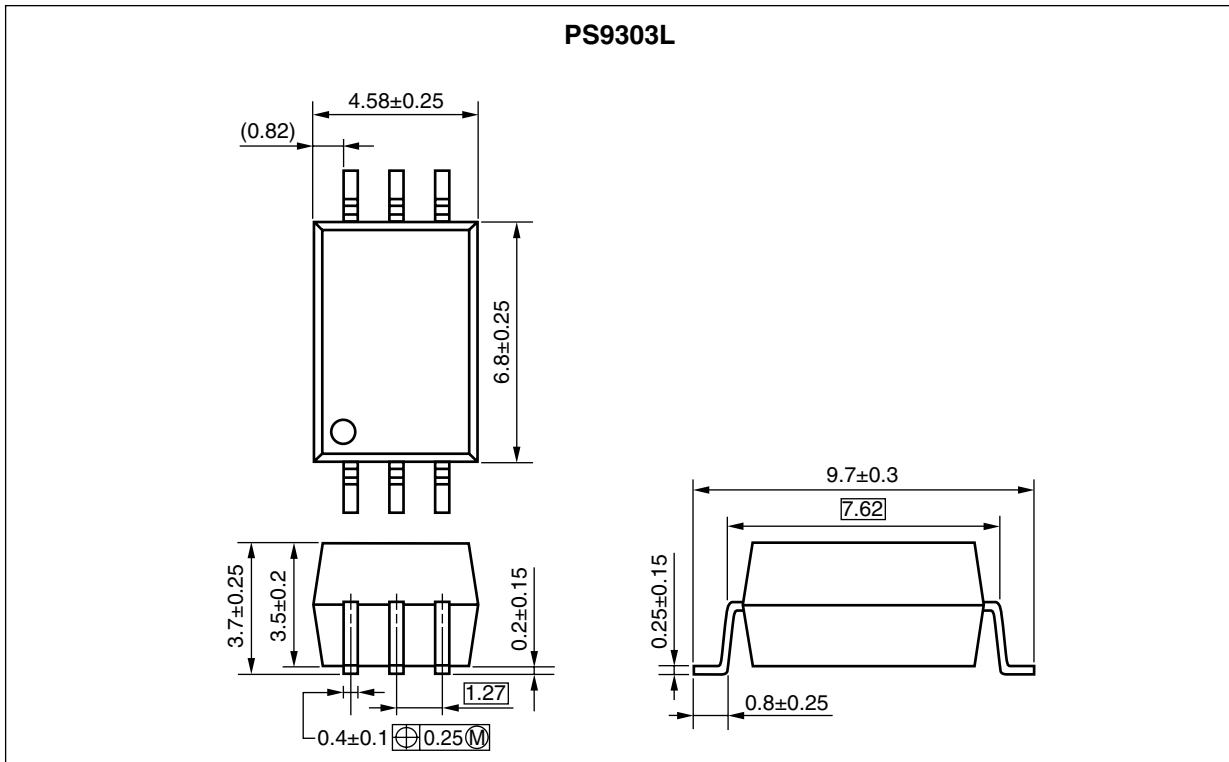
TRUTH TABLE

LED	Output
ON	H
OFF	L

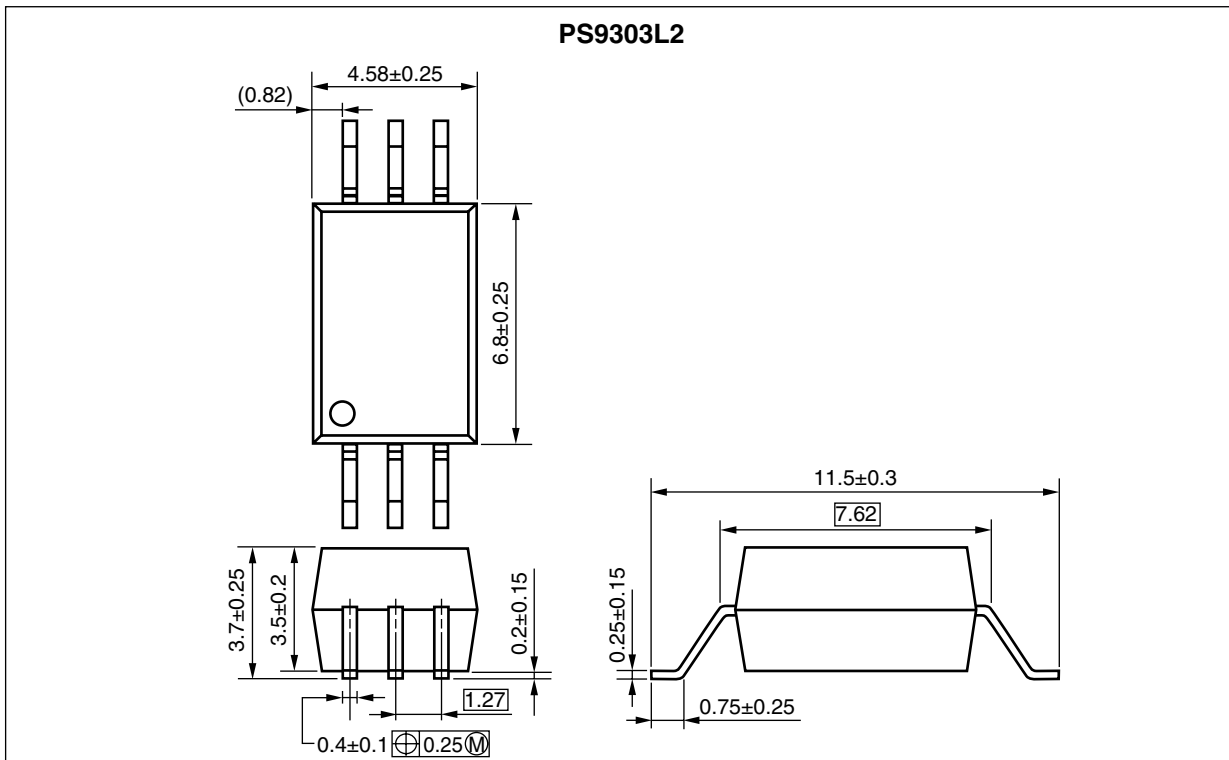
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PACKAGE DIMENSIONS (UNIT: mm)

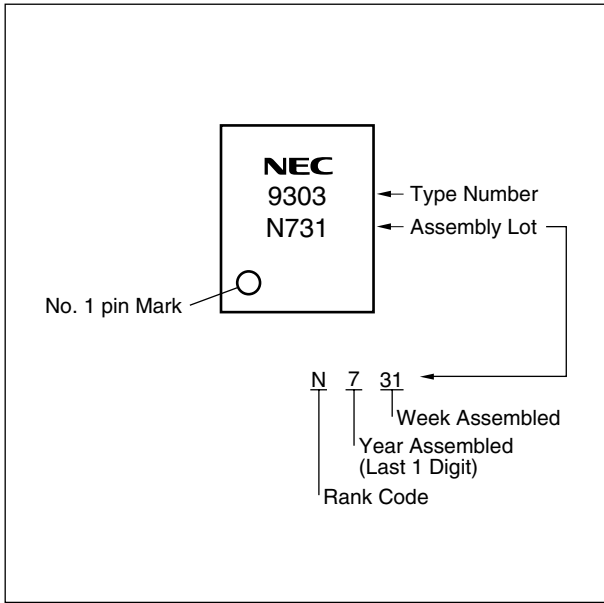
Lead Bending Type (Gull-wing) For Surface Mount



Lead Bending Type (Gull-wing) For Long Creepage Distance (Surface Mount)



MARKING EXAMPLE



PHOTOCOUPLER CONSTRUCTION

Parameter	PS9303L	PS9303L2
Air Distance (MIN.)	7 mm	8 mm
Outer Creepage Distance (MIN.)	7 mm	8 mm
Isolation Distance (MIN.)	0.4 mm	0.4 mm

ABSOLUTE MAXIMUM RATINGS (T_A = 25°C, unless otherwise specified)

Parameter		Symbol	Ratings	Unit
Diode	Forward Current ¹	I _F	20	mA
	Reverse Voltage	V _R	5	V
Detector	Supply Voltage	V _{CC}	-0.5 to +25	V
	Output Voltage	V _O	-0.5 to +25	V
	Output Current	I _O	25	mA
	Power Dissipation ²	P _C	100	mW
Isolation Voltage ³		BV	5 000	Vr.m.s.
Operating Ambient Temperature		T _A	-40 to +100	°C
Storage Temperature		T _{stg}	-55 to +125	°C

*1 Reduced to 0.33 mA/°C at T_A = 70°C or more.

*2 Reduced to 1.9 mW/°C at T_A = 70°C or more.

*3 AC voltage for 1 minute at T_A = 25°C, RH = 60% between input and output.
Pins 1-3 shorted together, 4-6 shorted together.

RECOMMENDED OPERATING CONDITIONS

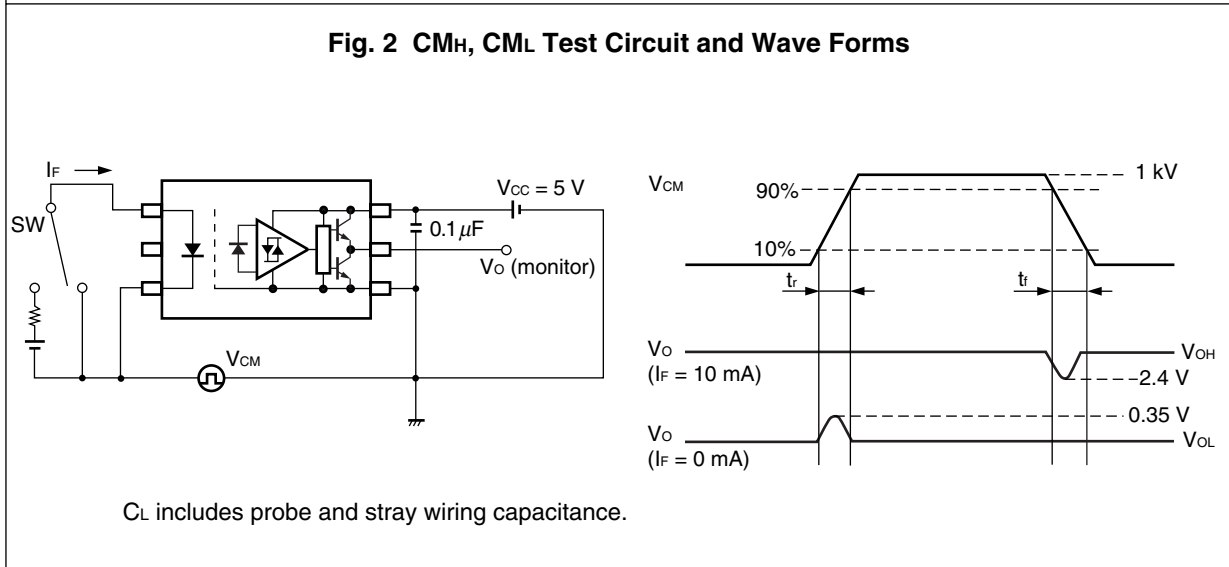
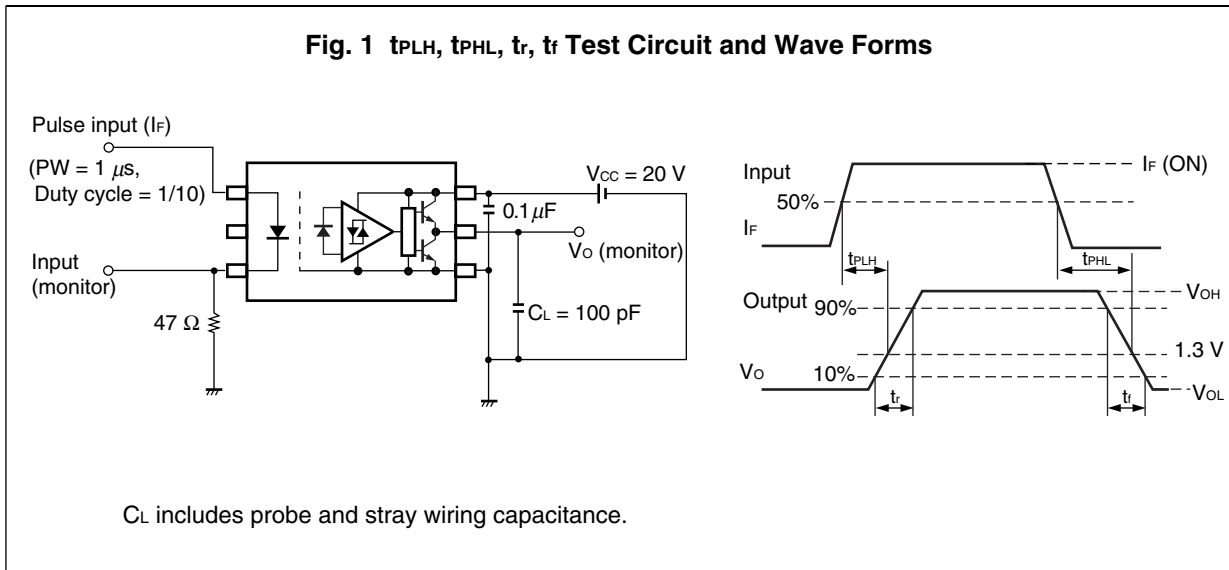
Parameter	Symbol	MIN.	TYP.	MAX.	Unit
Input Current	I _F	6		10	mA
Input Voltage	V _F	0		0.8	V
Supply Voltage	V _{CC}	4.5	15	20	V
Output Voltage	V _O	0		20	V

ELECTRICAL CHARACTERISTICS (T_A = -40 to +100°C, V_{CC} = 15 V, unless otherwise specified)

Parameter		Symbol	Conditions	MIN.	TYP.*1	MAX.	Unit
Diode	Forward Voltage	V _F	I _F = 10 mA, T _A = 25°C	1.3	1.55	1.8	V
	Reverse Current	I _R	V _R = 3 V, T _A = 25°C			200	μA
	Terminal Capacitance	C _t	V = 0 V, f = 1 MHz, T _A = 25°C		30		pF
Detector	High Level Output Voltage	V _{OH}	V _{CC} = 5 V, I _O = -3.5 mA, I _F = 10 mA	2.4	3.5		V
			V _{CC} = 20 V, I _O = -3.5 mA, I _F = 10 mA	17.4	18.1		
	Low Level Output Voltage	V _{OL}	I _O = 3.5 mA, V _F = 0.8 V		0.1	0.35	V
	High Level Supply Current	I _{CC} H	V _{CC} = 5 V, I _F = 10 mA		2	4	mA
			V _{CC} = 20 V, I _F = 10 mA		2	4	
	Low Level Supply Current	I _{CC} L	V _{CC} = 5 V, V _F = 0 V		3	5	mA
			V _{CC} = 20 V, V _F = 0 V		3	5	
	High Level Output Short Circuit Current	I _{OSH}	V _{CC} = 20 V, V _O = GND, I _F = 10 mA	-7	-40		mA
Low Level Output Short Circuit Current	I _{OSL}	V _{CC} = V _O = 20 V, V _F = 0 V	7	40		mA	
Coupled	Threshold Input Current	I _{FL} H	V _{CC} = 5 V, V _O > 2.4 V, I _O = -3.5 mA		2	5	mA
	Isolation Resistance	R _{I-O}	V _{I-O} = 500 V _{DC} , RH = 60%, T _A = 25°C	10 ¹²			Ω
	Isolation Capacitance	C _{I-O}	V = 0 V, f = 1 MHz, T _A = 25°C		0.6		pF
	Propagation Delay Time (H → L)	t _{PH} L	V _{CC} = 20 V, C _L = 100 pF, I _F = 10 → 0 mA, V _{THL} = 1.3 V		250	600	ns
	Propagation Delay Time (L → H)	t _{PL} H	V _{CC} = 20 V, C _L = 100 pF, I _F = 0 → 10 mA, V _{TLH} = 1.3 V		350	600	ns
	Pulse Width Distortion (PWD)	t _{PLH} -t _{PHL}	V _{CC} = 20 V, C _L = 100 pF, I _F = 10 ↔ 0 mA			550	ns
	Rise Time (10-90%)	t _r	V _{CC} = 20 V, C _L = 100 pF, I _F = 0 → 10 mA		175		ns
	Fall Time (90-10%)	t _f	V _{CC} = 20 V, C _L = 100 pF, I _F = 10 → 0 mA		95		ns
	Common Mode Transient Immunity at High Level Output	CM _H	V _{CC} = 5 V, T _A = 25°C, I _F = 10 mA, V _{CM} = 1.0 kV, V _{O(MIN.)} = 2.4 V	15			kV/μs
	Common Mode Transient Immunity at Low Level Output	CM _L	V _{CC} = 5 V, T _A = 25°C, I _F = 0 mA, V _{CM} = 1.0 kV, V _{O(MAX.)} = 0.35 V	15			kV/μs

*1 Typical values at T_A = 25°C

TEST CIRCUIT



USAGE CAUTIONS

1. This product is weak for static electricity by designed with high-speed integrated circuit so protect against static electricity when handling.
2. By-pass capacitor of 0.1 μ F is used between Vcc and GND near device. Also, ensure that the distance between the leads of the photocoupler and capacitor is no more than 10 mm.
3. Avoid storage at a high temperature and high humidity.

NOTES ON HANDLING**Cautions regarding noise**

Be aware that when voltage is applied suddenly between the photocoupler's input and output or between collector-emitters at startup, the output transistor may enter the on state, even if the voltage is within the absolute maximum ratings.

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