HRF-AT4611

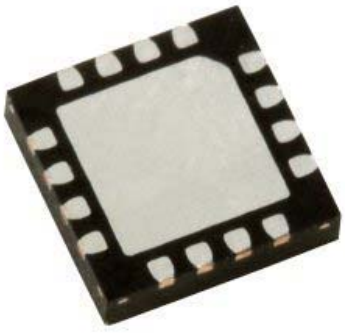
Honeywell

Preliminary

31.5 dB, DC-4GHz, 6 Bit Serial Digital Attenuator

Features

- Very Low DC Power Consumption
- Attenuation In Steps From 0.5 dB To 31.5 dB
- Single Or Dual Power Supply Voltages
- Serial Data Interface
- 50 Ohm Compatible Impedance
- Space Saving LPCC™ Surface Mount Packaging



HRF-AT4611 in LPCC™ Package

Product Description

The Honeywell HRF-AT4611 is a 6-bit digital attenuator that is ideal for use in broadband communication system applications that require accuracy, speed and low power consumption. The HRF-AT4611 is manufactured with Honeywell's patented Silicon On Insulator (SOI) CMOS manufacturing technology, which provides the performance of GaAs with the economy and integration capabilities of conventional CMOS

RF Electrical Specifications @ + 25°C

Results @ Vdd = 5.0 +/- 10%, Vss = 0 unless otherwise stated, Z0 = 50 Ohms

Parameter	Test Condition	Frequency	Minimum	Typical	Maximum	Units
Insertion Loss		DC – 1.0 GHz 2.5 GHz 3.0 GHz 4.0 GHz		2.5 3.0 3.6 5.5	2.9 3.5 3.9 5.8	dB dB dB dB
1dB Compression	VSS = 0V, Input Power	DC – 2.0 GHz		24		dBm
1dB Compression	VSS = - VDD, Input Power	DC – 2.0 GHz		29		dBm
Input IP3	VSS = 0V Two-tone inputs Up To +5 dB @ 0 dB Attenuation	DC – 2.0 GHz		38		dBm
Input IP3	V_{ss} = - V_{DD} Two-tone inputs Up To + 5 dBm @ 0 dBm Attenuation	DC – 2.0 GHz		>38		dBm
Return Loss*	Any Bit or Combination	DC - 3.0 GHz		11		dB
Attenuation Accuracy	All attenuation states All attenuation states All attenuation states All attenuation states	DC – 1.0 GHz 2.5 GHz 3.0 GHz 4.0 GHz	+/-(0.3 + 3% of programmed IL) +/-(0.3 + 3% of programmed IL) +/-(0.4 + 5% of programmed IL) +/-(0.4 + 5.5% of programmed IL)			dB dB dB dB
Trise, Tfall* Ton, Toff (Tpd) Transients	10% To 90% 50% Cntl To 90%/10%RF In-Band			10 15 30		nS nS mV
T clock Period (Tprd)*	T high / T low = ½ minimum clock period		50			nS
T data set up (Tsup)*	Set up to rising edge of clock		5			nS
T data hold (Thld)*	Data hold after rising edge of clock		2			nS
T latch set up (Tlsup*)	Data set up to falling edge of OE		5			nS

0.01uF Decoupling Capacitors Required On Power Supply Rails.

*By design

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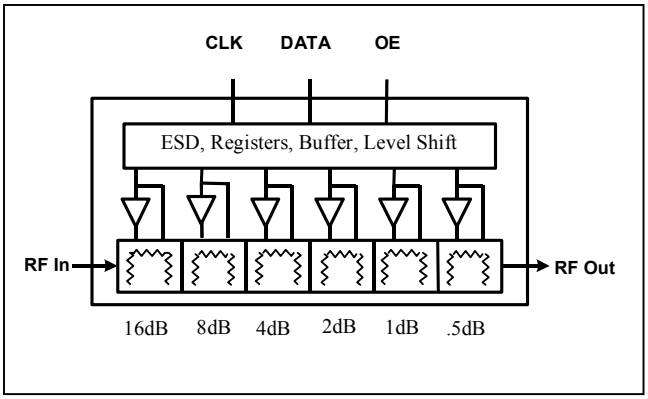
Functional Schematic



DC Electrical Specifications @ + 25°C

Parameter	Minimum	Typical	Maximum	Units
V_{DD}	3.3[1]	5.0		V
V_{SS}			-5.0	V
I_{DD} Power Supply Current			35	uA
CMOS Logic level (0)	0		0.8	V
CMOS Logic level (1)	V_{DD} – 0.8		V_{DD}	V
Input Leakage Current			10	uA

Note 1, the performance curves are for Vdd = +5.0 +/- 10%

Absolute Maximum Ratings[2]

Parameter	*Absolute Maximum*	*Units*
Input Power	+ 35	dBm
V_{DD}	+6.0	V
V_{SS}	-5.5	V
ESD Voltage	400	V
Operating Temperature	-40 To +85	Degrees C
Storage Temperature	-65 To +125	Degrees C
Digital Inputs	V_{DD}+0.6 max to -0.6 min	V

(Note 2) Operation of this Device beyond any of these parameters may cause permanent damage.

Latch-Up: Unlike conventional CMOS digital attenuators, Honeywell's HRF-AT4611 is immune to latch-up.

ESD Protection: Although the HRF-AT4611 contains ESD protection circuitry on all digital inputs, conventional precautions should be taken to ensure that the Absolute Maximum Ratings are not exceeded.

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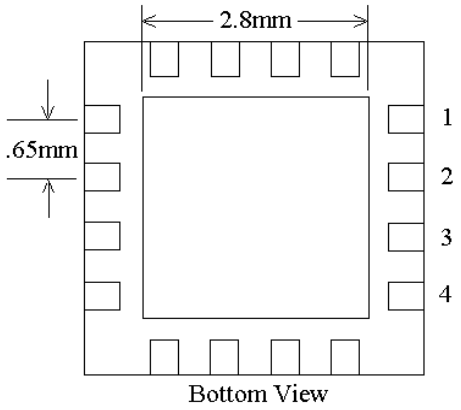
Package Outline Drawing



This package conforms to the LPCC™ 4 X 4 mm 16 lead body dimensions.
See ASAT LPCC Marketing Outline Dwg. # DGMJ00004 Latest Rev. at
http://www.asat.com for additional dimensional information.

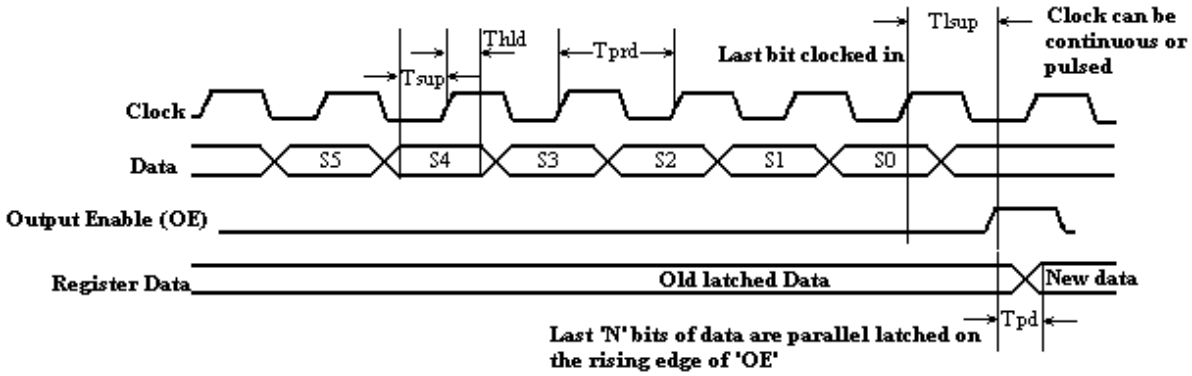
Pin Configuration

Pin	Function	Pin	Function
1	VDD	9	GROUND
2	GROUND	10	RF OUTPUT
3	RF INPUT	11	GROUND
4	GROUND	12	VSS
5	GROUND	13	DIGITAL GROUND
6	GROUND	14	OE
7	GROUND	15	CLK
8	GROUND	16	DATA

Note: Bottom ground plate must be grounded for proper RF performance.

Serial Data Load

Serial data is shifted into the register on the rising edge of clock, MSB first. The OE rising edge must occur prior to any additional rising clock edges. See the Electrical Spec Table for AC parameters.



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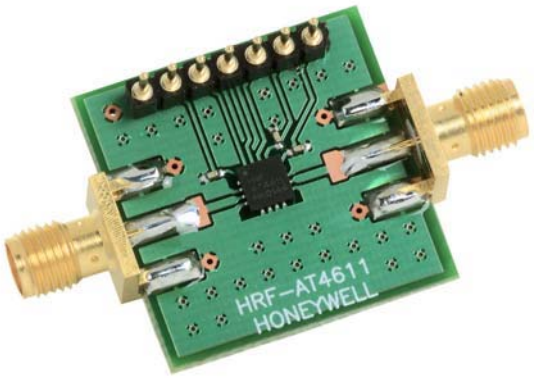
Truth Table

S5	S4	S3	S2	S1	S0	Output
0	0	0	0	0	0	Reference Input
0	0	0	0	0	1	0.5 dB
0	0	0	0	1	0	1 dB
0	0	0	1	0	0	2 dB
0	0	1	0	0	0	4 dB
0	1	0	0	0	0	8 dB
1	0	0	0	0	0	16 dB
1	1	1	1	1	1	31.5 dB

Operation: Data on serial input D is clocked into internal registers on the low to high transition of the Clock signal (CK). The register output is enabled when Output Enable (OE) is in the low state. "0" = CMOS Low, "1" = CMOS High.

Evaluation Circuit Board



HRF-AT4611 Evaluation Board

Honeywell's evaluation board provides an easy to use method of evaluating the RF performance of our attenuator. Simply connect power, DC and RF signals to be measuring attenuator performance in less than 10 minutes.

Evaluation Circuit Board Layout Design Details

Item	Description
PCB	Impedance Matched Multi-Layer FR4
Attenuator	HRF-AT4611 Digital Attenuator
Chip Capacitor	Panasonic Model ECU-E1C103KBQ Capacitor, .01uf 0402 10% 16V
RF Connector	Johnson Connectors Model 142-0701-801 SMA RF Coaxial Connector
DC Pin	Mil-Max Model 800-10-064-10-001 Header Pins

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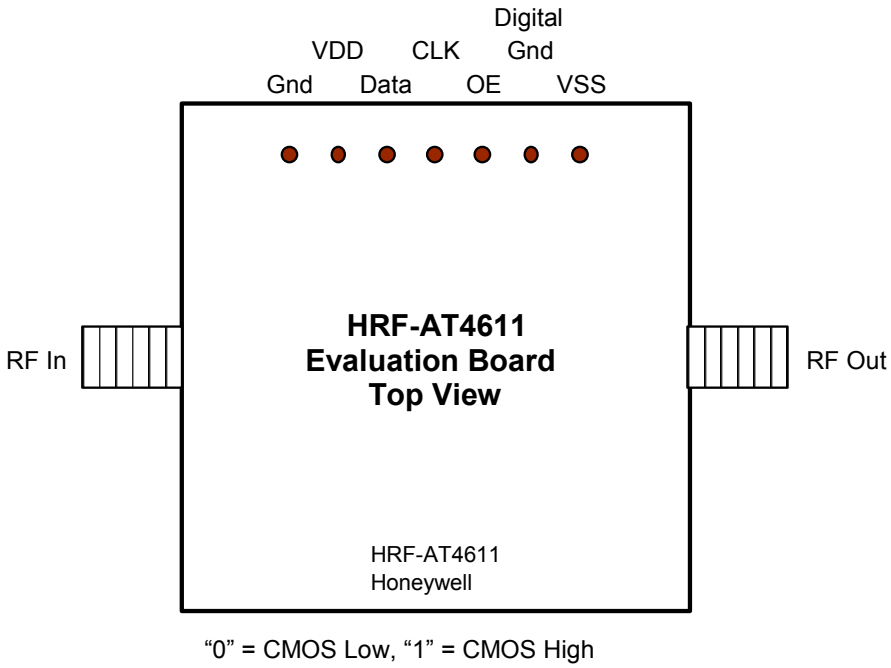
Evaluation Circuit Board Connections

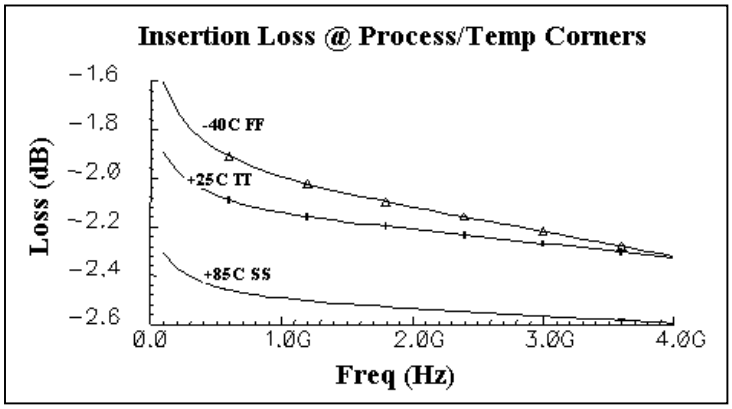


"0" = CMOS Low, "1" = CMOS High

Refer to HRF-AT4611 Product Spec. for Serial Data Load Information
(http://www.ssec.honeywell.com/microwave/products/attenuators.html)

Performance Curves

Insertion Loss



This Insertion Loss curve represents the Min/Max conditions for the "0" pass state versus all processing and temperature conditions. The Min case is at –40C with the "Fast" processing conditions. The Max case is at +85C with the "Slow" processing conditions. All other combinations fall within that band. The typical 25C case is labeled +25C TT.

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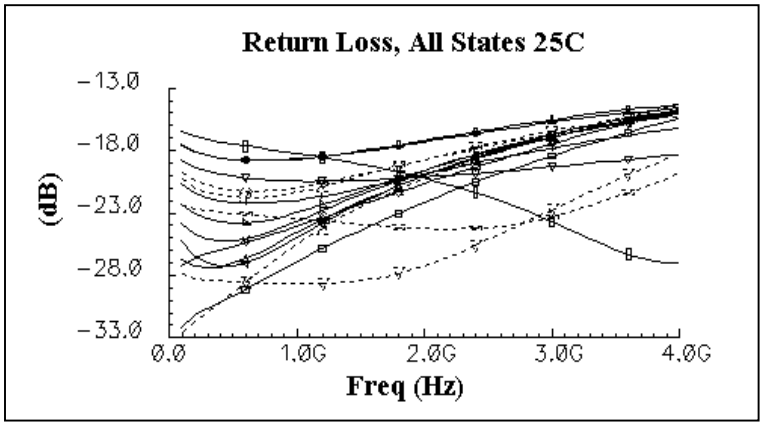
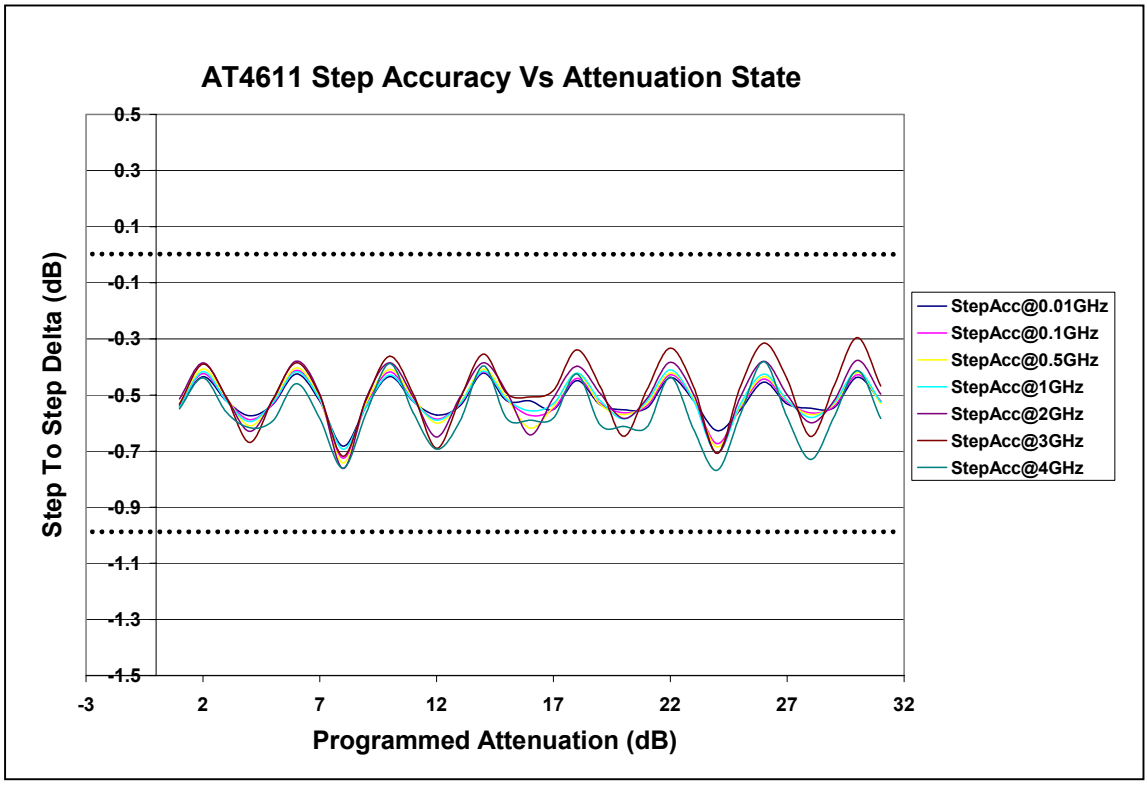
Return Loss



This Return Loss set of curves represents the combination of all Return Loss cases for all attenuation settings. All cases are better than –14dB Return Loss at 4GHz.

Step Accuracy



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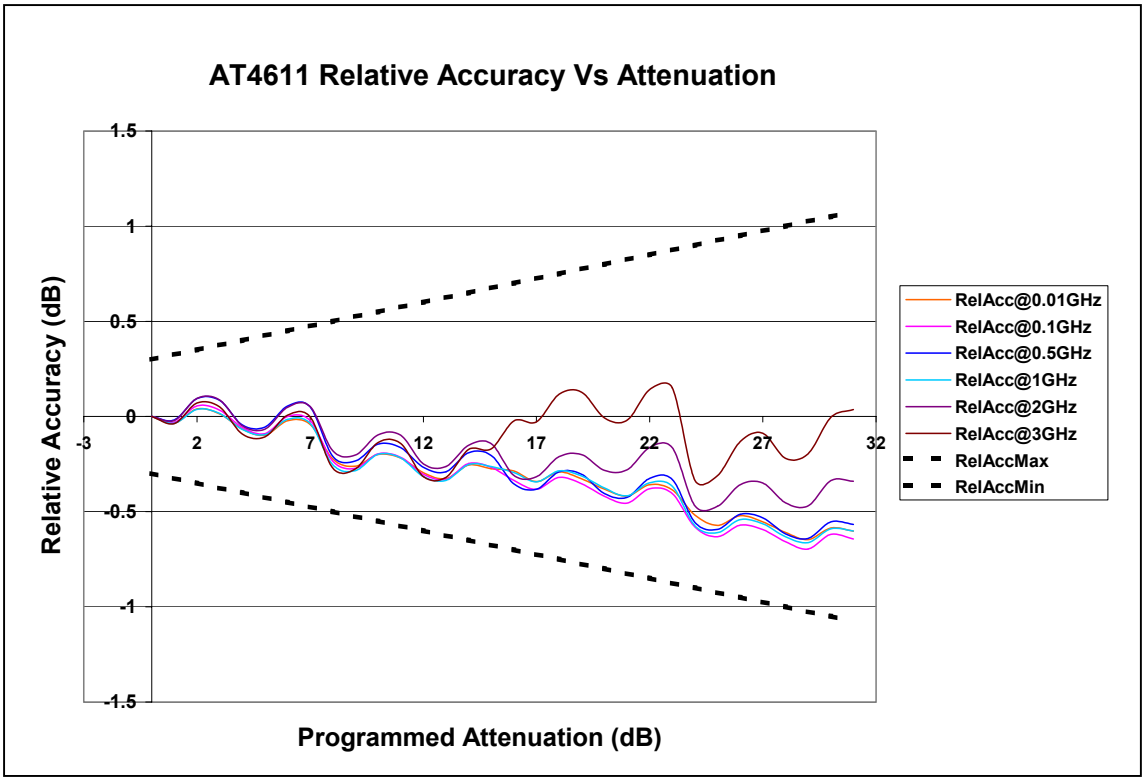
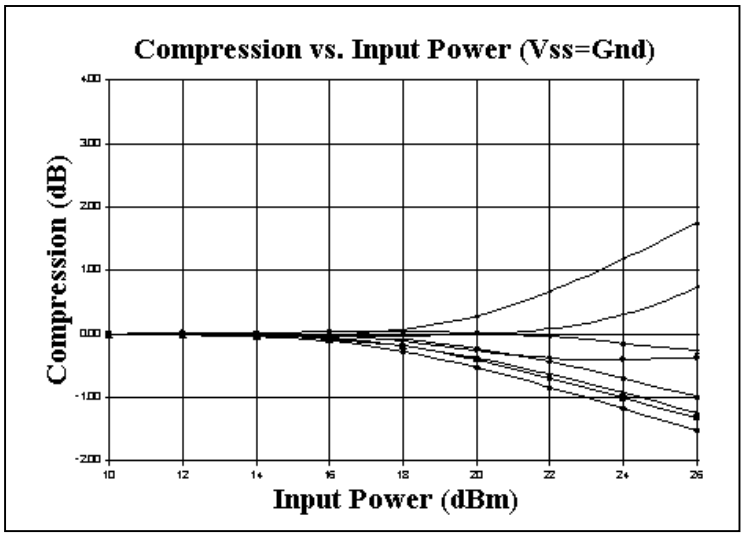
Relative Accuracy



Compression



The P1dB curve shows all states with a P1dB compression at approximately 23dB input power. The conditions for this curve are Vdd = +5.0v and Vss = 0v. For higher P1dB compression values, supply Vss with a negative voltage as shown in the next curve.

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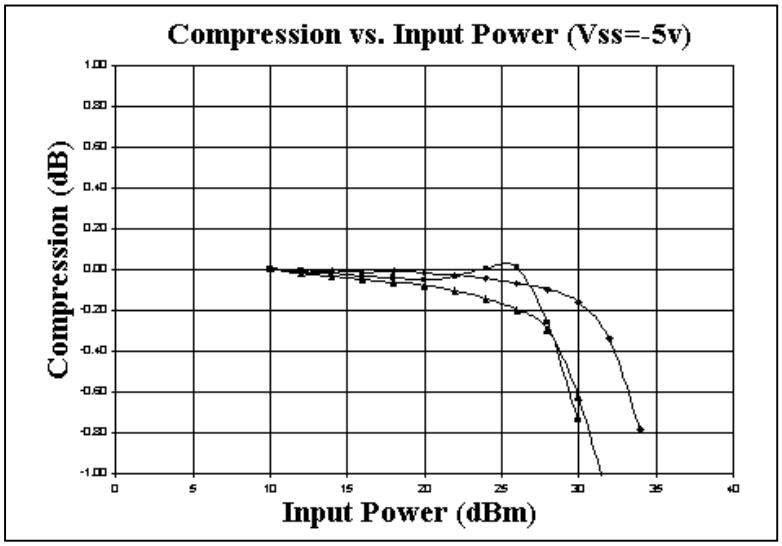

The P1dB curve shows all states with a P1dB compression at approximately 31dB input power. The conditions for this curve are Vdd = +5.0v and Vss = -5.0v.

Ordering Information

Ordering Number	Delivery Method	Units Per Shipment
HRF-AT4611-B	In Chip Tubes	Customer Specific, Usually Minimum Of 50 Per Chip Tube
HRF-AT4611-TR	On Tape And Reel[3]	Customer Specific
HRF-AT4611-E	On Individual Engineering Evaluation Board	One Board Per Box

(Note 3) Call Honeywell for details

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