

<b>SANYO</b>	No. 4098	<b>2SC3897</b>
	NPN Triple Diffused Planar Silicon Transistor Very High-Definition CRT Display Horizontal Deflection Output Applications	

**Features**

- High-speed ( $t_f$  typ = 100ns).
- High reliability (Adoption of HVP process).
- High breakdown voltage ( $V_{CBO} = 1500V$ ).
- Adoption of MBIT process.

**Absolute Maximum Ratings at  $T_a = 25^\circ C$**

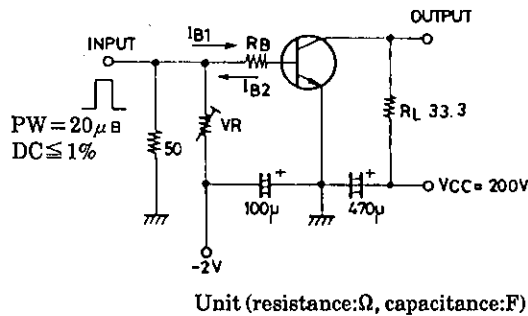
			unit
Collector-to-Base Voltage	$V_{CBO}$	1500	V
Collector-to-Emitter Voltage	$V_{CEO}$	800	V
Emitter-to-Base Voltage	$V_{EBO}$	6	V
Collector Current	$I_C$	10	A
Peak Collector Current	$i_{cp}$	25	A
Collector Dissipation	$P_C$	3.0	W
		70	W
		150	$^\circ C$
Junction Temperature	$T_j$		
Storage Temperature	$T_{stg}$	-55 to +150	$^\circ C$

$T_c = 25^\circ C$

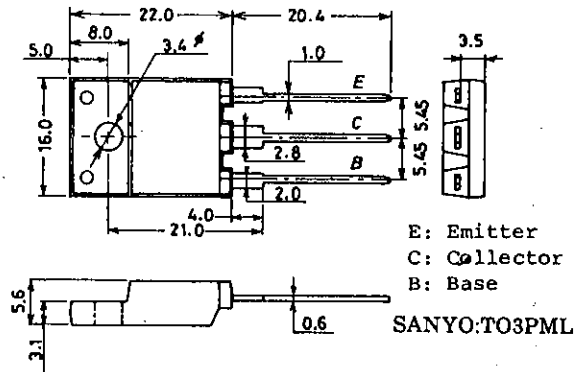
**Electrical Characteristics at  $T_a = 25^\circ C$**

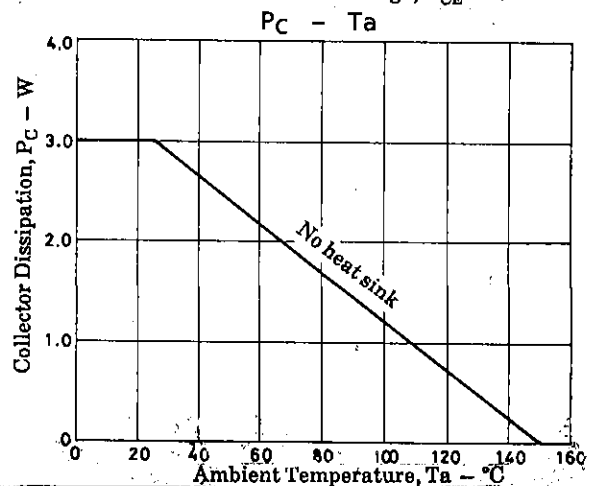
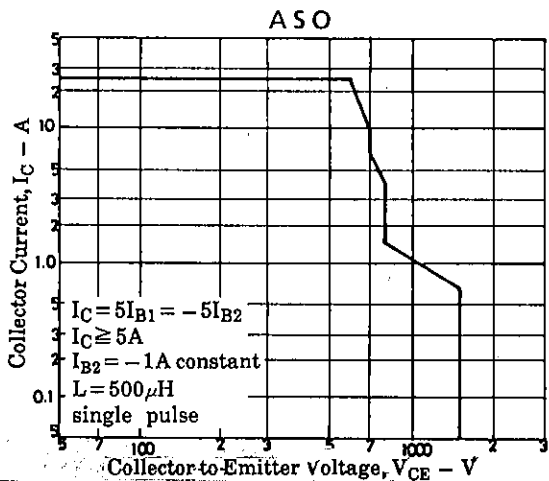
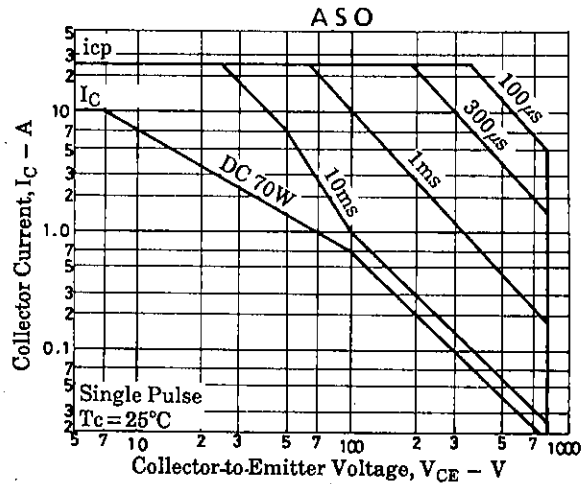
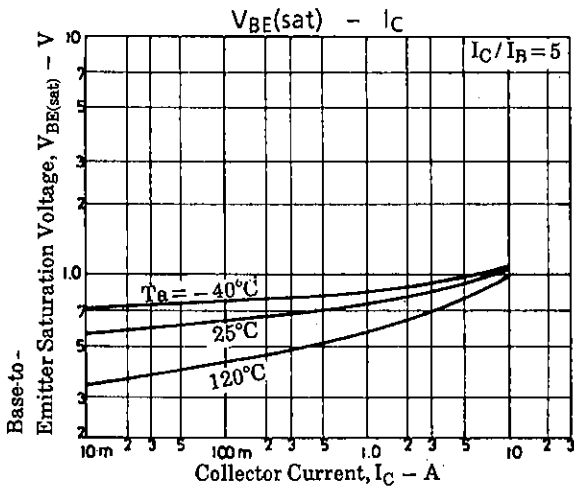
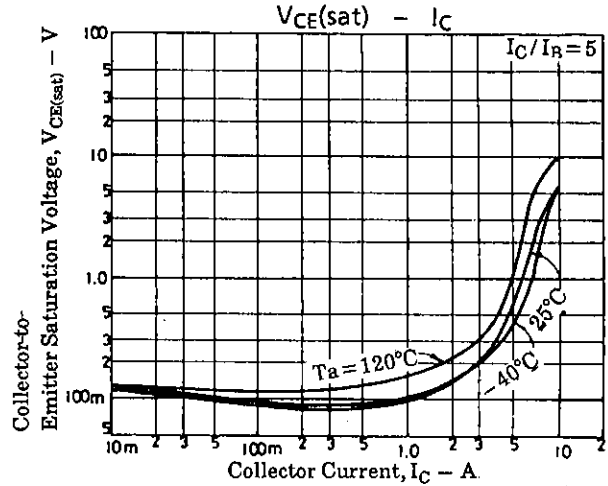
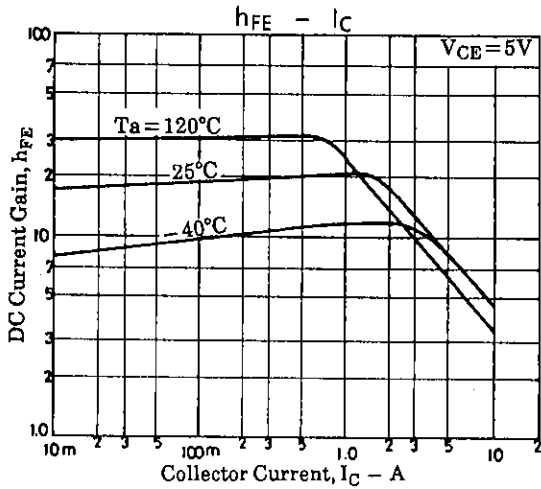
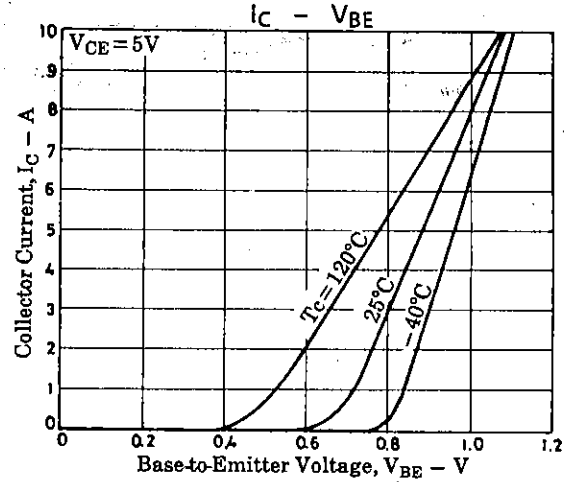
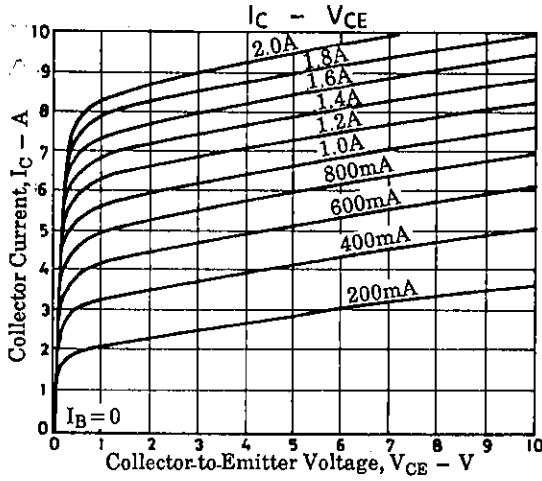
		min	typ	max	unit
Collector Cutoff Current	$I_{CBO}$			10	$\mu A$
	$I_{CES}$			1.0	mA
Collector Sustain Voltage	$V_{CEO(sus)}$	800			V
Emitter Cutoff Current	$I_{EBO}$			1.0	mA
C-E Saturation Voltage	$V_{CE(sat)}$			5	V
B-E Saturation Voltage	$V_{BE(sat)}$			1.5	V
DC Current Gain	$h_{FE(1)}$	8			
	$h_{FE(2)}$	4		8	
Storage Time	tstg			3.0	$\mu s$
Fall Time	$t_f$		0.1	0.2	$\mu s$

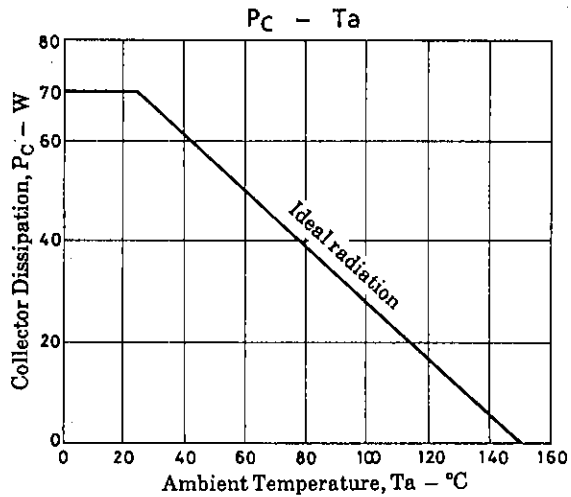
**Switching Time Test Circuit**



**Package Dimensions 2039A**  
(unit : mm)







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