

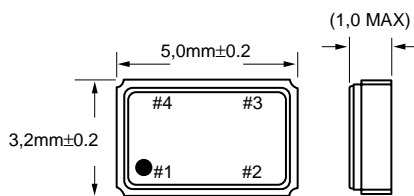
# VFS5



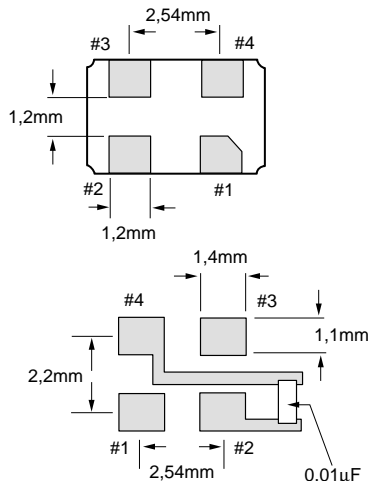
## HCMOS/TTL Compatible Miniature Ceramic Tristate Oscillator (3,2 x 5,0mm)

### FEATURES

- Wide Frequency Range
- Industrial Temperature Available
- Leadless Ceramic Package
- Industry Standard Footprint
- Ultra Miniature Package



Recommended Solder Pad Layout



All dimensions are typical unless otherwise specified.

### Creating a Part Number

VFS5 [ ] [ ] [ ] - [ ] - [ ] - FREQ.

FREQUENCY STABILITY		OPERATIONAL TEMP. RANGE	
Code	Specification	Code	Specification
B	±50 ppm		-10°C to +70°C (std.)
	±100 ppm (std.)	1	-40°C to +85°C

DUTY CYCLE		INPUT VOLTAGE	
Code	Specification	Code	Specification
	±10% (std.)		5.0 ±5% Volt (std.)

Example: VFS5BHH-1: Frequency Stability ±50ppm, Duty Cycle ±2.5%, Operational Temperature Range -40°C to +85°C.

	Parameter	Symb	Condition	Min	Typ	Max	Unit	Note
Absolute Max. Ratings	Input Break Down Voltage	Vcc		-0.5		7.0	V	
	Storage Temp.	Ts		-55		+125	°C	
Electrical	Frequency Range	F		1.8		50	MHz	
	Frequency Stability	ΔF/F	Overall conditions including: calibration, temp., aging 10 yrs, shock, vibration			±100	ppm	1
	Input Voltage	Vcc		4.75	5.00	5.25	V	
	Input Current	Icc	15pF load			75	mA	2
	Load	10TTL gates of 15pF						
	Duty Cycle		@50%Vcc	40		60	%	
	Rise/Fall Time	Tr/Tf			5	10	ns	
	Logic "1" Level	Voh	Max Load	0.9Vcc			V	
	Logic "0" Level	Vol	Max Load			0.1Vcc	V	
	Start-up Time	Ts			3	10	ms	
Environmental and Mechanical	Tristate Function	Input HIGH (>2.5V) or floating: ACTIVE Input LOW (<0.5V): INFINITE IMPEDANCE						3
	Enable/Disable Time					100	ns	
Electrical Connections	Operating Temperature Range	-10°C to +70°C (-40°C to +85°C available)						
	Mechanical Shock	Per MIL-STD-202, Method 213, Cond. E						
	Thermal Shock	Per MIL-STD-883, Method 1011, Cond. A						
	Vibration	Per MIL-STD-883, Method 2007, Cond. A						
	Soldering Conditions	260°C, for 10s, Max; 230°C, for 90s, Max.						
	Hermetic Seal	Leak rate less than 5 x 10 <sup>-8</sup> atm.cc/s of helium						
Pin Out	Pin #1–Tristate Control		Pin #2–Ground, Case		Pin #3–Output			Pin #4–Vcc

### Notes:

1. Standard frequency stability (±50ppm, others available).
2. Current load, frequency dependent.

All specifications are subject to change without notice.