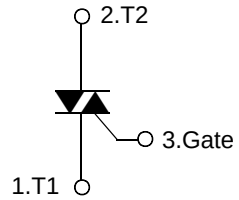
Sensitive Gate Triacs

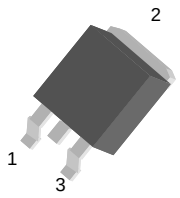
Symbol



Features

- Repetitive Peak Off-State Voltage : 600V
- R.M.S On-State Current ($I_{T(RMS)}$= 4 A)
- High Commutation dv/dt
- Sensitive Gate Triggering 4 Mode

D-PAK(TO-252)



General Description

This device is sensitive gate triac suitable for direct coupling to TTL, HTL, CMOS and application such as various logic functions, low power AC switching applications, such as fan speed, small light controllers and home appliance equipment.

Absolute Maximum Ratings (T_J = 25°C unless otherwise specified)

Symbol	Parameter	Condition	Ratings	Units
V_{DRM}	Repetitive Peak Off-State Voltage		600	V
$I_{T(RMS)}$	R.M.S On-State Current	T_C = 109 °C	4.0	A
I_{TSM}	Surge On-State Current	One Cycle, 50Hz/60Hz, Peak, Non-Repetitive	30/33	A
I^2t	I^2t		4.5	A^2S
P_{GM}	Peak Gate Power Dissipation		1.5	W
$P_{G(AV)}$	Average Gate Power Dissipation		0.1	W
I_{GM}	Peak Gate Current		1.0	A
V_{GM}	Peak Gate Voltage		7.0	V
T_J	Operating Junction Temperature		- 40 ~ 125	°C
T_{STG}	Storage Temperature		- 40 ~ 150	°C

Electrical Characteristics

Symbol	Items		Conditions	Ratings			Unit
				Min.	Typ.	Max.	
I_{DRM}	Repetitive Peak Off-State Current		$V_D = V_{DRM}$, Single Phase, Half Wave T_J = 125 °C	—	—	1.0	mA
V_{TM}	Peak On-State Voltage		I_T = 6 A, Inst. Measurement	—	—	1.6	V
I^+_{GT1}	Ⅰ	Gate Trigger Current	V_D = 6 V, R_L=10 Ω	—	—	5	mA
I^-_{GT1}	Ⅱ			—	—	5	
I^-_{GT3}	Ⅲ			—	—	5	
I^+_{GT3}	Ⅳ			—	8	12	
V^+_{GT1}	Ⅰ	Gate Trigger Voltage	V_D = 6 V, R_L=10 Ω	—	—	1.4	V
V^-_{GT1}	Ⅱ			—	—	1.4	
V^-_{GT3}	Ⅲ			—	—	1.4	
V^+_{GT3}	Ⅳ			—	1.6	2.0	
V_{GD}	Non-Trigger Gate Voltage		T_J = 125 °C, V_D = 1/2 V_{DRM}	0.2	—	—	V
(dv/dt)c	Critical Rate of Rise Off-State Voltage at Commutation		T_J = 125 °C, [di/dt]c = -2.0 A/ms, V_D=2/3 V_{DRM}	5	—	—	V/μs
I_H	Holding Current			—	—	10	mA
$R_{th(j-c)}$	Thermal Impedance		Junction to case	—	—	2.6	°C/W

Fig 1. Gate Characteristics

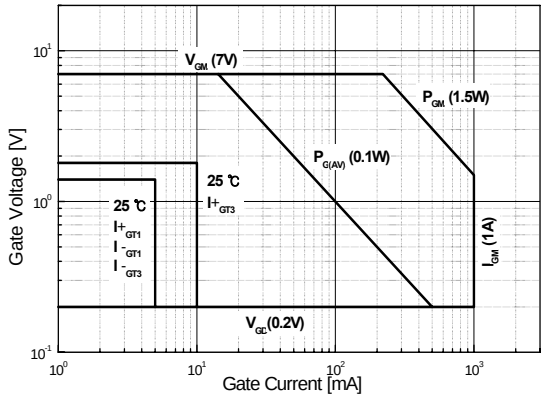


Fig 2. On-State Voltage

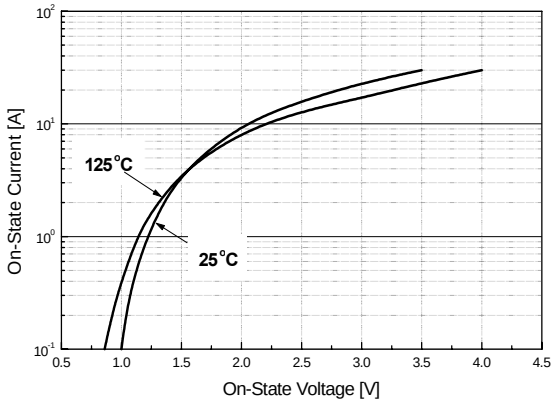


Fig 3. On State Current vs. Maximum Power Dissipation

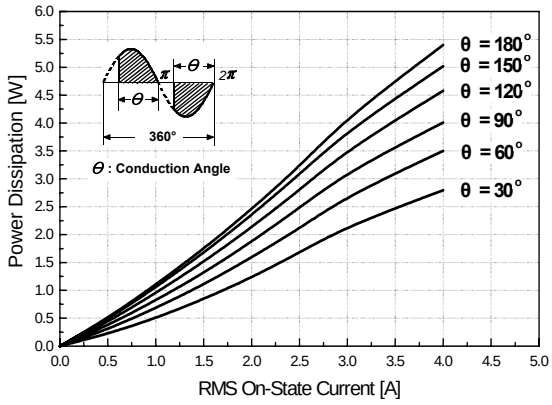


Fig 4. On State Current vs. Allowable Case Temperature

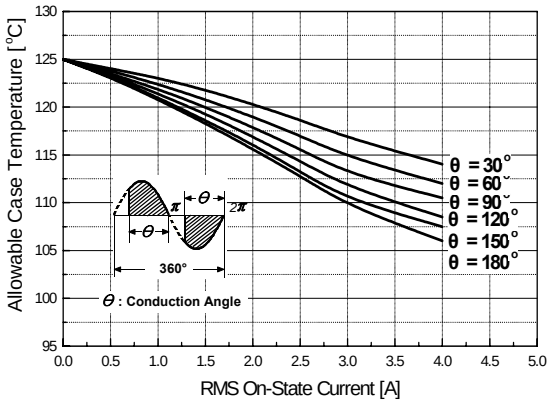


Fig 5. Surge On-State Current Rating (Non-Repetitive)

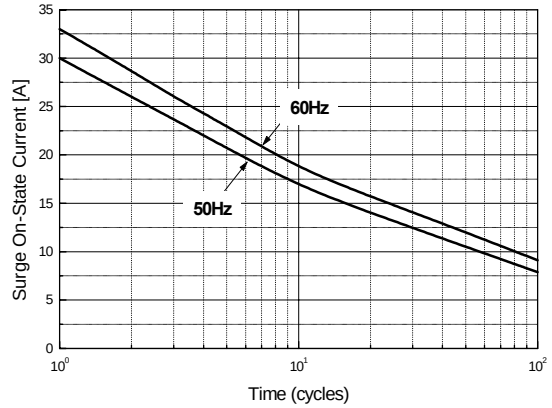


Fig 6. Gate Trigger Voltage vs. Junction Temperature

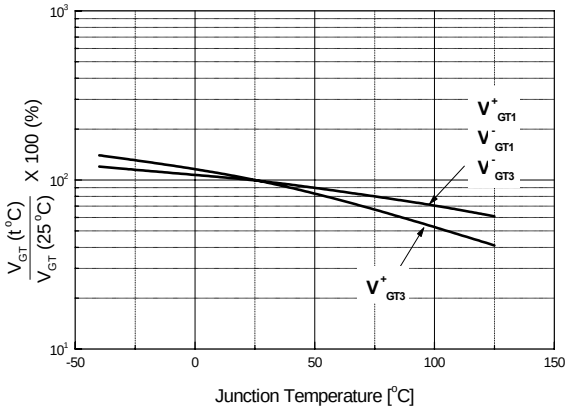


Fig 7. Gate Trigger Current vs. Junction Temperature

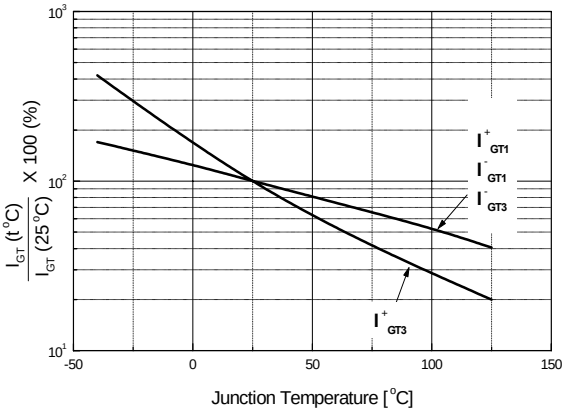


Fig 8. Transient Thermal Impedance

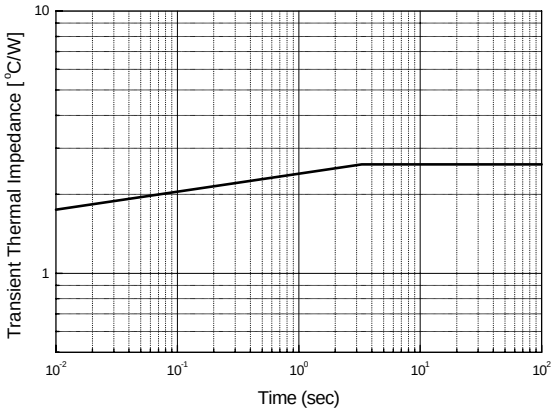
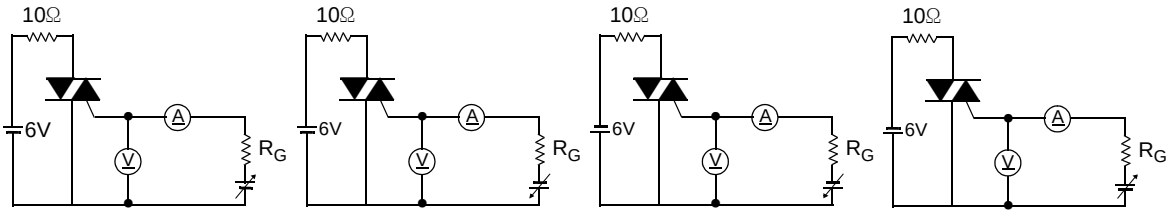


Fig 9. Gate Trigger Characteristics Test Circuit



TO-252(D-PAK) Package Dimension

Dim.	mm			Inch		
	Min.	Typ.	Max.	Min.	Typ.	Max.
A	6.48	6.604	6.73	0.255	0.26	0.265
B	5.0	5.08	5.21	0.197	0.2	0.205
C	7.42	7.8	8.18	0.292	0.307	0.322
D	2.184	2.286	2.388	0.086	0.09	0.094
E	0.762	0.813	0.864	0.03	0.032	0.034
F	1.016	1.067	1.118	0.04	0.042	0.044
G		2.286			0.09	
H		2.286			0.09	
I	0.534	0.61	0.686	0.021	0.024	0.027
J	1.016	1.067	1.118	0.04	0.042	0.044
K		0.508			0.02	
L		0.762			0.03	
ϕ		1.57			0.06